### **Boolean Functions**

Let  $\mathbb{B} = \{0, 1\}$ . 1 ... true, 0 ... false

Boolean Function of Arity *n* 

 $f: \mathbb{B}^n \to \mathbb{B}$ 

Boolean functions are often described using truth tables.



**Problem:** arity *n* gives truth table of size  $\theta(2^n)$ 

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#### Terminology

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 $t_1 \vee t_2$ 

0

1

1

1

Boolean expressions $t_1$ and $t_2$ are <b>equivalent</b> iff $f^{t_1} = f^{t_2}$ , i.e., they yield the same truth value for all truth assignments.			
Example: ¬(2	$(x_1 \wedge x_2)$ is equivalent to $ eg x_1 \vee  eg x_2$		
Tautology			
A boolean ex assignment.	pression <i>t</i> is a <b>tautology</b> if it yields true for <b>all</b> truth		
Satisfiability			
A boolean ex assignment	pression <i>t</i> is <b>satisfiable</b> if it yields true for <b>at least one</b> true		

#### **Boolean Expressions**

Let  $x_1, x_2, \ldots, x_n$  be boolean variables.

Abstract Syntax for Boolean Expressions (x ranges over variables)  $t, t_1, t_2 ::= 0 | 1 | x | \neg t | t_1 \land t_2 | t_1 \lor t_2 | t_1 \Rightarrow t_2 | t_1 \Rightarrow t_2$ 

Truth Assignment

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 $v: \{x_1, \dots, x_n\} \to \mathbb{B}$ Function v is often written as  $[v(x_1)/x_1, v(x_2)/x_2, \dots, v(x_n)/x_n]$ . Example: • boolean expression:  $\neg(x_1 \land x_2) \Rightarrow (\neg x_1 \lor x_4)$ 

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• truth assignment:  $[1/x_1, 0/x_2, 1/x_3, 1/x_4]$ 

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## Conjunctive Normal Form (CNF)

Definitions
Literal is a boolean variable or its negation.
Clause is a disjunction of literals.

• A boolean expression if **CNF** is a conjunction of clauses.

Example:  $(x_1 \lor \neg x_3) \land (\neg x_1 \lor x_2 \lor x_3) \land (x_2 \lor \neg x_3)$ 

Theorem

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For any boolean expression there is an equivalent one in CNF.

Cook's Theorem Satisfiability of boolean expressions (in CNF) is NP-complete.

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A boolean expression t defines a boolean function  $f^t : \mathbb{B}^n \to \mathbb{B}$  by the

 $t_1 \wedge t_2$ 

0

0

0

1

 $t_1$ 

0 0

0 1

1 0

 $1 \mid 1$ 

 $t_1 \mid t_2$ 

0 0

0 1

1 0

1

1

1

0

0

1

 $t_2 \parallel t_1 \Leftrightarrow t_2$ 

• boolean expressions and normal forms

binary decision diagrams (BDDs)

Evaluation of Boolean Expressions

 $t_1$   $t_2$ 

0 0

0 1

1 0

1

 $t_1 \mid t_2 \parallel t_1 \Rightarrow t_2$ 

1

1

1

0

1

algorithms on BDDs

following (structural) rules:

 $t \parallel \neg t$ 

0 0

0 1

1 0

1 | 1

0 1

1 0

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# **Combinatorial Circuits**



Are these two circuits equivalent? co-NP-hard problem!

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If-Then-Else Normal Form

#### Definition

A boolean expression is in **If-Then-Else normal form (INF)** iff it is given by the following abstract syntax

 $t, t_1, t_2 ::= 0 \mid 1 \mid x \rightarrow t_1, t_2$ 

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where x ranges over boolean variables.

Example:  $x_1 \rightarrow (x_2 \rightarrow 1, 0), 0$  (equivalent to  $x_1 \wedge x_2$ )

Boolean expressions in INF can be drawn as decision trees.

Problems over Boolean Expressions are Hard Many problems related to boolean expressions are hard from the theoretical point of view (NP-complete or co-NP-complete).

#### Our Aim

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We are looking for

- compact representation and
- efficient manipulation

with boolean expressions for real-life examples.

We will have a look at **Binary Decision Diagrams (BDDs)** [Randal E. Bryant'86].

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#### Shannon's Expansion Law

Let t be a boolean expression and  $\boldsymbol{x}$  a variable. We define boolean expressions

t[0/x] where every occurrence of x in t is replaced with 0, and
t[1/x] where every occurrence of x in t is replaced with 1.

Shannon's Expansion Law

Let x be an arbitrary boolean variable. Any boolean expressions t is equivalent to

 $x \rightarrow t[1/x], t[0/x].$ 

#### Corollary

For any boolean expression there is an equivalent one in INF.

## If-Then-Else Operator

Let t,  $t_1$  and  $t_2$  be boolean expressions.

Syntax

 $t \rightarrow t_1, t_2$ 

#### Semantics

If-Then-Else operator  $t \to t_1, t_2$  is equivalent to  $(t \land t_1) \lor (\neg t \land t_2)$ .

t	t1	t <sub>2</sub>	$t \rightarrow t_1, t_2$
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	1

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### Binary Decision Diagrams

Let the set of boolean variables be  $\{x_1, \ldots, x_n\}$ .

Binary Decision Diagram (BDD)

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A BDD is a rooted, directed, acyclic graph (V, E) such that

- 0,1  $\in$  V (representing false and true) and the nodes 0 and 1 have no outgoing edges
- every node  $v \in V \smallsetminus \{0,1\}$  has exactly two successors  $\mathit{low}(v) \in V$  and  $\mathit{high}(v) \in V$
- every node  $v \in V \smallsetminus \{0,1\}$  has a label  $var(v) \in \{x_1,\ldots,x_n\}$

Assume a given total ordering < on boolean variables.

Ordered BDD
A BDD is <b>ordered</b> if on all paths from the root the variables respect the
ordering <.

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## Reduced Ordered BDDs (ROBDDs)

Reduced BDD A BDD is **reduced** iff for all nodes  $u, v \in V \setminus \{0, 1\}$ : 1  $low(u) \neq high(u)$ 2 low(u) = low(v) and high(u) = high(v) and var(u) = var(v) implies that u = v.

ROBDD with a root node u describes a boolean expression  $t^u$  according to the following (inductive) definition:

 $\bullet t^0 \stackrel{\text{def}}{=} 0$ •  $t^1 \stackrel{\text{def}}{=} 1$ •  $t^u \stackrel{\text{def}}{=} var(u) \rightarrow t^{high(u)}, t^{low(u)}$ 

Lecture 13 () mantics and Verification 2005 Representing ROBDDs in Memory – Array Implementation



```
Example: T(4) = (2, 0, 2), H(1, 4, 5) = 6, and H(3, 0, 2) = undef.
```

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Canonicity of ROBDDs

describes the function f, i.e.

for all  $(v_1, \ldots, v_n) \in \mathbb{B}^n$ .

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if low = high then

return low

else

else

end if

end if

Consequences:

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Canonicity Lemma

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add a new node (row) to T with attributes (var, low, high)

For any boolean function  $f : \mathbb{B}^n \to \mathbb{B}$  and a given ordering of variables  $x_1 < x_2 < \cdots < x_n$  there is **exactly one ROBDD** with root *u* which

 $t^{u}[v_1/x_1,\ldots,v_n/x_n]=f(v_1,\ldots,v_n)$ 

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 $H: (var, low, high) \mapsto u$ 

• A given ROBDD with root u is tautology iff u = 1.

• A given ROBDD with root u is satisfiable iff  $u \neq 0$ .

Makenode and Reducedness of BDDs

 $T: u \mapsto (var(u), low(u), high(u))$ 

u := H(var, low, high)

**return** *H*(*var*, *low*, *high*)

if  $u \neq undef$  then

return u

**Makenode** (var, low, high): Node =



## Ordering of Variables (Exponential Difference in Size)



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## Building an ROBDD from a Boolean Expression

Let *t* be a boolean expression and  $x_1 < x_2 < \cdots < x_n$ . **Build**(t, 1) builds a corresponding ROBDD and returns its root.

```
Build(t, i): Node =
if i > n then
  if t is true then return 0 else return 1
else
  low := Build(t[0/x_i], i+1)
  high := \text{Build}(t[1/x_i], i+1)
  var := i
  return Makenode(var, low, high)
end if
```

Complexity: exponentially many recursive calls! Is this necessary? Yes, checking if t is a tautology is co-NP-hard!

# Boolean Operations on ROBDDs

Idea (assume  $x_1 < x_2 < \cdots < x_n$ )

•  $x_i = x_i$ 

•  $x_i < x_j$ 

Let us assume that ROBDDs for boolean expressions  $t_1 \mbox{ and } t_2$  are already constructed.

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Apply with Dynamic Programming in  $O(|u_1| \cdot |u_2|)$ 

Two dimensional array G(-, -) initially empty.

else if  $(u_1 \in \{0, 1\}$  and  $u_2 \in \{0, 1\})$  then

**Apply**  $(u_1, u_2: \text{Node}): \text{Node} =$ 

else if  $var(u_1) = var(u_2)$  then

else if  $var(u_1) < var(u_2)$  then

else if  $var(u_1) > var(u_2)$  then

if  $G(u_1, u_2) \neq$  empty then return  $G(u_1, u_2)$ 

 $u := u_1 op u_2$ 

u := ...

u := ...

u := ...

end if  $G(u_1, u_2) := u$ return u

How to construct ROBDD for

- $\neg t_1$
- $t_1 \wedge t_2$
- $t_1 \vee t_2$

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- $t_1 \Rightarrow t_2$
- $t_1 \Leftrightarrow t_2$

with an emphasis on efficiency?

 $egin{aligned} &(x_i 
ightarrow t_1, t_2) \wedge (x_i 
ightarrow t_1', t_2') \ &\equiv \ &x_i 
ightarrow (t_1 \wedge t_1'), (t_2 \wedge t_2') \end{aligned}$ 

 $egin{aligned} &(x_i 
ightarrow t_1, t_2) \wedge (x_j 
ightarrow t_1', t_2') \ &\equiv \ &x_i 
ightarrow ig(t_1 \wedge (x_j 
ightarrow t_1', t_2')ig), ig(t_2 \wedge (x_j 
ightarrow t_1', t_2')ig) \end{aligned}$ 

The same equivalences hold also for  $\lor$ ,  $\Rightarrow$  and  $\Leftrightarrow$ .

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	Other Operations on	ROBDDs

Let t be a boolean expression with its ROBDD representation.

The following operations can be done efficiently:

- **Restriction**  $t[0/x_i]$  ( $t[1/x_i]$ ): restricts the variable  $x_i$  to 0 (1)
- SatCount(t): returns the number of satisfying assignments
- AnySat(t): returns some satisfying assignment
- AllSat(t): returns all satisfying assignments
- Existential quantification  $\exists x_i.t$ : equivalent to  $t[0/x_i] \lor t[1/x_i]$
- **Composition**  $t[t'/x_i]$ : equivalent to  $t' \rightarrow t[1/x_i], t[0/x_i]$

Apply (for  $op \in \{\land, \lor, \Rightarrow, \Leftrightarrow\}$ )

**Apply**  $(u_1, u_2: \text{Node}): \text{Node} =$ if  $(u_1 \in \{0, 1\}$  and  $u_2 \in \{0, 1\}$ ) then  $u := u_1 op u_2$ else if  $var(u_1) = var(u_2)$  then  $\ell := \operatorname{Apply}(low(u_1), low(u_2)); h := \operatorname{Apply}(high(u_1), high(u_2))$  $u := Makenode(var(u_1), \ell, h)$ else if  $var(u_1) < var(u_2)$  then  $\ell := \operatorname{Apply}(low(u_1), u_2); h := \operatorname{Apply}(high(u_1), u_2)$  $u := Makenode(var(u_1), \ell, h)$ else if  $var(u_1) > var(u_2)$  then  $\ell := \operatorname{Apply}(u_1, low(u_2)); h := \operatorname{Apply}(u_1, high(u_2))$  $u := Makenode(var(u_2), \ell, h)$ end if return u Problem: Exponentially many recursive calls! Lecture 13 () antics and Verification 2005

Use of ROBDDs

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- Combinatorial circuits.
- Combinatorial problems.

• Verification (equivalence checking, temporal logic model checking).

• Program analysis.

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• ...

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