

# Model Based Testing of Embedded Systems

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**Arne Skou**

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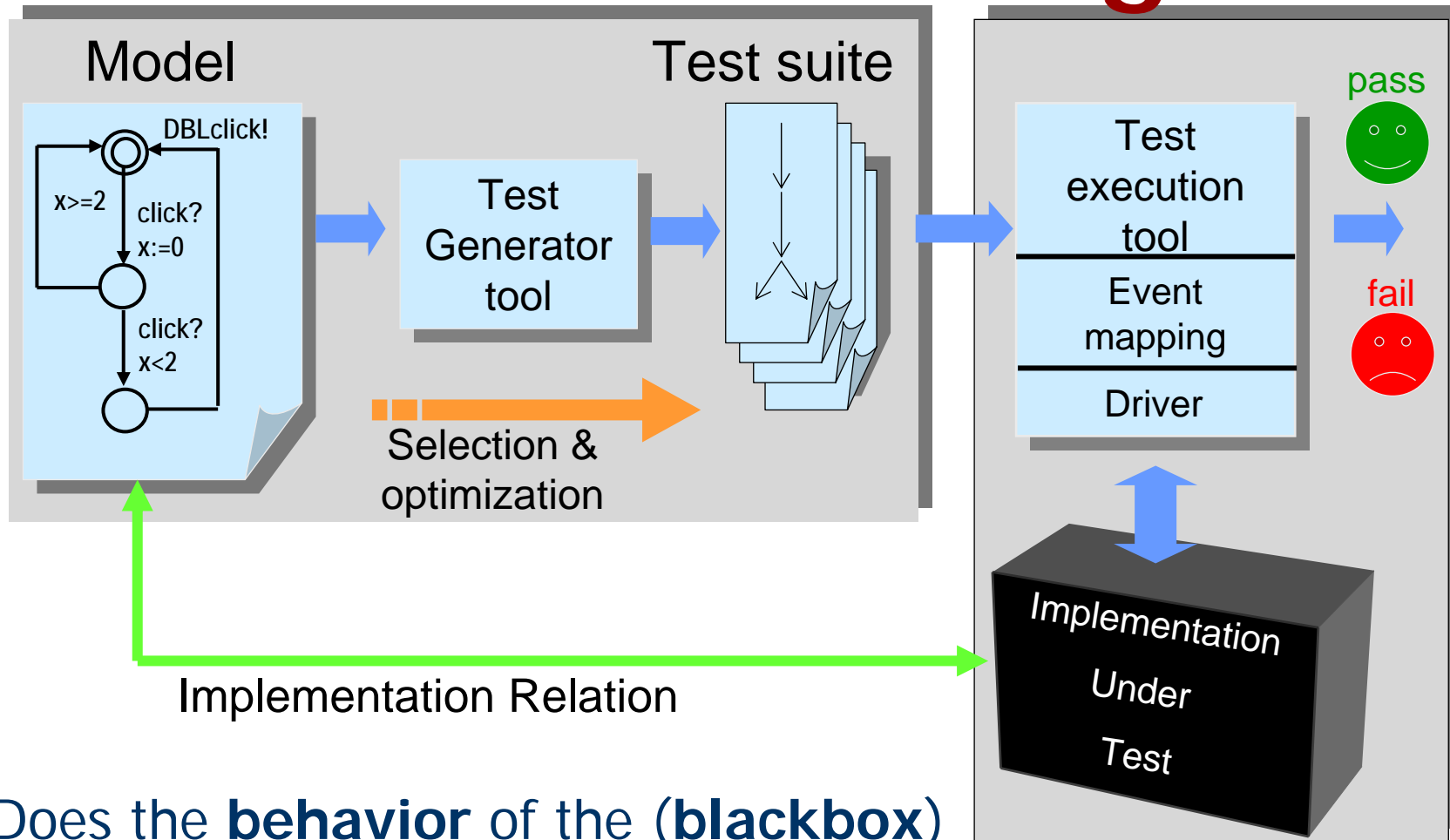
**BRICS**

Basic Research  
in Computer Science



CENTER FOR INDLEJREDE SOFTWARE SYSTEMER

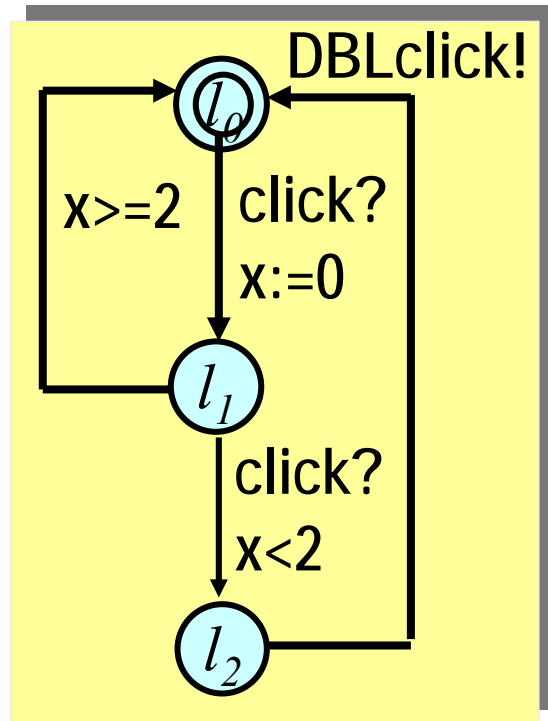
# Automated Model Based Conformance Testing



Does the **behavior** of the (**blackbox**) implementation *comply* to that of the specification?

# Specification Based Testing

A Specification



Test cases

Click!  
Wait 1.5  
click!  
DBLClick?  
(pass)

Click!  
Wait 5  
click!  
DBLClick?  
(fail)

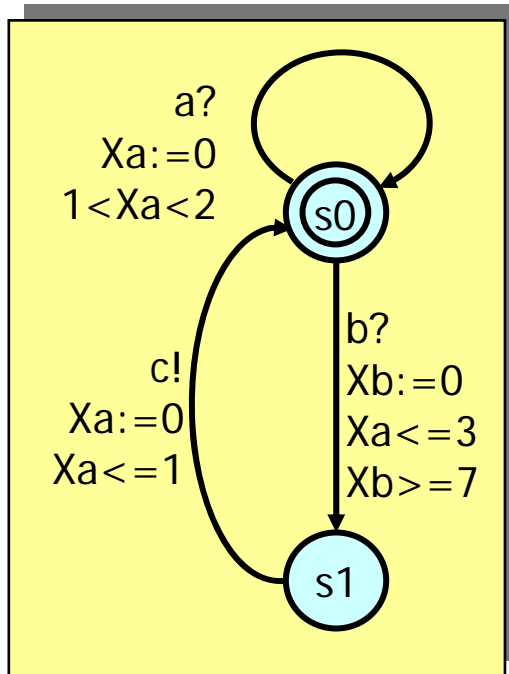
Click!  
Wait 0.1  
click!  
DBLClick?  
(pass)

Timed Automaton = FSM + Clocks (dense) + Guards + Resets

- $x_1 \sim c, x_1 - x_2 \sim c$ , where  $\sim \in \{<, \leq, \geq, >\}$
- $x := c$
- Semantic state:  $(l, \bar{u})$        $(l_1, x = 1.17)$

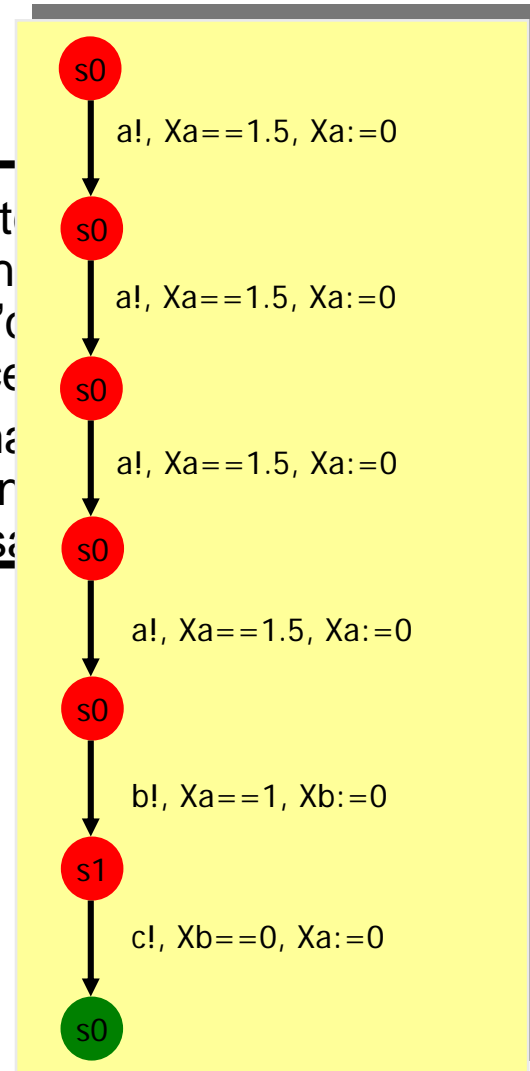
# Exercises

## Simple TA-specification



## Generated test case

- 1) compute trace that a "c" produces
- 2) How many test cases do you think are necessary?

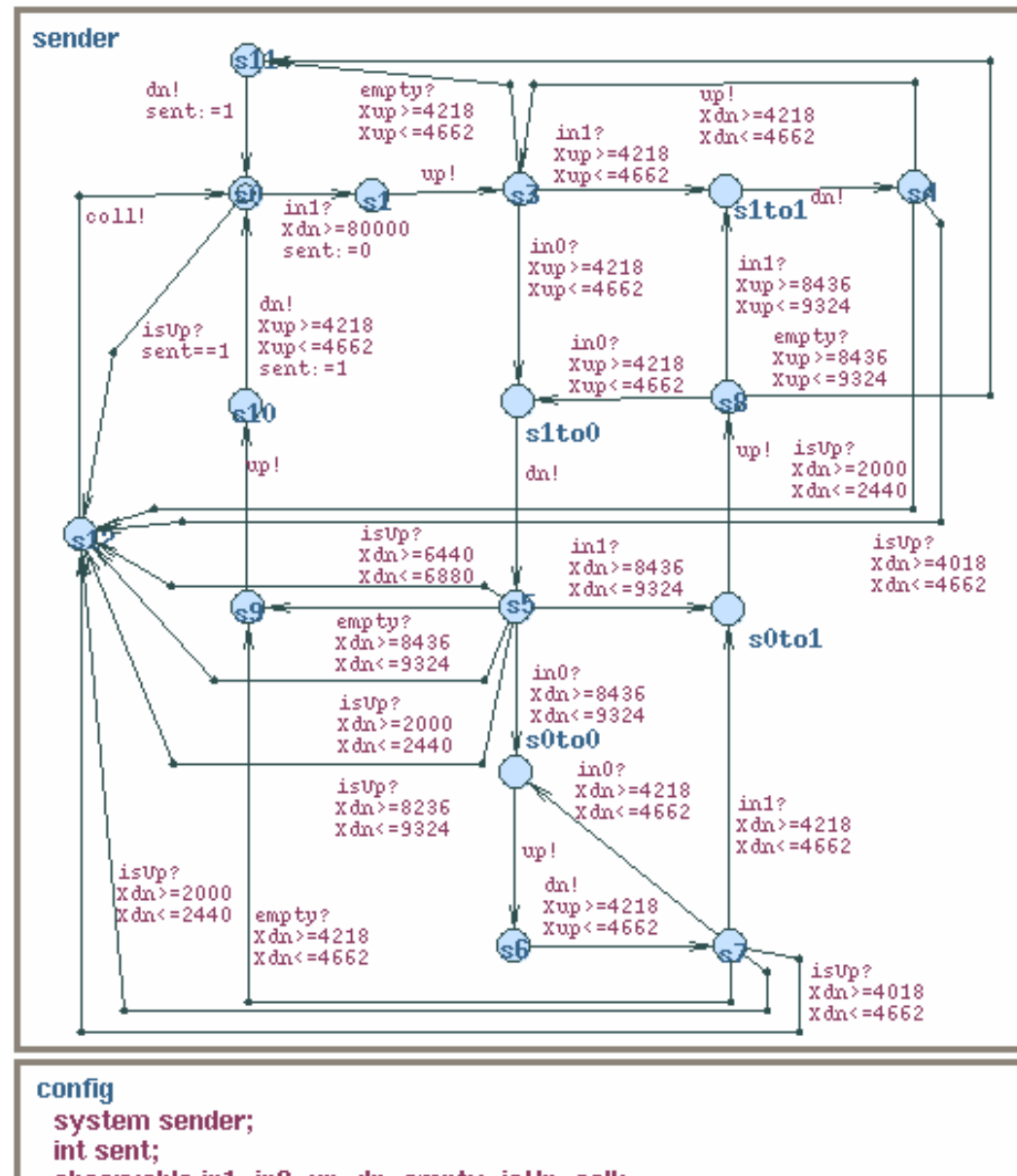


12 test cases are generated!



# How do we cope with real-life specs?

Philips  
Sender  
with  
collision  
detection



# Correctness Criteria

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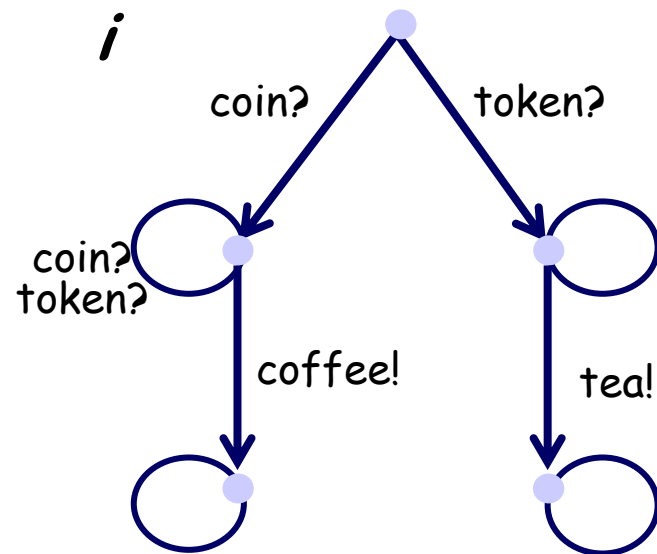


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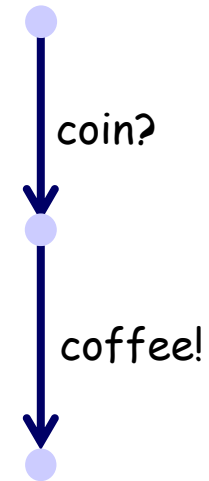
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# I conforms-to S ??

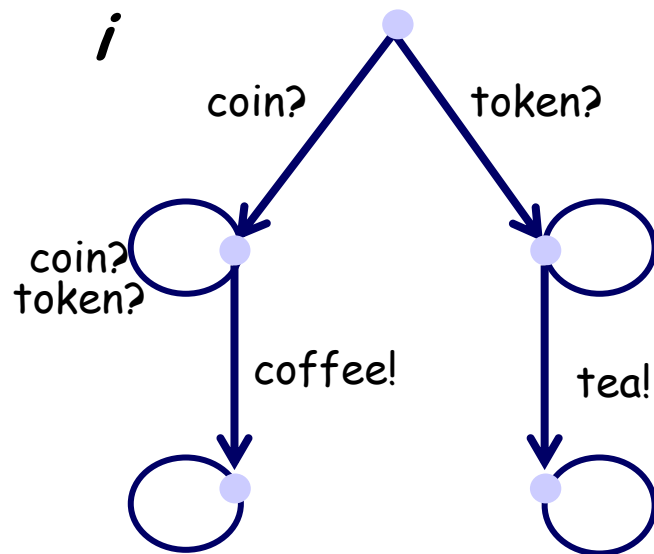


*s*

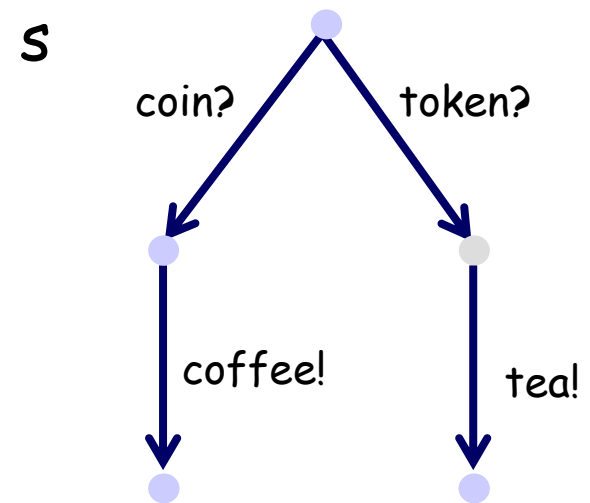
**ioco**



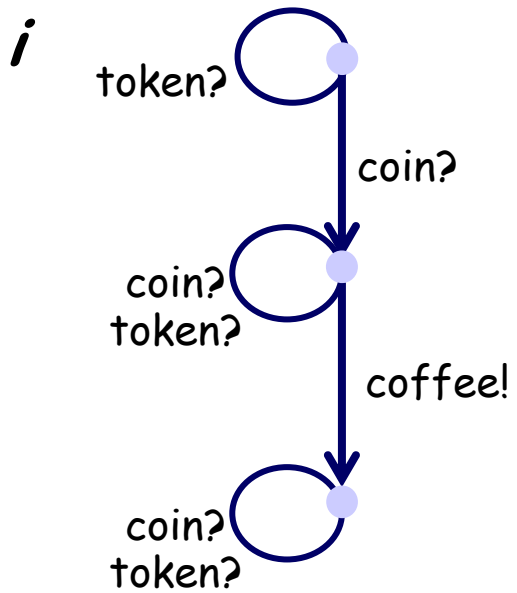
# I conforms-to S ??



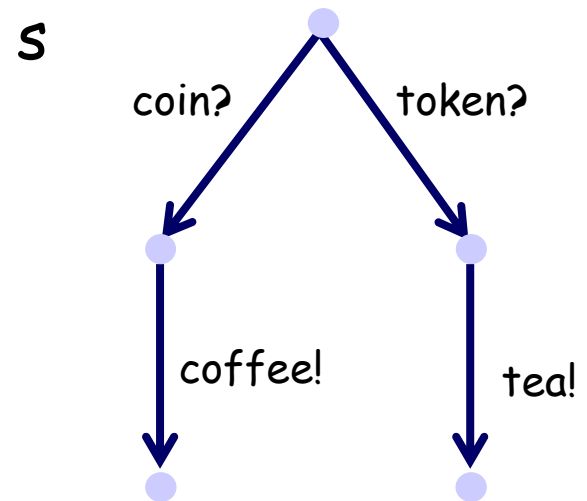
**ioco**



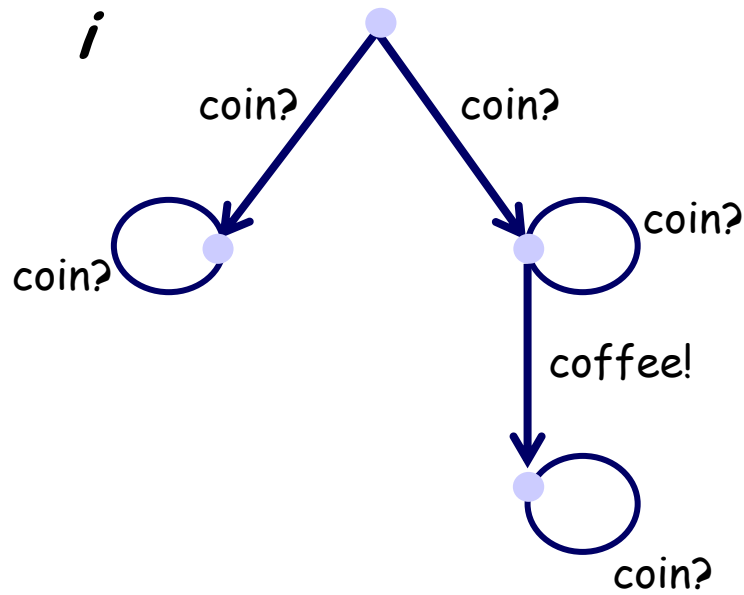
# I conforms-to S ??



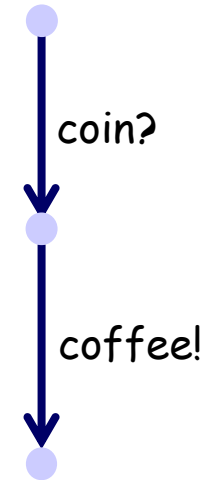
~~ioco~~



# I conforms-to S ??



*s*



~~io~~co

# Implementation

## Relation **ioco**

$$i \text{ ioco } s \quad =_{\text{def}} \quad \forall \sigma \in \text{Straces}(s) : \text{out}(i \text{ after } \sigma) \subseteq \text{out}(s \text{ after } \sigma)$$

$$p \text{ after } \sigma \quad = \quad \{ p' \mid p \xRightarrow{\sigma} p' \}$$

$$p \xrightarrow{\delta} p \quad = \quad p \xrightarrow{L_U} p \quad = \quad \forall x! \in L_U \cup \{\tau\} : p \not\xrightarrow{x!}$$

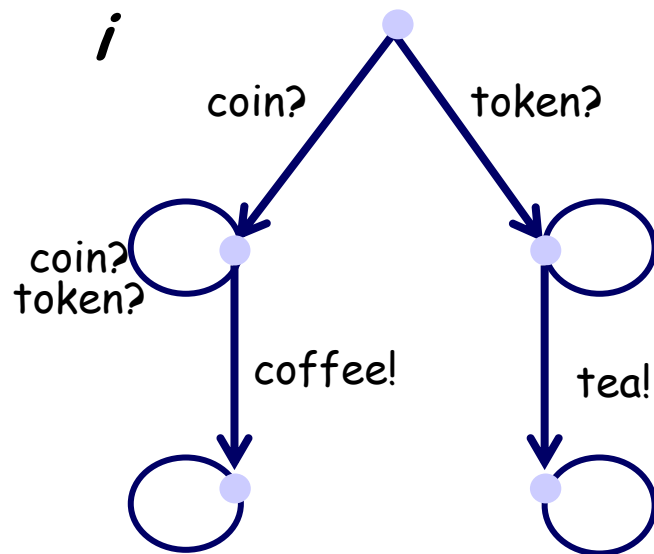
$$\begin{aligned} \text{out}(P) &= \{ x! \in L_U \mid p \xrightarrow{x!} , p \in P \} \\ &\cup \{ \delta \mid p \xrightarrow{\delta} p, p \in P \} \end{aligned}$$

$$\text{Straces}(s) \quad = \quad \{ \sigma \in (L \cup \{\delta\})^* \mid s \xRightarrow{\sigma} \}$$

# Implementation Relation

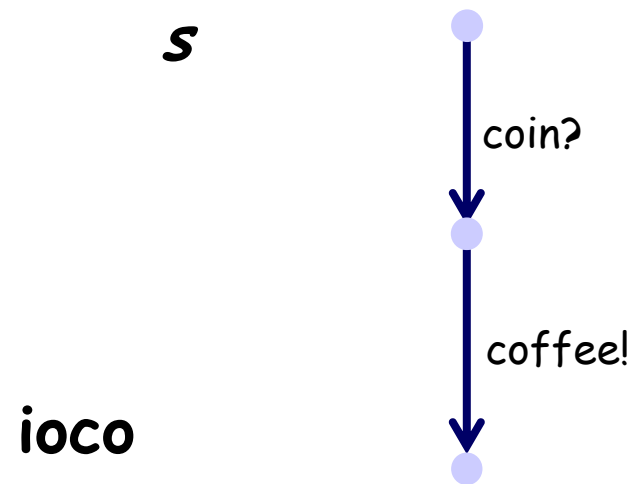
## ioco

$i \text{ ioco } s \stackrel{\text{def}}{=} \forall \sigma \in \text{Straces}(s) : \text{out}(i \text{ after } \sigma) \subseteq \text{out}(s \text{ after } \sigma)$



$\text{out}(i \text{ after } \text{coin?}) = \{ \text{coffee!} \}$

$\text{out}(i \text{ after } \text{token?}) = \{ \text{tea!} \}$



$\text{out}(s \text{ after } \text{coin?}) = \{ \text{coffee!} \}$

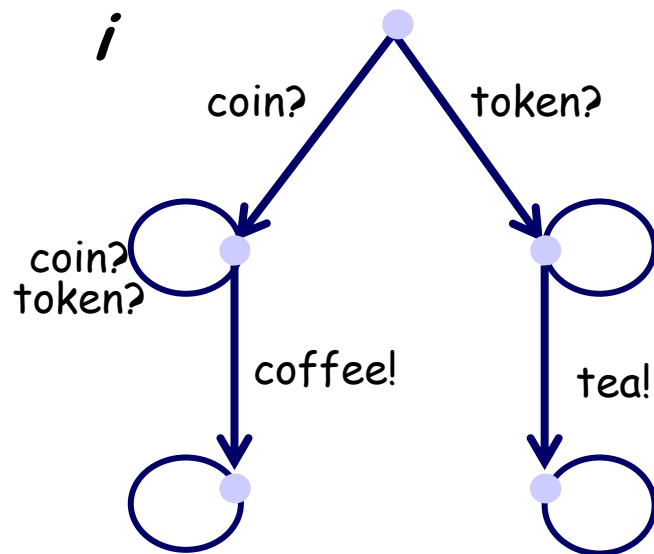
$\text{out}(s \text{ after } \text{token?}) = \emptyset$

But  $\text{token?} \notin \text{Straces}(s)$

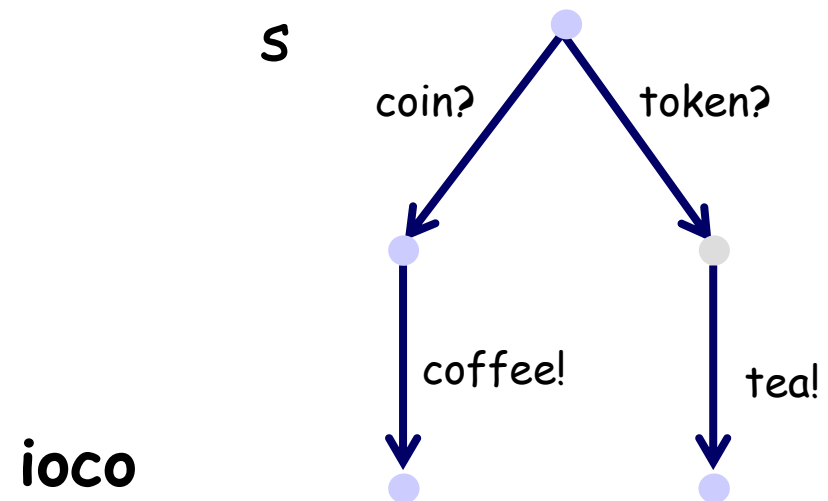
# Implementation Relation

## ioco

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$\text{out}(i \text{ after } \text{coin?}) = \{ \text{coffee!} \}$   
 $\text{out}(i \text{ after } \text{token?}) = \{ \text{tea!} \}$

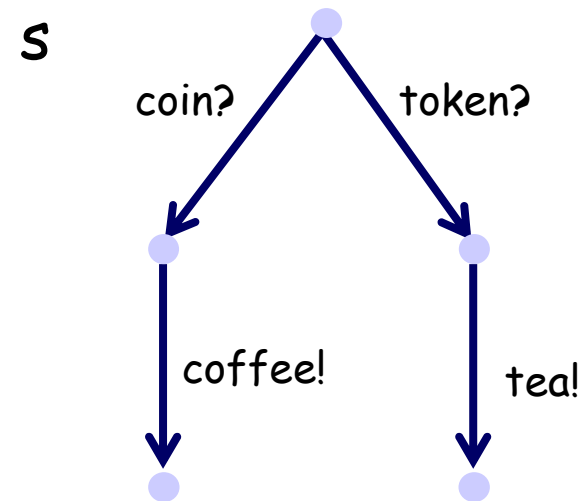
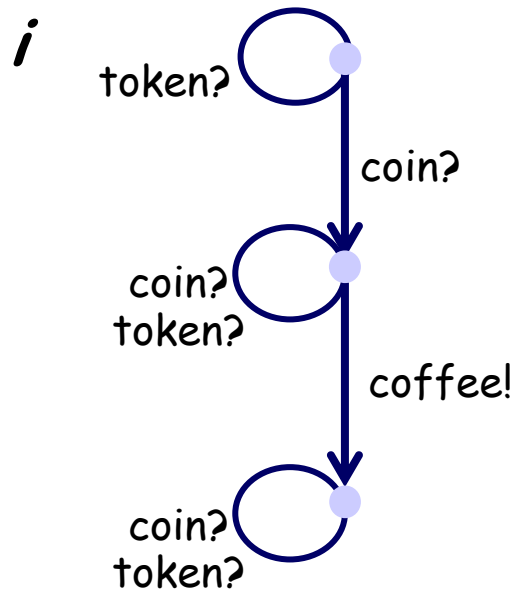


$\text{out}(s \text{ after } \text{coin?}) = \{ \text{coffee!} \}$   
 $\text{out}(s \text{ after } \text{token?}) = \{ \text{tea!} \}$

# Implementation Relation

## ioco

$i \text{ ioco } s \stackrel{\text{def}}{=} \forall \sigma \in \text{Straces}(s) : \text{out}(i \text{ after } \sigma) \subseteq \text{out}(s \text{ after } \sigma)$



~~ioco~~

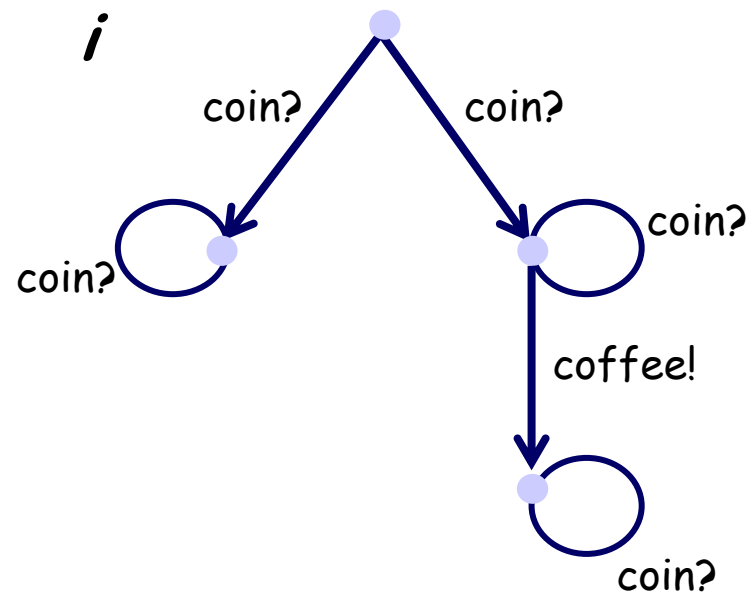
$\text{out}(i \text{ after } \text{token?}) = \{\delta\}$

$\text{out}(s \text{ after } \text{token?}) = \{\text{tea!}\}$

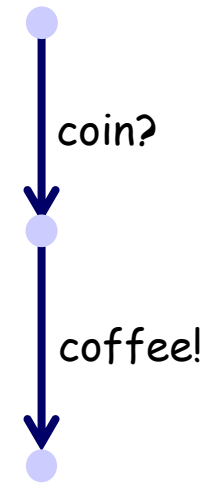
# Implementation Relation

## ioco

$$i \text{ ioco } s \stackrel{\text{def}}{=} \forall \sigma \in \text{Straces}(s) : \text{out}(i \text{ after } \sigma) \subseteq \text{out}(s \text{ after } \sigma)$$



*s*



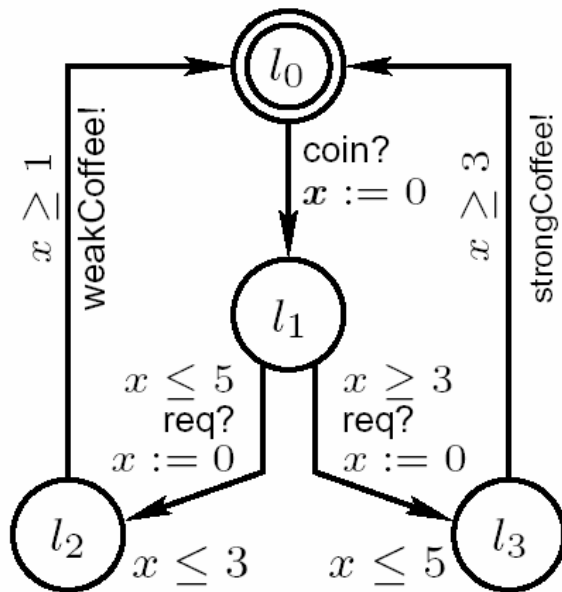
~~ioco~~

$$\text{out}(i \text{ after } \text{coin?}) = \{ \delta, \text{coffee!} \}$$

$$\text{out}(s \text{ after } \text{coin?}) = \{ \text{coffee!} \}$$

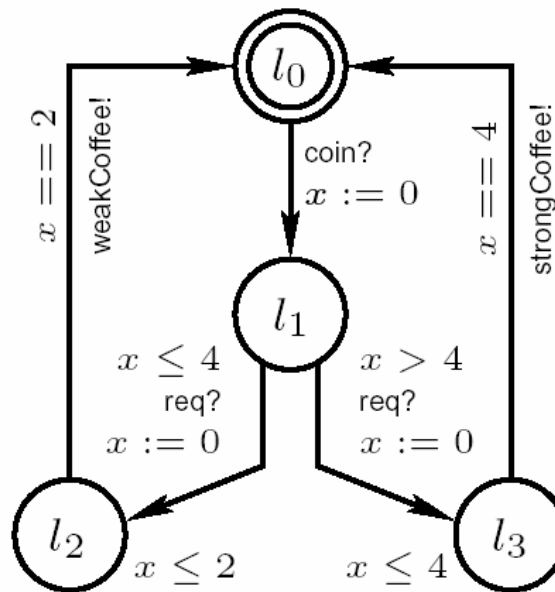
# Timed Conformance??

Specification



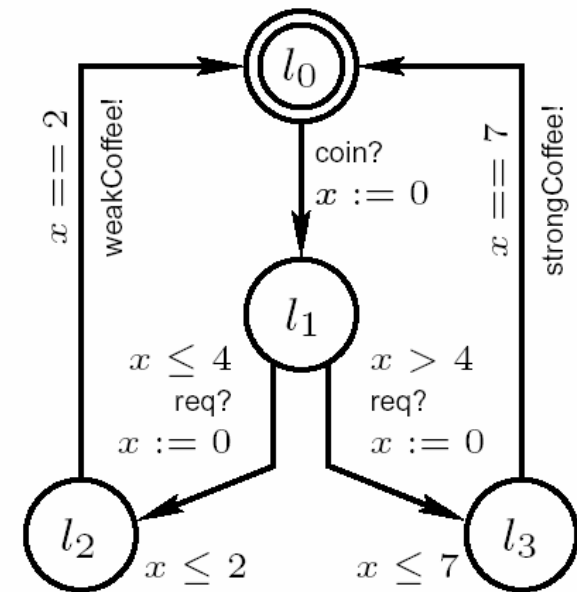
Example Traces

Implementation 1



- c?.2.r?.2.weakC
- c?.5.r?.4.strongC

Implementation 2



- c?.2.r?.2.weakC
- c?.5.r?.7

l1 **rt-ioco** S

l2 ~~rt-ioco~~ S

# Real-Time Conformance

- **TTr**(s): the set of *timed traces* from s
  - eg.:  $\sigma = \text{grasp?} \cdot 50 \cdot \text{release?} \cdot 50$
- **Out**(s after  $\sigma$ ) = possible outputs and delays after  $\sigma$ 
  - eg. **Out** (interface after  $\text{grasp?} \cdot 220$ ) = {touch!, 0..270}

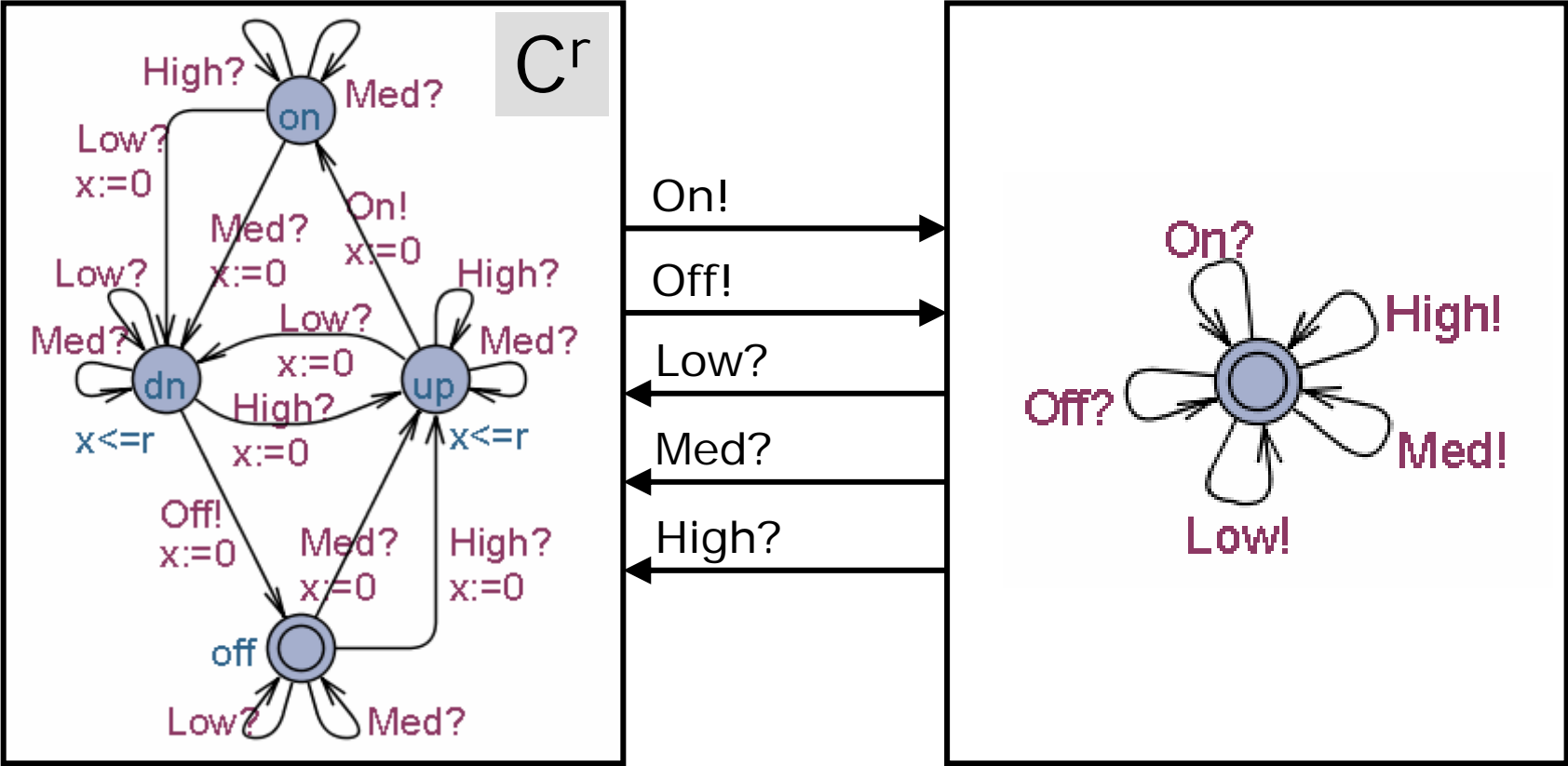
## • **i rt-ioco s =def**

- $\forall \sigma \in \text{TTr}(s): \text{Out}(i \text{ after } \sigma) \subseteq \text{Out}(s \text{ after } \sigma)$
- $\text{TTr}(i) \subseteq \text{TTr}(s)$

## • **Intuition**

- **never produces illegal output, and**
- **always produces required output in time**

# Sample Cooling Controller



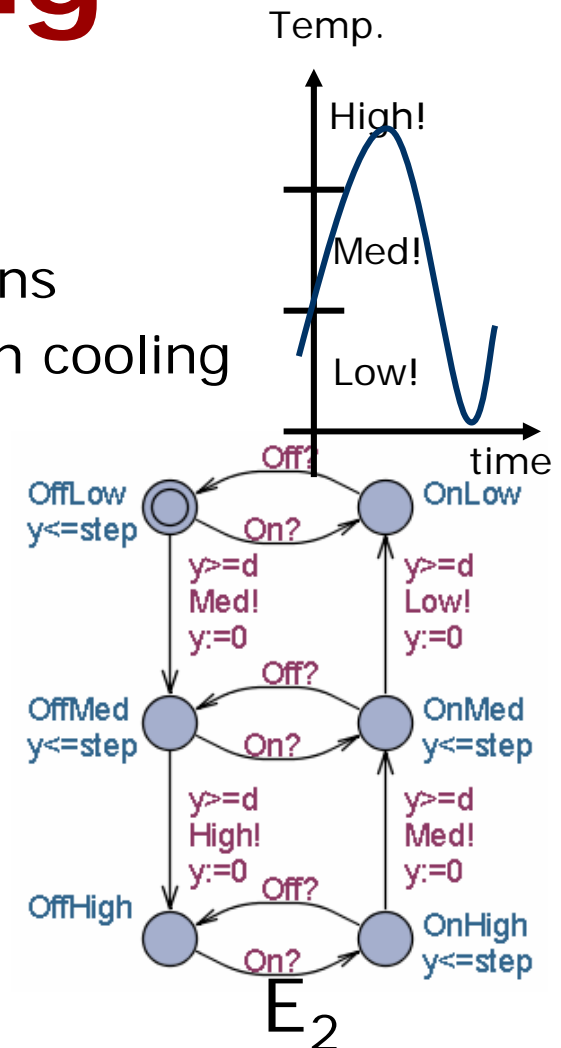
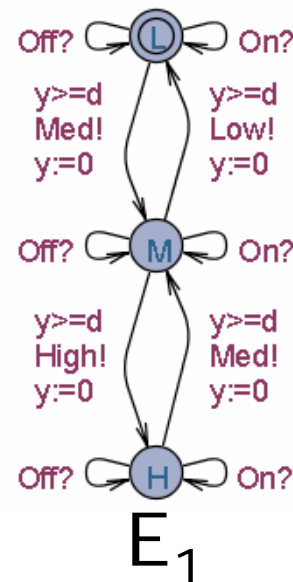
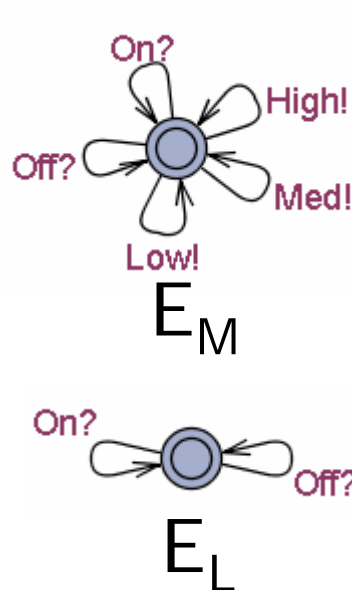
IUT-model

Env-model

# Env. Modeling

## ■ Realism and Guiding

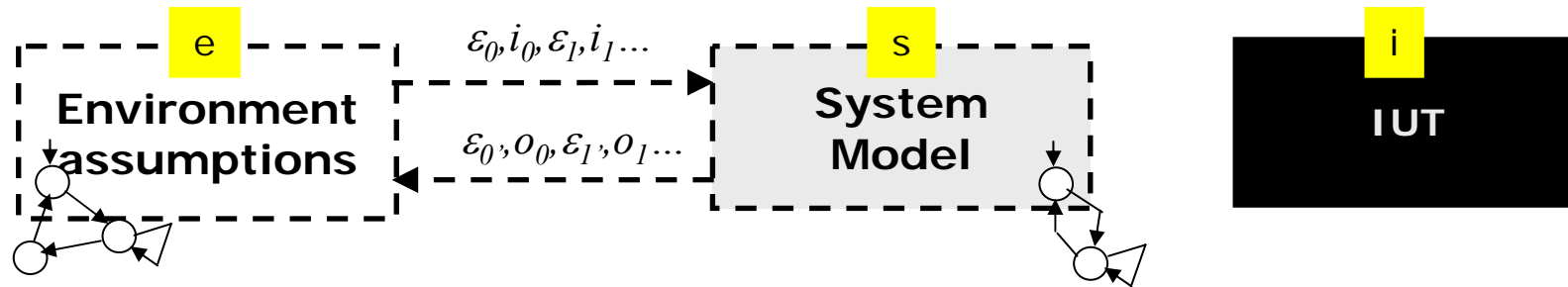
- $E_M$  Any action possible at any time
- $E_1$  Only realistic temperature variations
- $E_2$  Temperature never increases when cooling
- $E_L$  No inputs (completely passive)



$$E_L \subseteq E_2 \subseteq E_1 \subseteq E_M$$

# Implementation relation

## Relativized real-time io-conformance

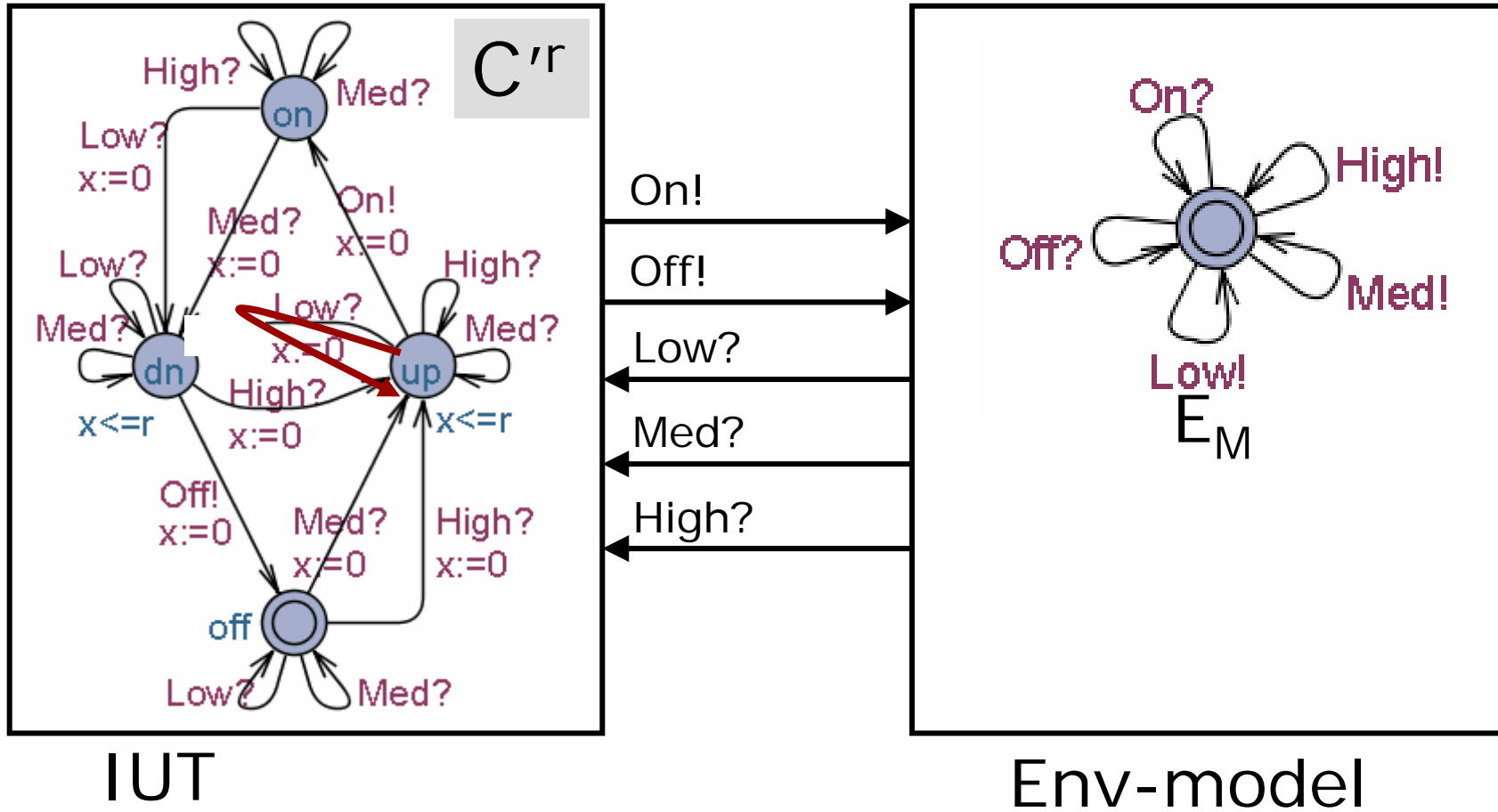


- Let  $P$  be a set of states
- $TTr(P)$ : the set of *timed traces* from states in  $P$
- $P$  after  $\sigma$  = the set of states reachable after timed trace  $\sigma$
- $Out(P)$  = possible outputs and delays in  $P$

- $i \text{ rt-ioco}_e s = \text{def}$ 
  - $\forall \sigma \in TTr(e): Out((e,i) \text{ after } \sigma) \subseteq Out((e,s) \text{ after } \sigma)$
- $i \text{ rt-ioco}_e s$  iff  $TTr(i) \cap TTr(e) \subseteq TTr(s) \cap TTr(e)$

- **Intuition, for all relevant environment behaviors**
  - never produces illegal output, and
  - always produces required output in time
- $\sim$ timed trace inclusion

# Sample Cooling Controller



$C^r \text{ rt-ioco } E_M C^r$

# Test Generation

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# Test Generation Principles

- Test purpose based generation
  - ✱ “test that light can become max”
  - ✱ Formalize purpose
  - ✱ Use model to compute sequence that meets purpose (inputs and expected outputs)
- Model Coverage
  - ✱ State-coverage
  - ✱ Transition
  - ✱ Def-Use pairs
  - ✱ ...
- Randomized model interpretation
- Fault-Models
- ONLINE (randomized) testing

# Time Optimal Real-Time Test Generation using UPPAAL

---

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Paul Pettersson  
Uppsala University  
Sweden

Kim Larsen,  
**Brian Nielsen,**  
Arne Skou  
Aalborg University  
Denmark

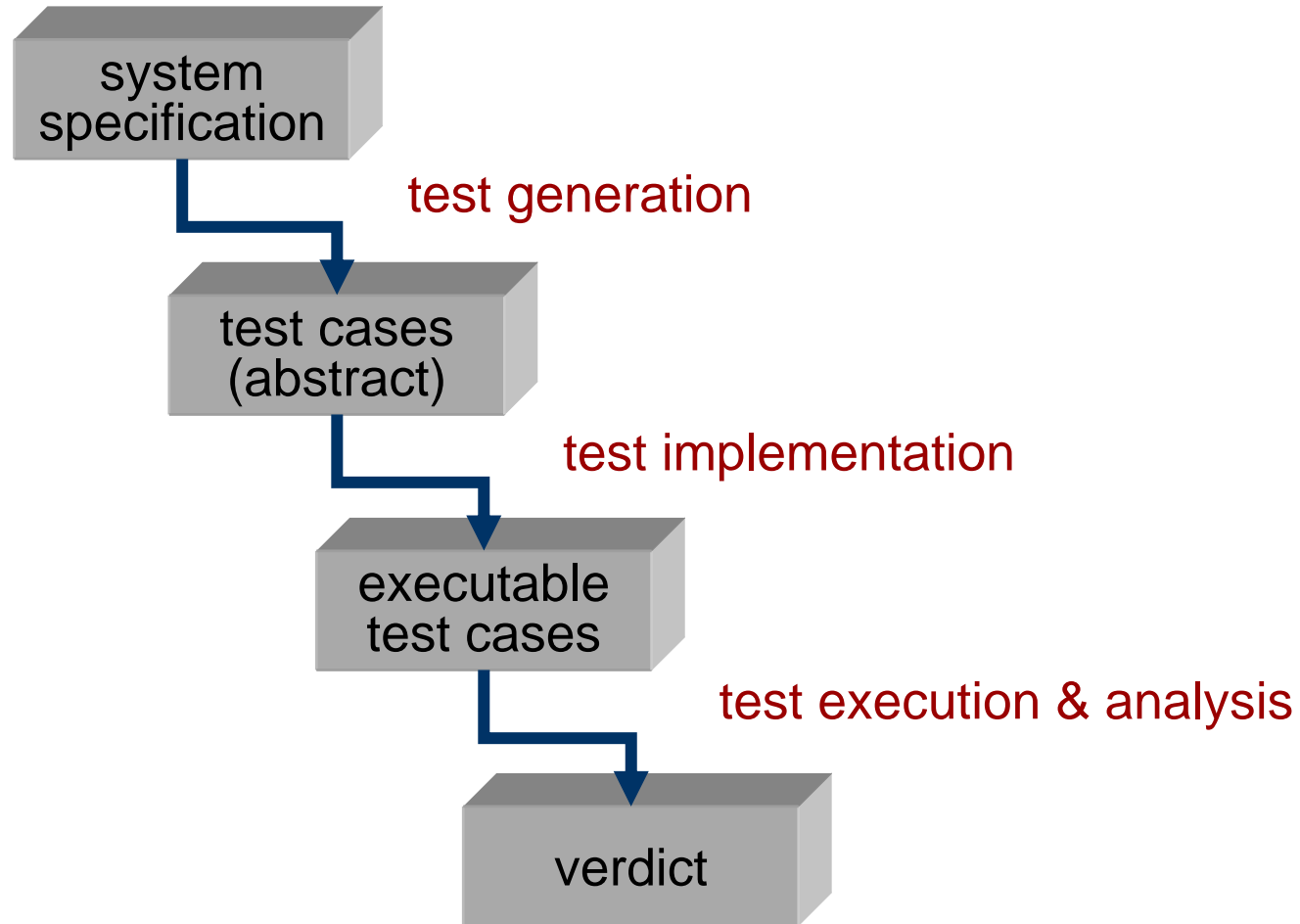


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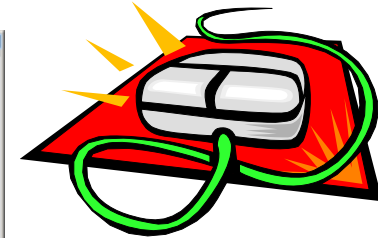
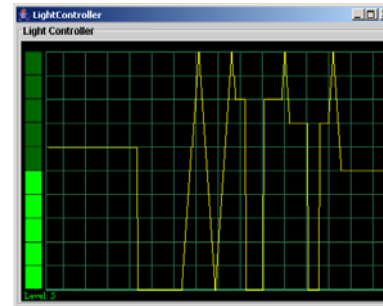


CENTER FOR INDLEJREDE SOFTWARE SYSTEMER

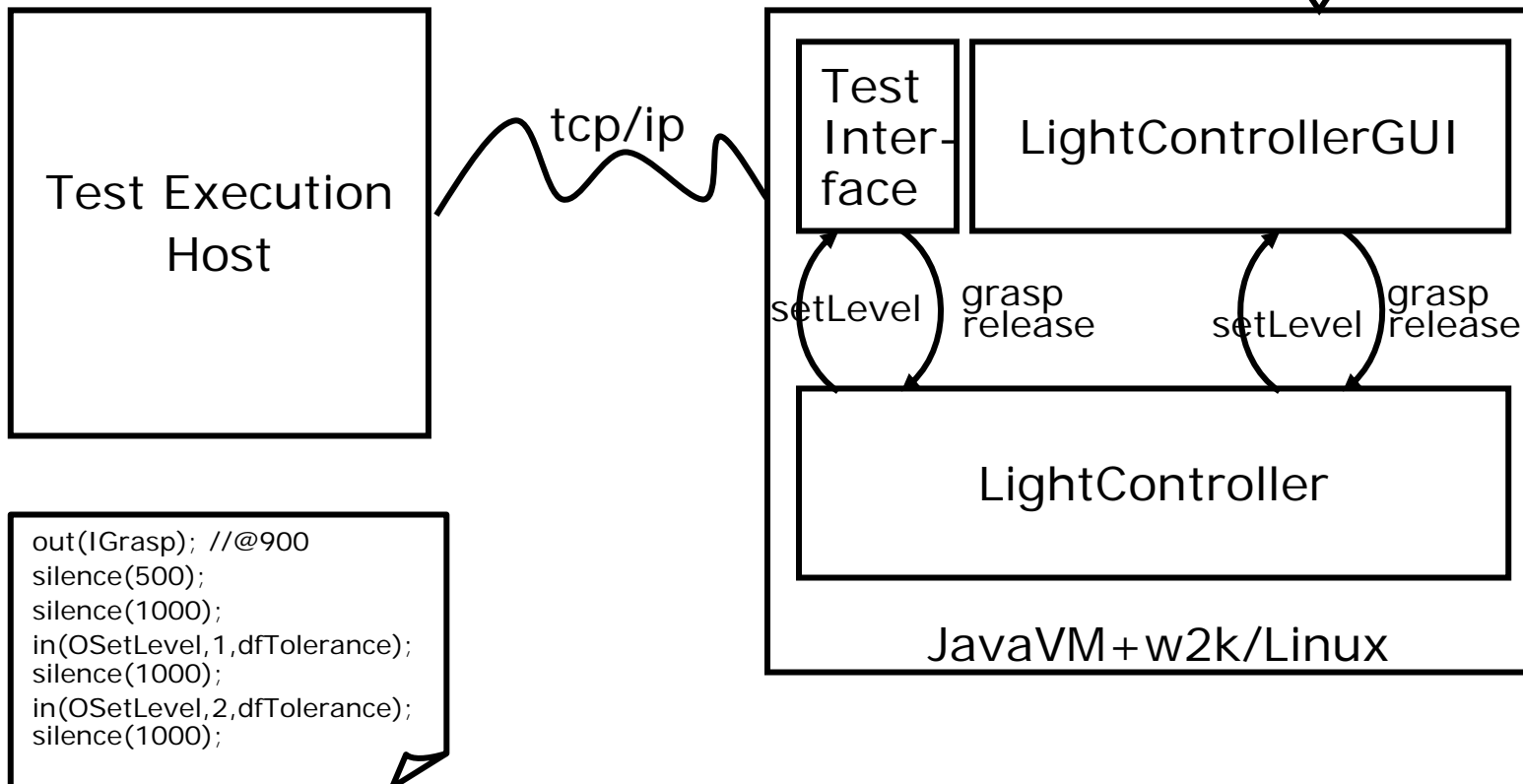
# Testing Process



# Test Setup



mousePress  
mouseRelease

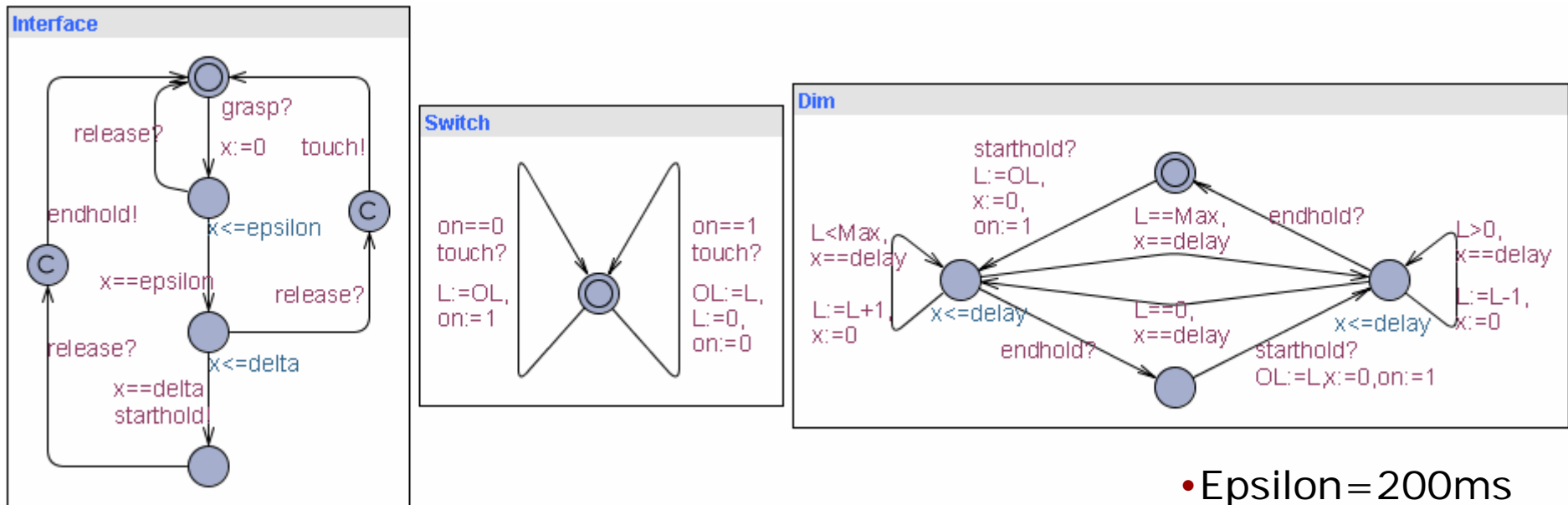


# "Scripts" for LightControl

Events: const IGrasp=0; const int IRelease=1; const int OSetLevel=0;

- void **out**(int eventNo);  
*send eventNo to IUT at now();*
- void **silence**(int msDelay);  
*expect no outputs for msDelay: otherwise fail*
- void **in** (int eventNo,int par, int msTolerance);  
*expect input event(par) before now()+msTolerance  
otherwise fail*
- void **at**(int eventNo, int par, int msTime, int msTolerance);  
*expect input eventNo(par) at time msTime from  
start of test +/- msTolerance*

# Timed Tests



**EXAMPLE** test cases for **Interface**

- Epsilon = 200ms
- Delta = 500ms

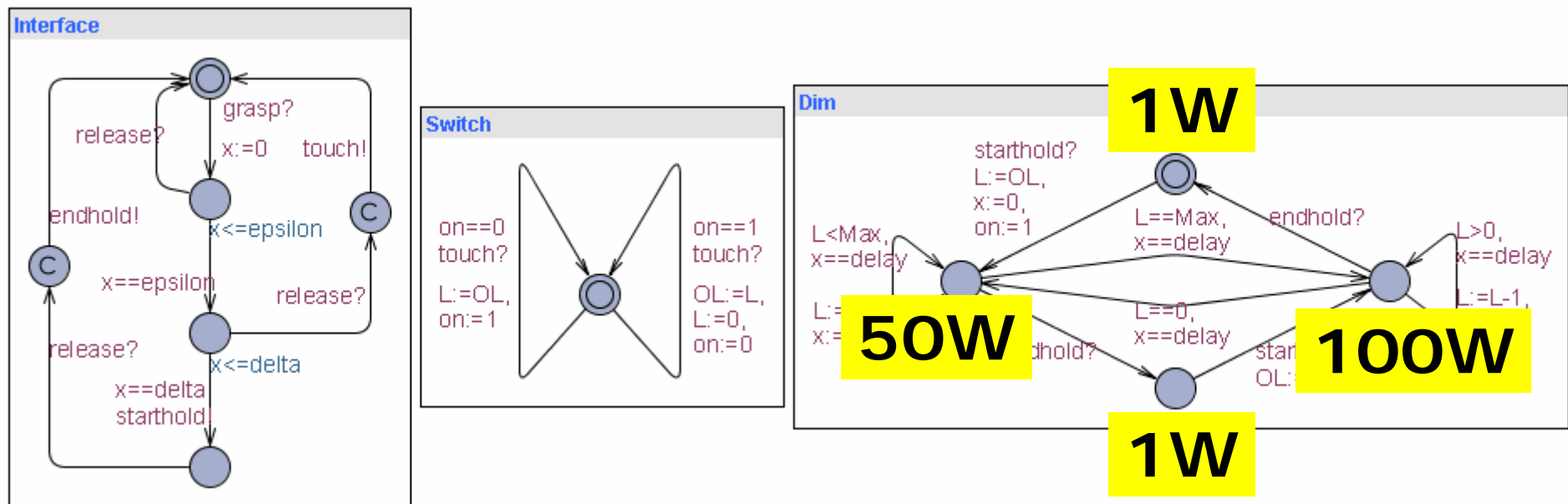
0 • grasp! • 210 • release! • touch? • **PASS**

0 • grasp! • 317 • release! • touch? • 2½ • grasp! • 220 • release! • touch? • **PASS**

1000 • grasp! • 517 • starthold? • 100 • release! • endhold? • **PASS**

**INFINITELY MANY SEQUENCES!!!!!!**

# Optimal Tests

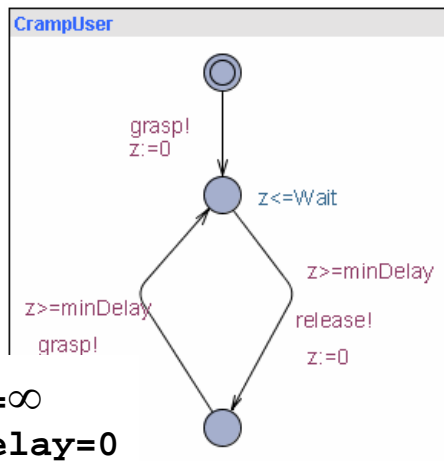


- **Shortest** test for max light??
- **Fastest** test for max light??
- **Fastest** edge-covering test suite??
- Least **power** consuming test??

# Test Purposes 1

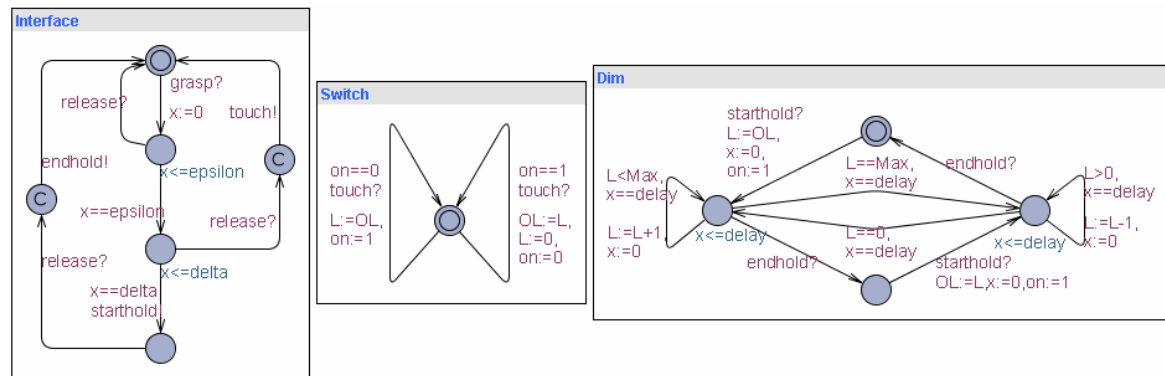
A specific test objective (or observation) the tester wants to make on SUT

Environment model



Wait= $\infty$   
minDelay=0

System model



**TP1:** Check that the light can become bright:

**E<> L==10**

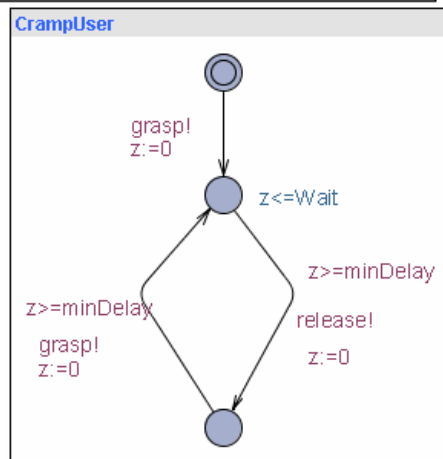
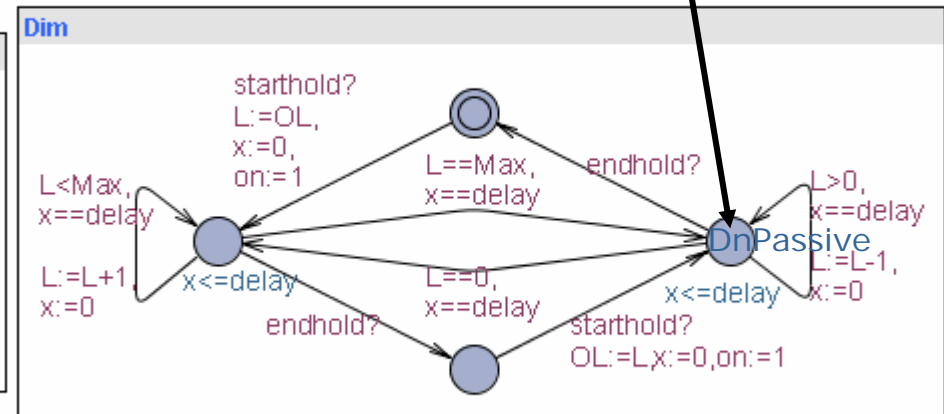
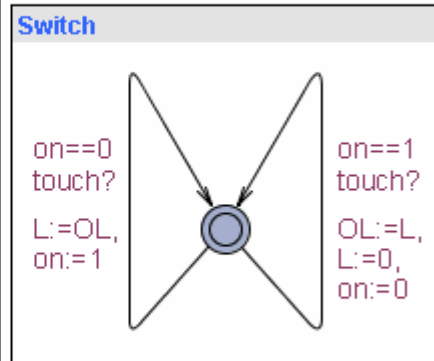
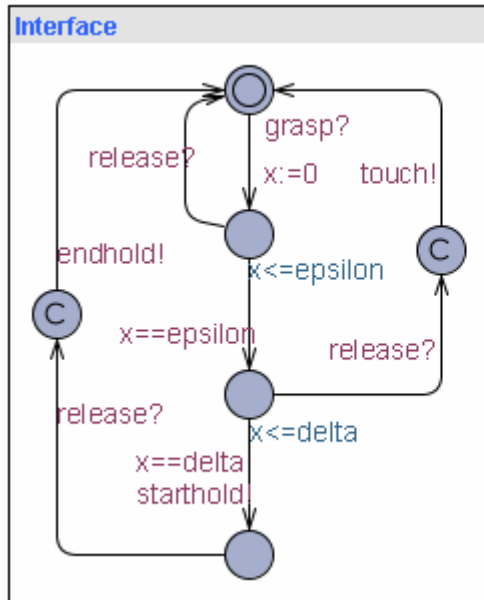
- *Shortest (and fastest) Test:*

```

out(IGrasp); silence(500); in(OSetLevel,0); silence(1000);
in(OSetLevel,1); silence(1000); in(OSetLevel,2); silence(1000);
in(OSetLevel,3); silence(1000); in(OSetLevel,4); silence(1000);
in(OSetLevel,5); silence(1000); in(OSetLevel,6); silence(1000);
in(OSetLevel,7); silence(1000); in(OSetLevel,8); silence(1000);
in(OSetLevel,9); silence(1000); in(OSetLevel,10);
out(IRelease);
    
```

# Test Purposes 2

**TP2:** Check that controller can enter location 'DnPassive':  
**E<> Dim.DnPassive**



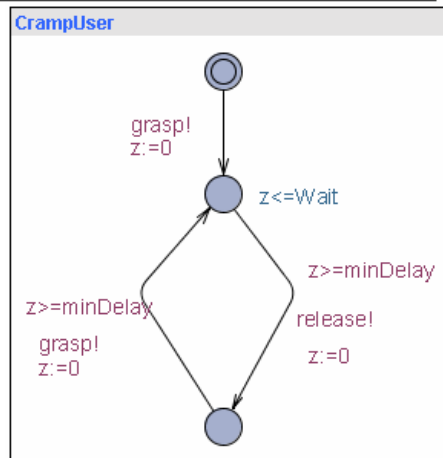
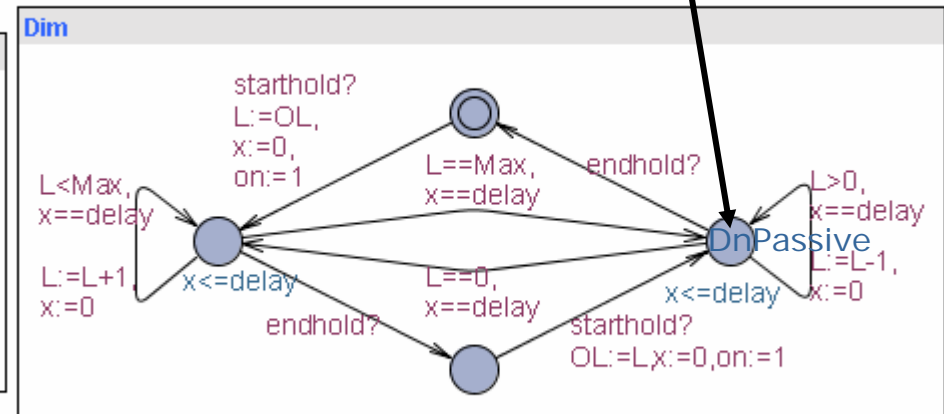
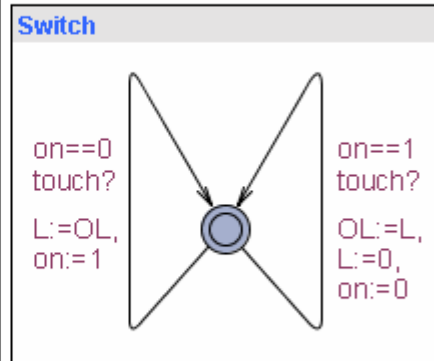
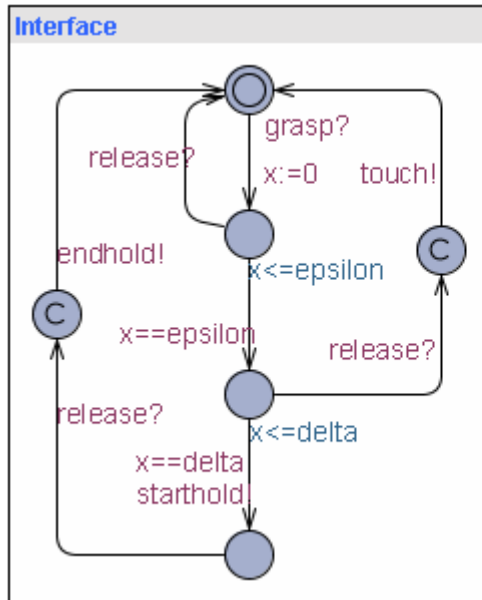
- If delay=1000
- *Shortest (and fastest) Test:*

```

out (IGrasp);
silence(500);
in (OSetLevel,0);
out (IRelease);
out (IGrasp);
silence(500);
    
```

# Test Purposes 2

**TP2:** Check that controller can enter location 'DnPassive':  
**E<> Dim.DnPassive**



- If delay=40?
- *Shortest* Test:
- *Fastest* Test:

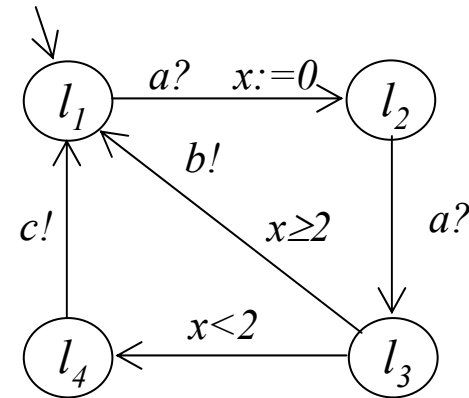
```
out (IGrasp);
silence(500);
in(OSetLevel,0);
out(IRelease);
out(IGrasp);
silence(500);
```

```
out(IGrasp);silence(500);in(OSetLevel,0);silence(40);
in(OSetLevel,1);silence(40);in(OSetLevel,2);silence(40);
in(OSetLevel,3);silence(40);in(OSetLevel,4);silence(40);
in(OSetLevel,5);silence(40);in(OSetLevel,6);silence(40);
in(OSetLevel,7);silence(40);in(OSetLevel,8);silence(40);
in(OSetLevel,9);silence(40);in(OSetLevel,10);silence(40);
```



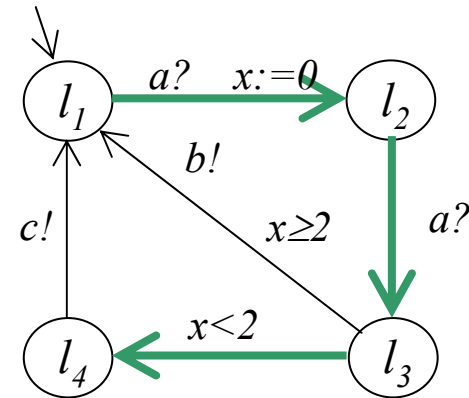
# Coverage Based Test Generation

- Multi purpose testing
- Cover measurement
- Examples:
  - ✱ Location coverage,
  - ✱ Edge coverage,
  - ✱ Definition/use pair coverage



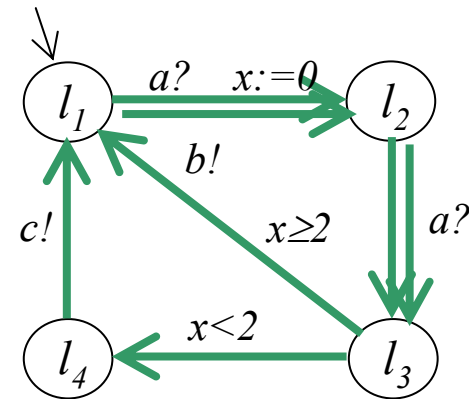
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# Coverage Based Test Generation

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  - ✱ Location coverage,
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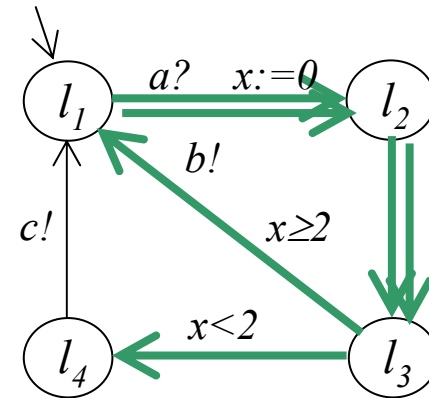
# Coverage Based Test Generation

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- Cover measurement
- Examples:

- ✱ Location Coverage,

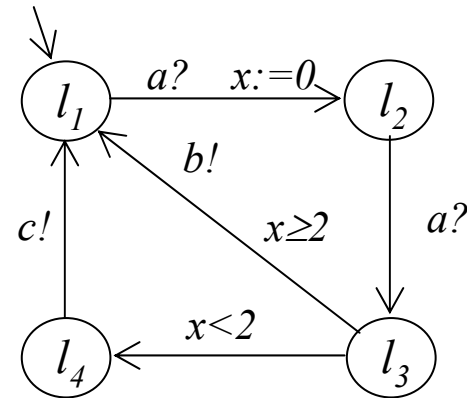
- ✱ Edge Coverage,

- ✱ **Definition/Use Pair Coverage**



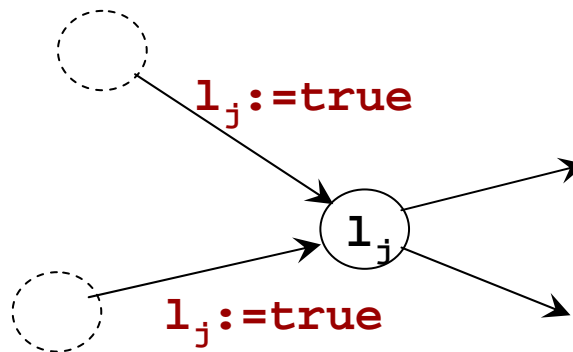
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- Multi purpose testing
- Cover measurement
- Examples:
  - ✱ Locations coverage,
  - ✱ Edge coverage,
  - ✱ Definition/use pair coverage
  - ✱ All Definition/Use pairs
- Generated by min-cost reachability analysis of annotated graph



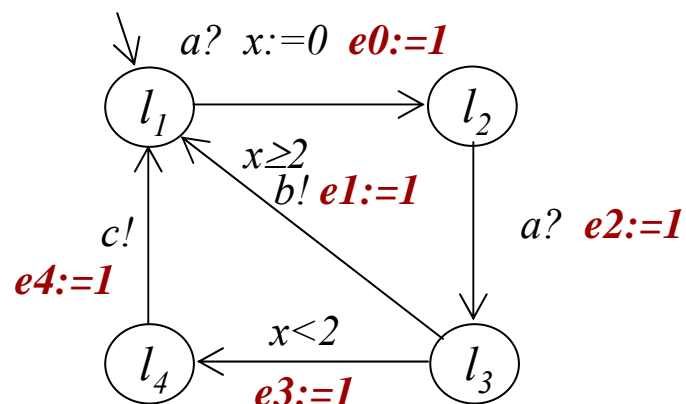
# Location Coverage

- Test sequence traversing all locations
- Encoding:
  - ✱ Enumerate locations  $l_0, \dots, l_n$
  - ✱ Add an auxiliary variable  $l_i$  for each location
  - ✱ Label each ingoing edge to location  $i$   $l_i := \text{true}$
  - ✱ Mark initial visited  $l_0 := \text{true}$
- Check: **EF**(  $l_0 = \text{true} \wedge \dots \wedge l_n = \text{true}$  )



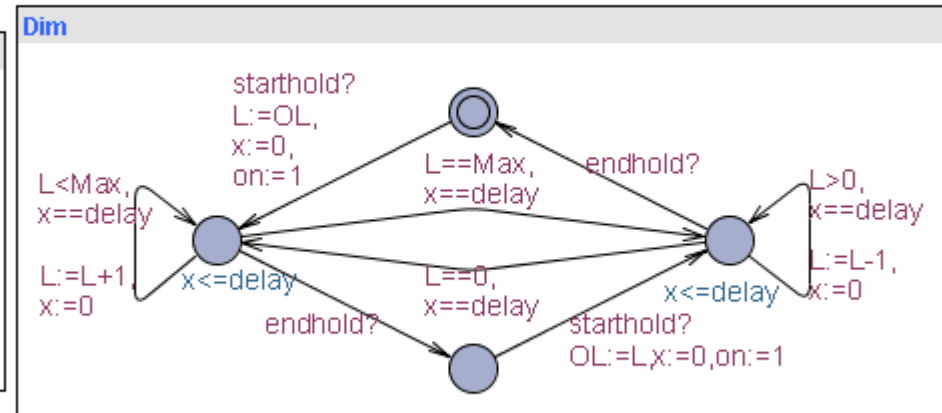
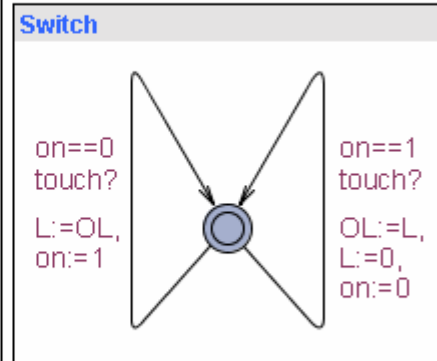
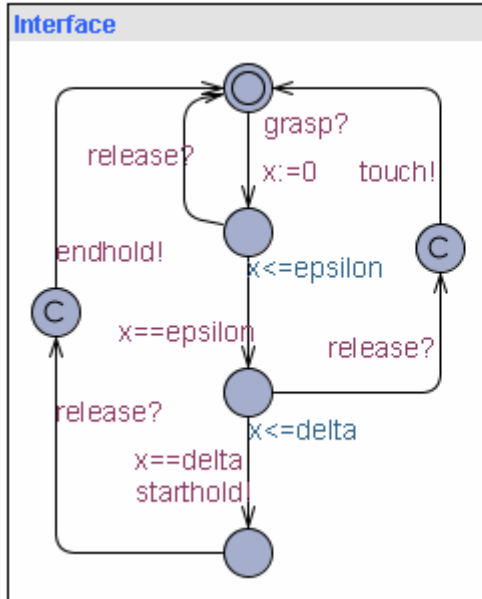
# Edge Coverage

- Test sequence traversing all edges
- Encoding:
  - ✱ Enumerate edges  $e_0, \dots, e_n$
  - ✱ Add auxiliary variable  $e_i$  for each edge
  - ✱ Label each edge  $e_i := \text{true}$
- Check: **EF**(  $e_0 = \text{true} \wedge \dots \wedge e_n = \text{true}$  )



# Fastest Edge Coverage

Cost=12600 ms



```

out(IGrasp);           //touch:switch light on
silence(200);
out(IRelease);
in(OSetLevel,0);

out(IGrasp); // @200 // touch: switch light off
silence(200);
out(IRelease); // touch
in(OSetLevel,0);

//9
out(IGrasp); // @400 //Bring dimmer from ActiveUp
silence(500); //hold //To Passive DN (level=0)
in(OSetLevel,0);
out(IRelease);
    
```

```

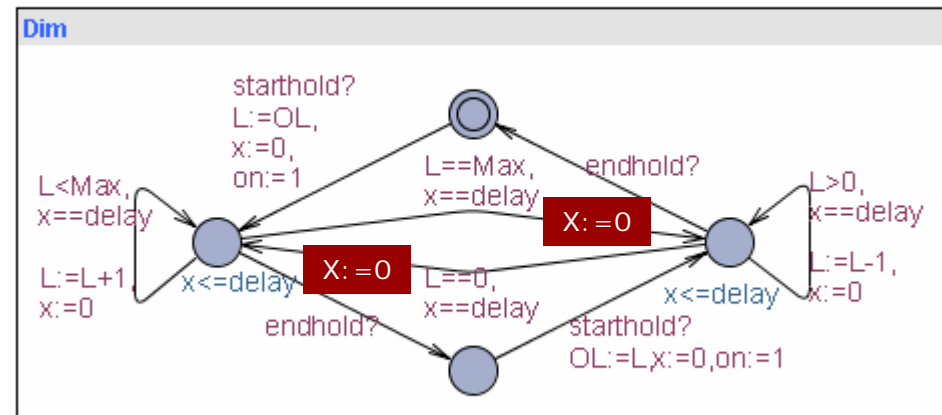
//13
out(IGrasp); // @900 // Bring dimmer PassiveDn->ActiveDN->
silence(500); //hold // ActiveUP+increase to level 10
silence(1000); in(OSetLevel,1);
silence(1000); in(OSetLevel,2);
silence(1000); in(OSetLevel,3);
silence(1000); in(OSetLevel,4);
silence(1000); in(OSetLevel,5);
silence(1000); in(OSetLevel,6);
silence(1000); in(OSetLevel,7);
silence(1000); in(OSetLevel,8);
silence(1000); in(OSetLevel,9);
silence(1000); in(OSetLevel,10)
silence(1000); in(OSetLevel,9); //bring dimm State to ActiveDN

out(IRelease); //check release->grasp is ignored
out(IGrasp); // @12400
out(IRelease);
silence(dFTolerance);
    
```

# Mutants

- M1 incorrectly implements switch

```
synchronized public void handleTouch()  
    if(lightState==lightOff) {  
        setLevel(oldLevel);  
        lightState=lightOn;  
    }  
    else { //was missing  
if(lightState==lightOn){  
    oldLevel=level;  
    setLevel(0);  
    lightState=lightOff;  
}  
}
```



- M2 incorrect additional delay in dimmer as if x:=0 was on ActiveUP ↔ActiveDN transitions

# Outcome

Description	Test#	M0	M1	M2
MaxLevel	1	pass	pass	pass
Short ActiveDn	4	pass	pass	pass
Resume	5	Pass	Fail	pass
Edge Cov.	3	Pass	Fail	Fail

# Online Testing Of Real-Time Systems

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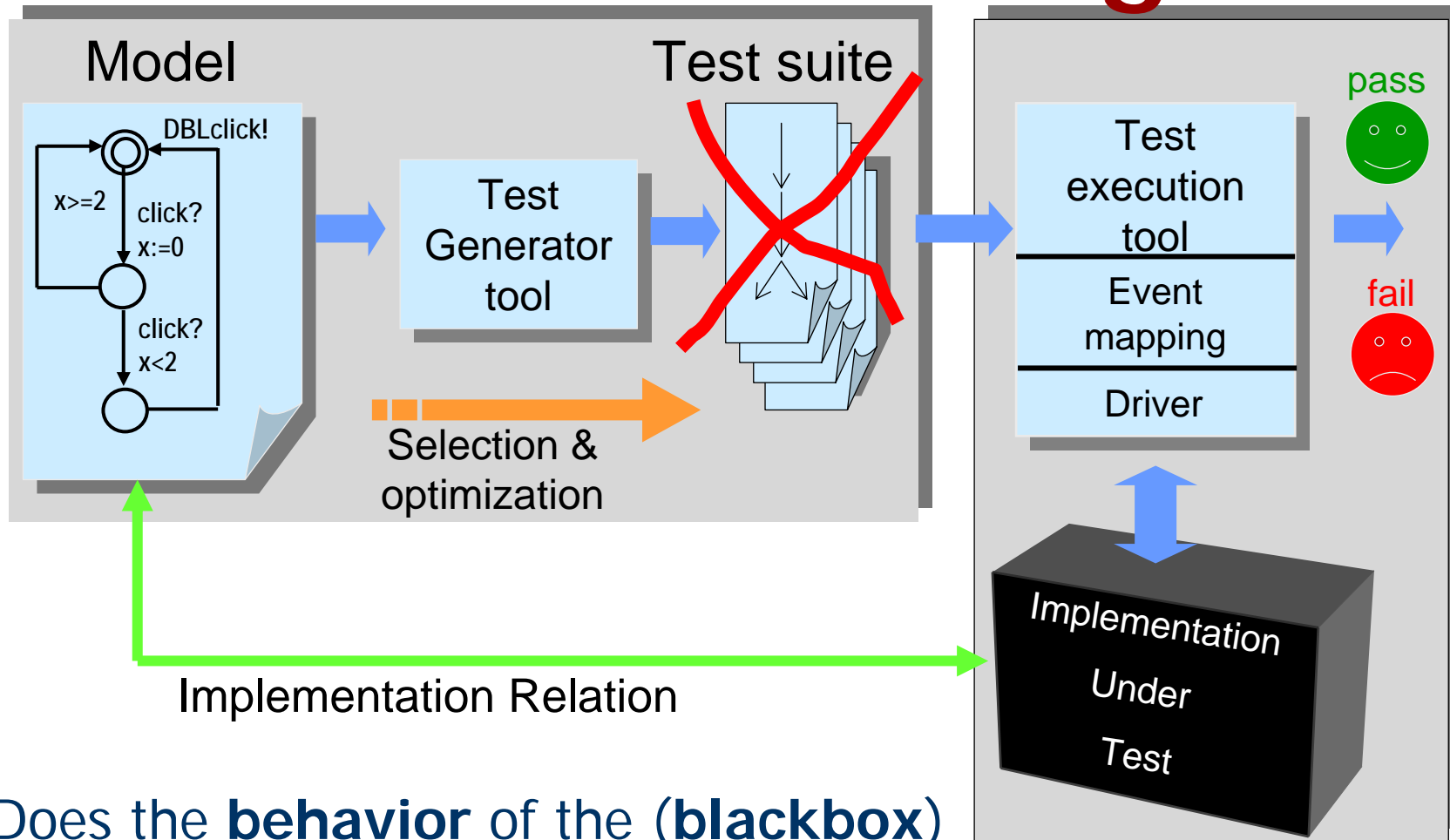
**BRICS**

Basic Research  
in Computer Science



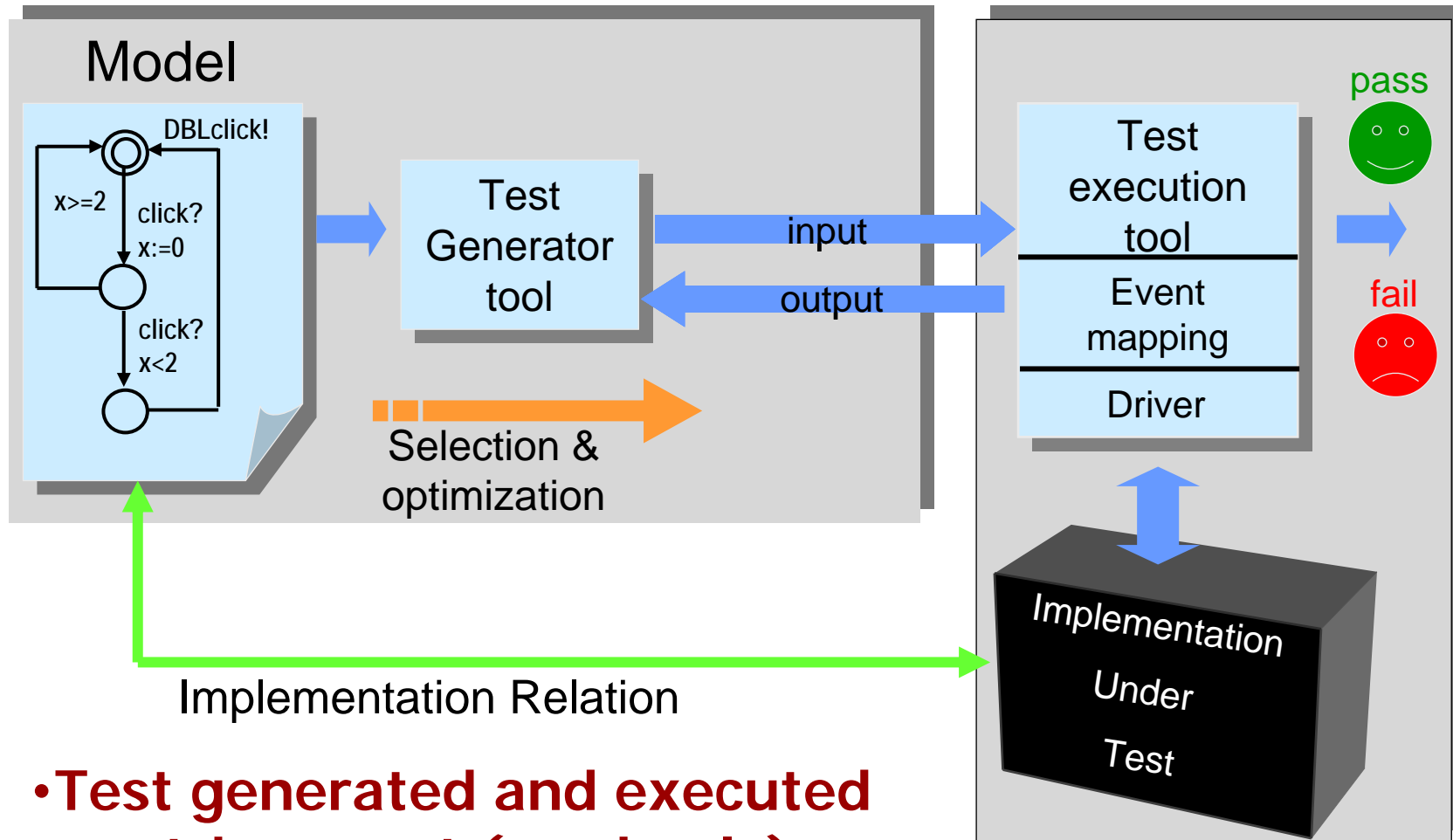
**CENTER FOR INDLEJREDE SOFTWARE SYSTEMER**

# Automated Model Based Conformance Testing



Does the **behavior** of the (**blackbox**) implementation *comply* to that of the specification?

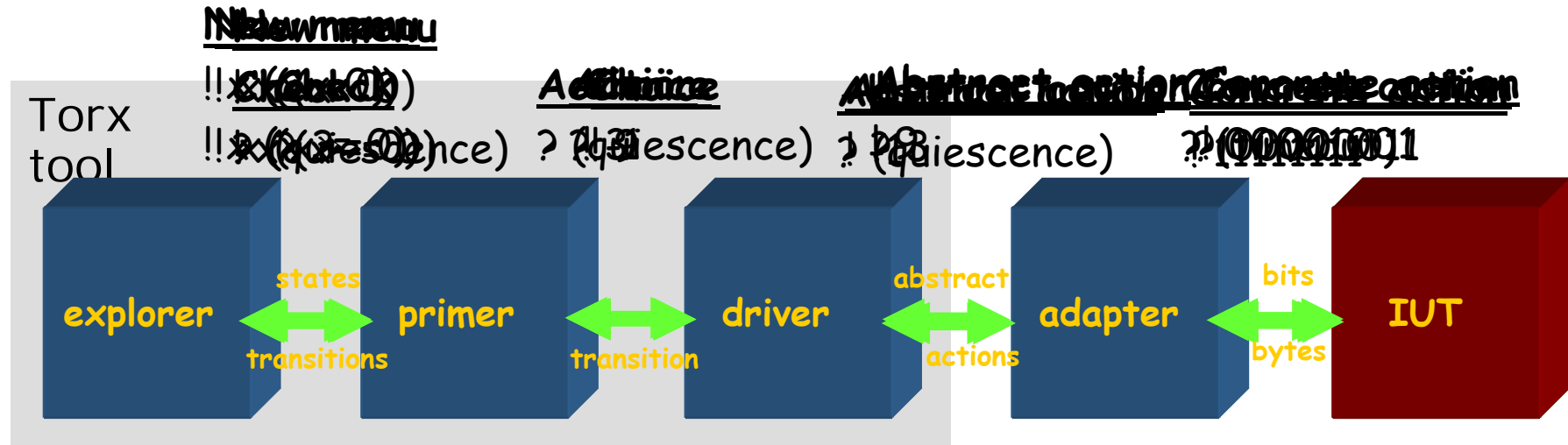
# Online Testing



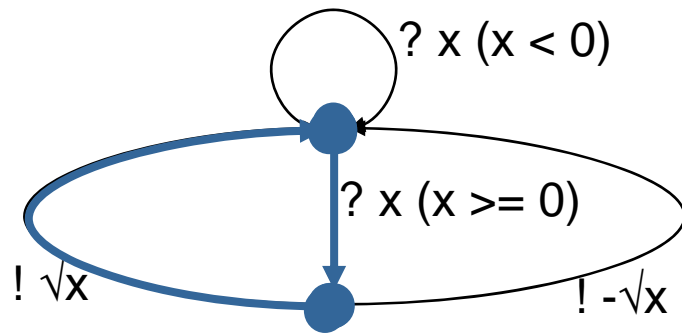
- Test generated and executed event-by-event (randomly)

- A.K.A on-the-fly testing

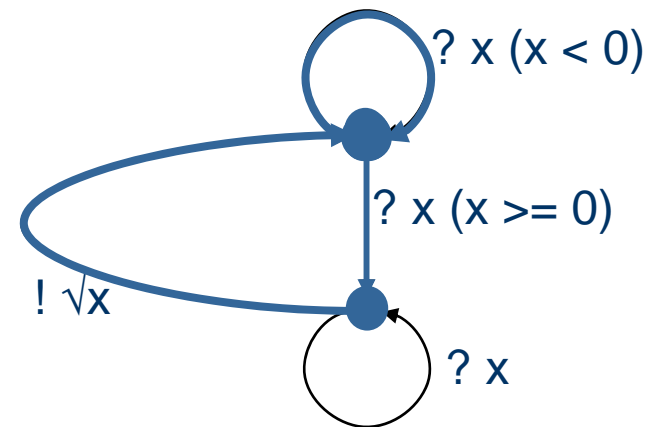
# On-The-Fly Testing



specification



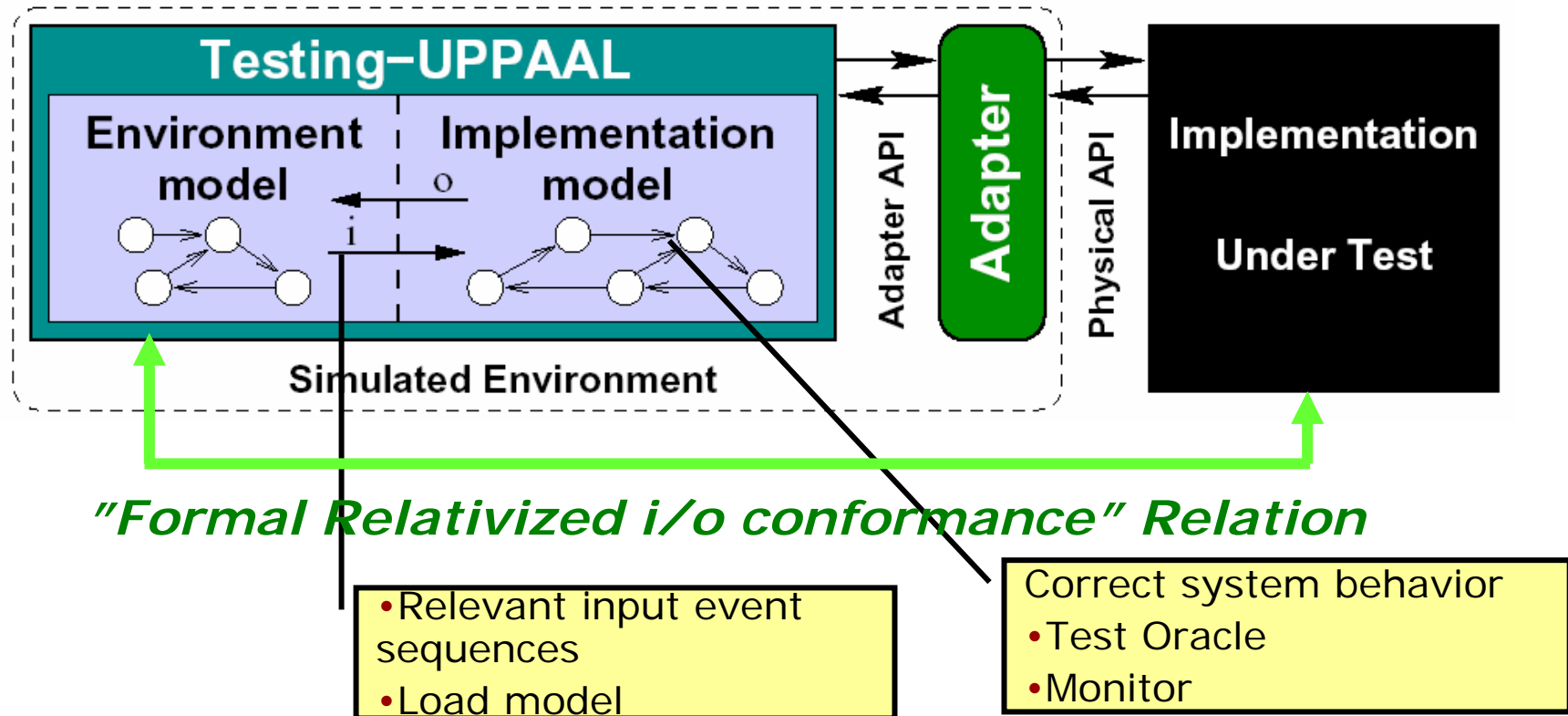
implementation



[Jan Tretmans].

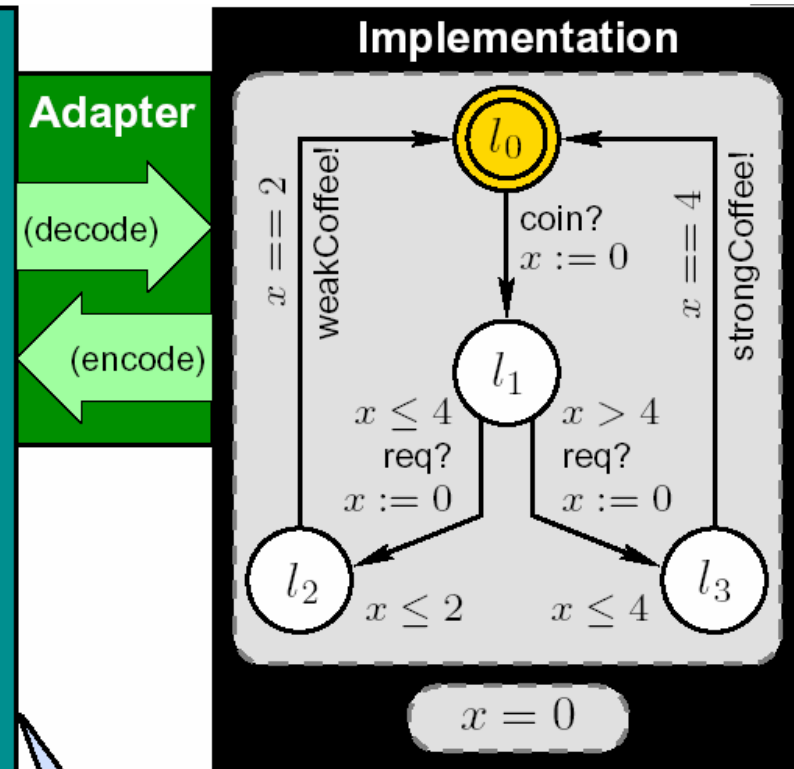
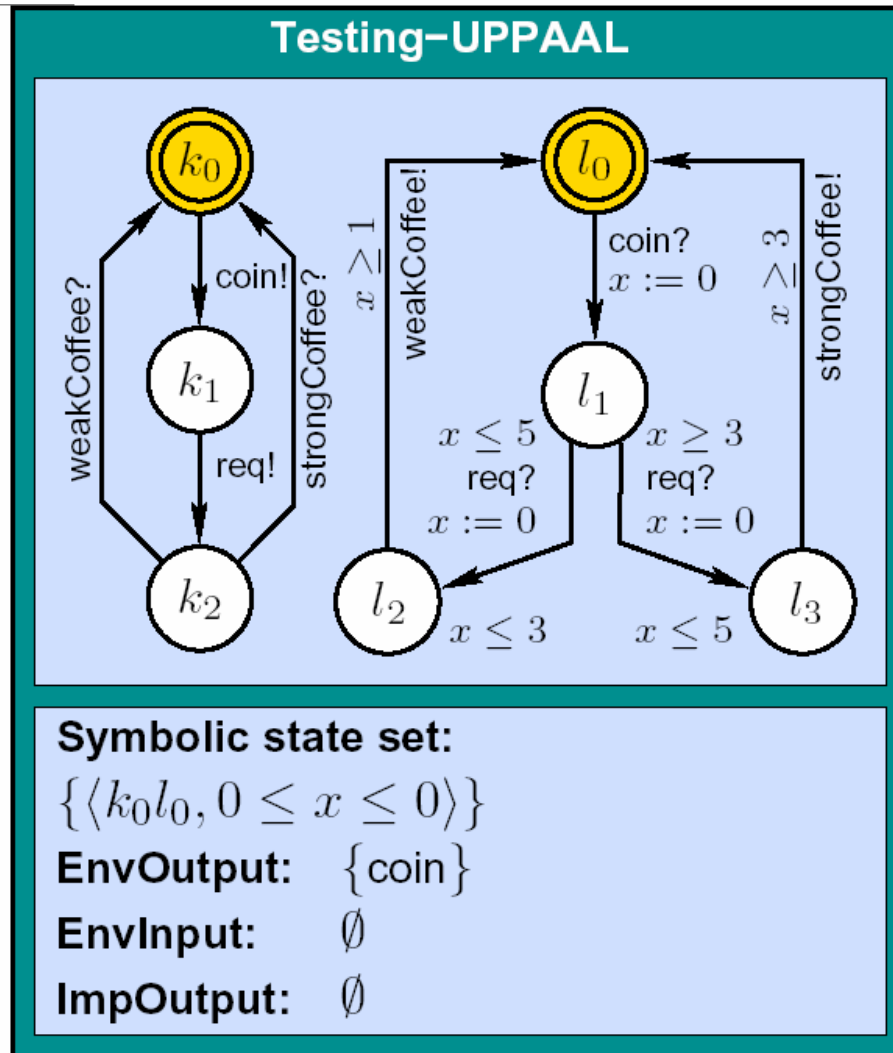
# Our Framework

- *UppAal Timed Automata Network: Env || IUT*



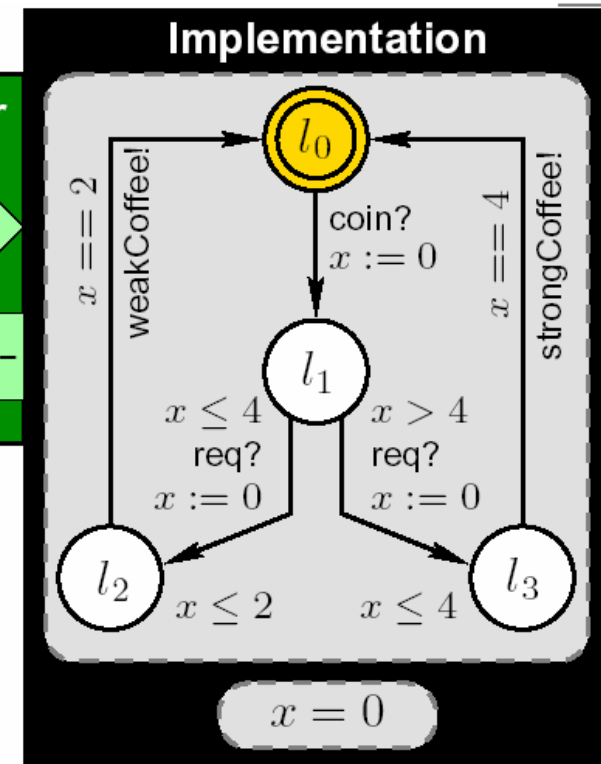
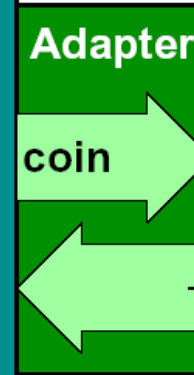
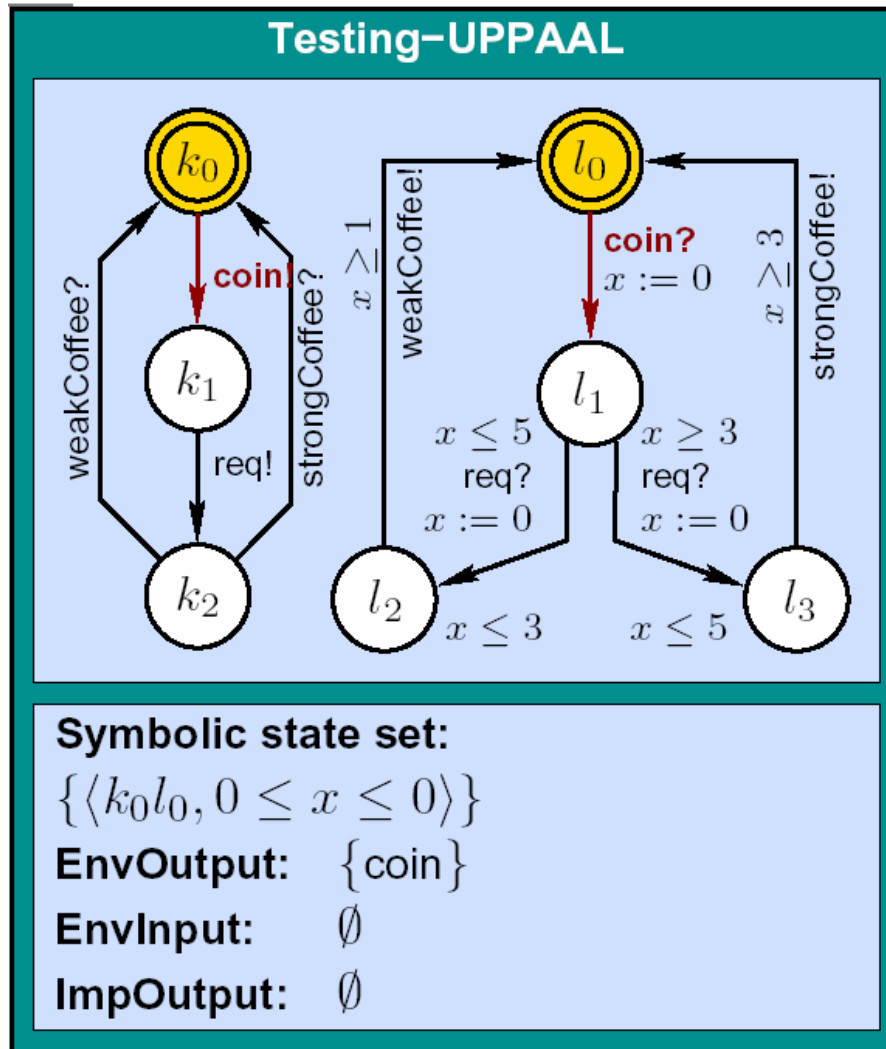
- *Complete and sound algorithm*
- *Efficient symbolic reachability algorithms*
- **UppAal-TRON:** Testing Real-Time Systems Online
- Release 1.3 <http://www.cs.aau.dk/~marius/tron/>

# Online Testing



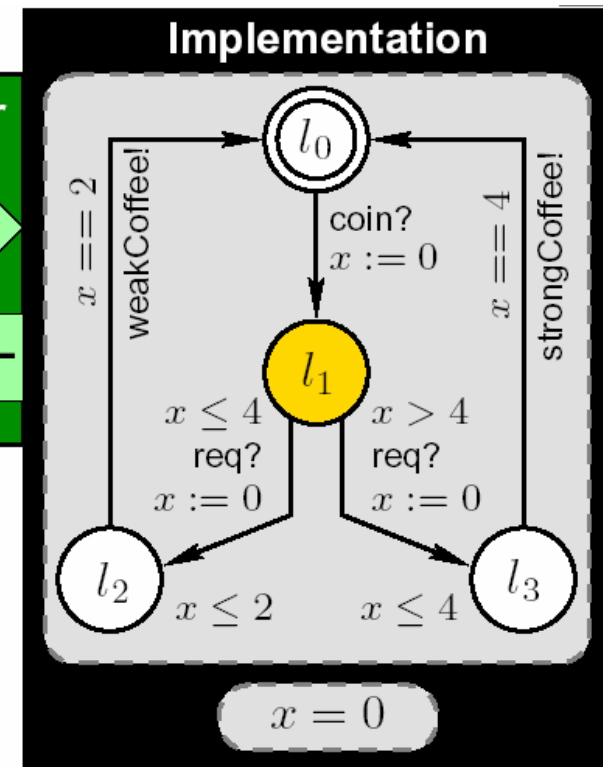
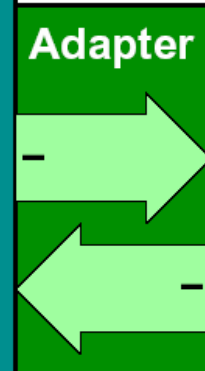
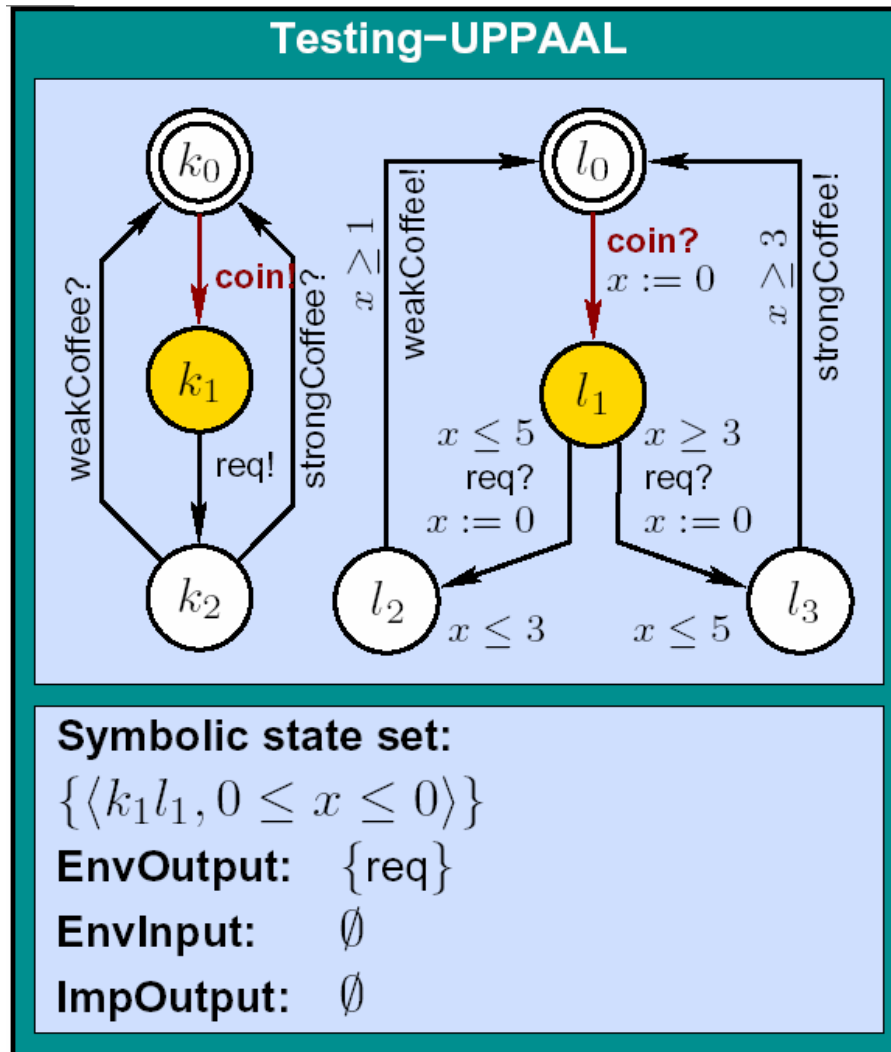
Wait for output (delay) or offer input?

# Online Testing



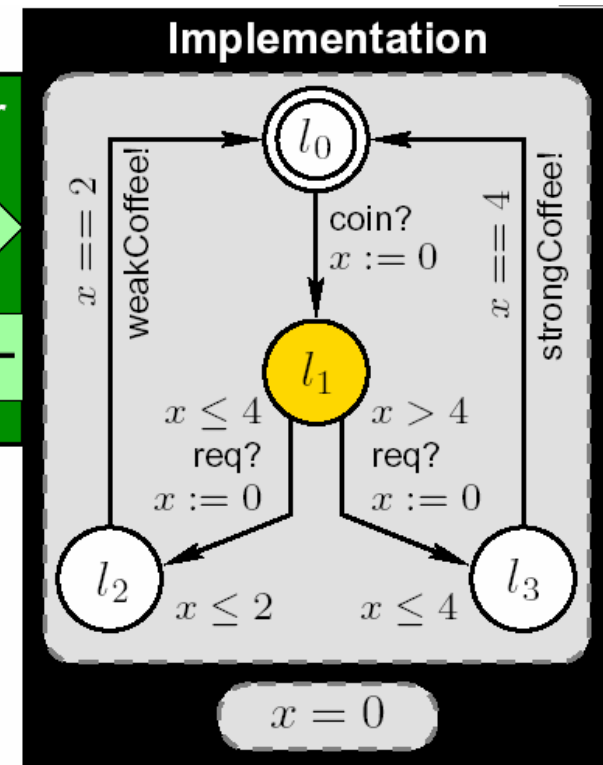
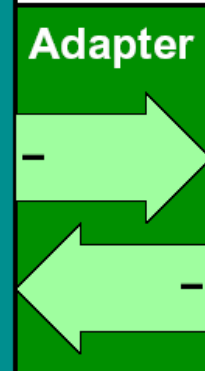
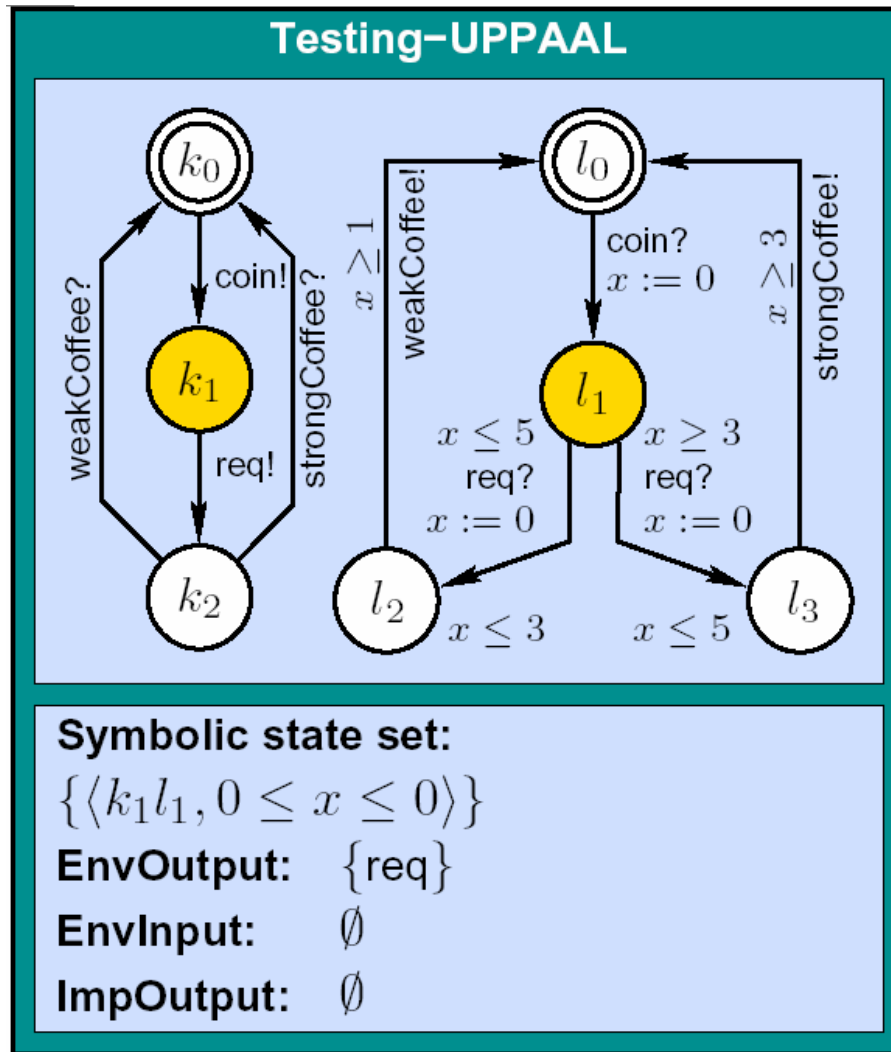
Let's offer input  
choose (the only) "coin"

# Online Testing



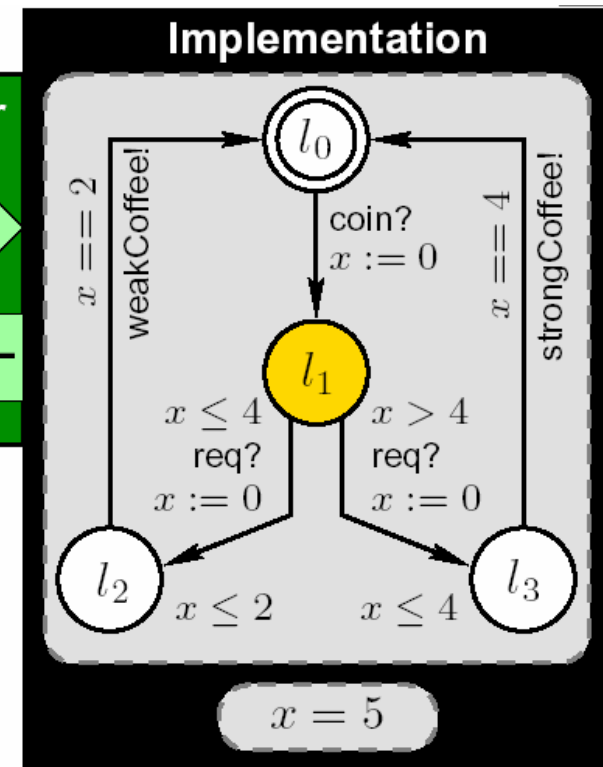
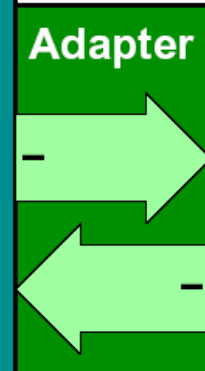
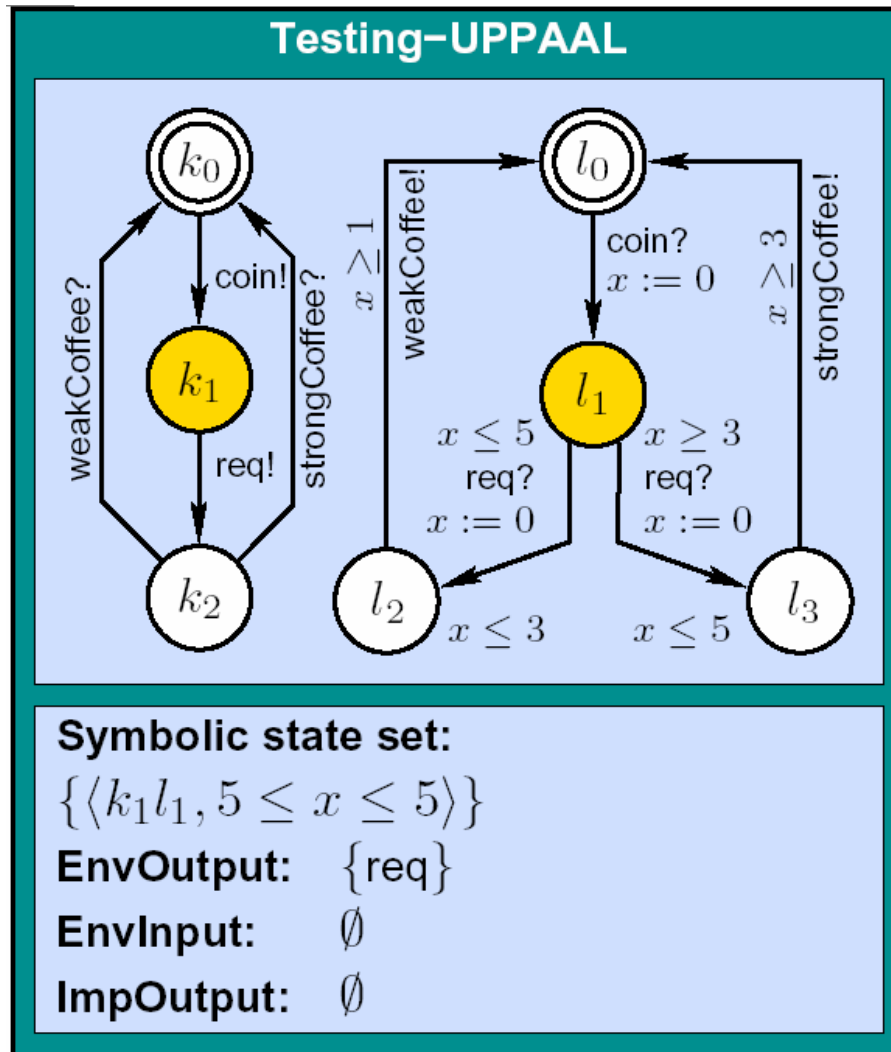
**Update the state set and other variables**

# Online Testing



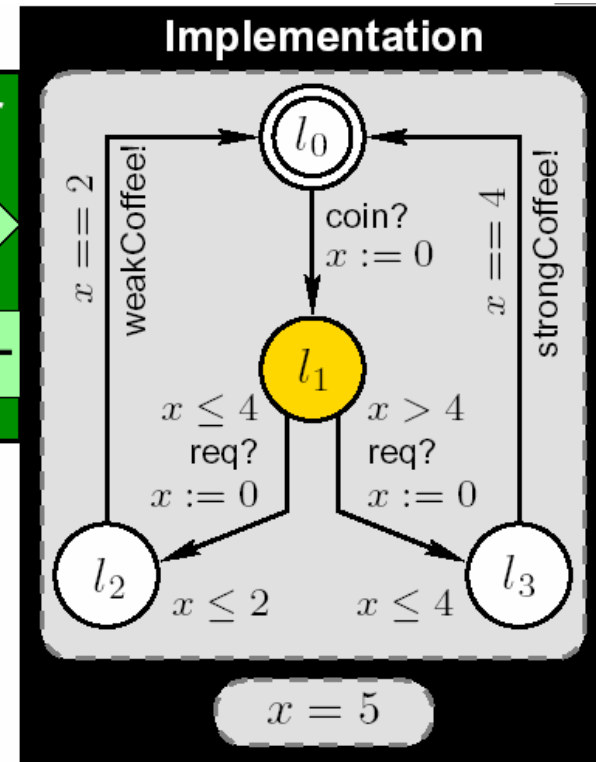
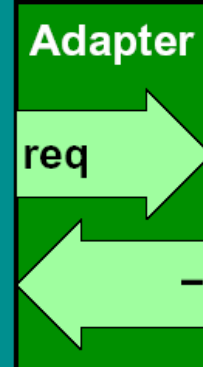
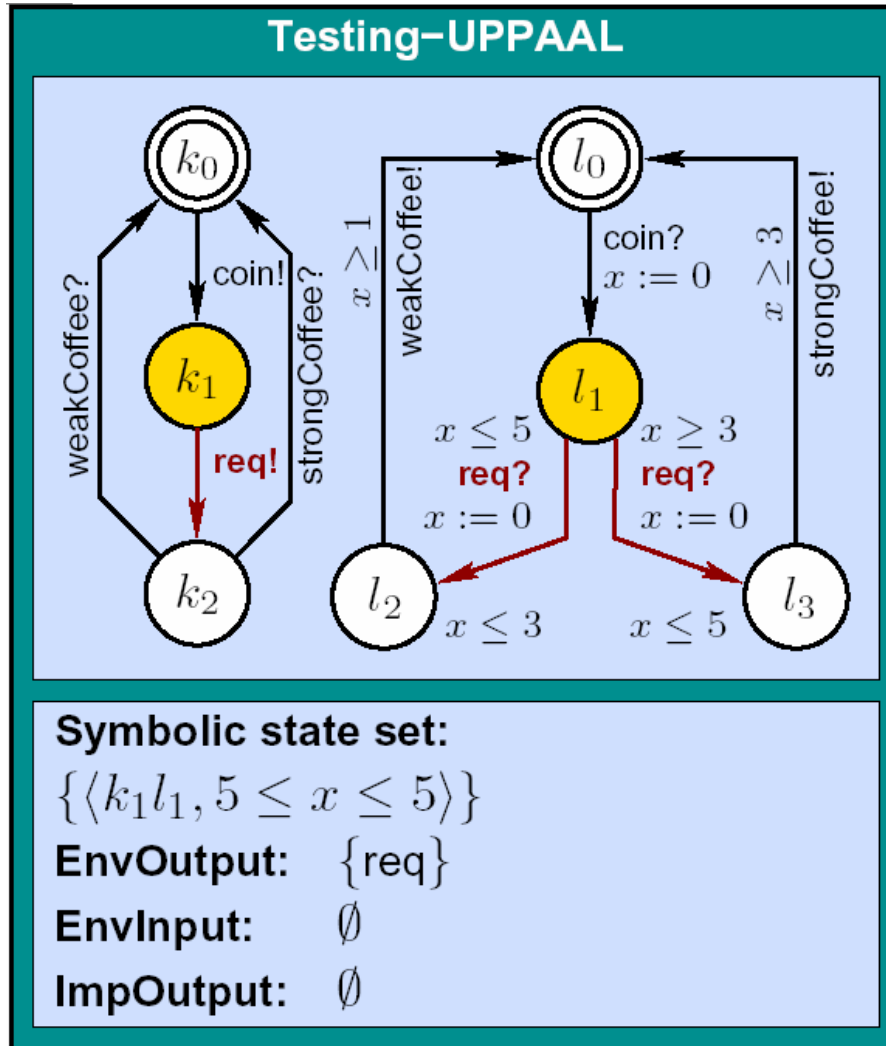
Wait or offer input?  
 Let's wait for 5 units

# Online Testing



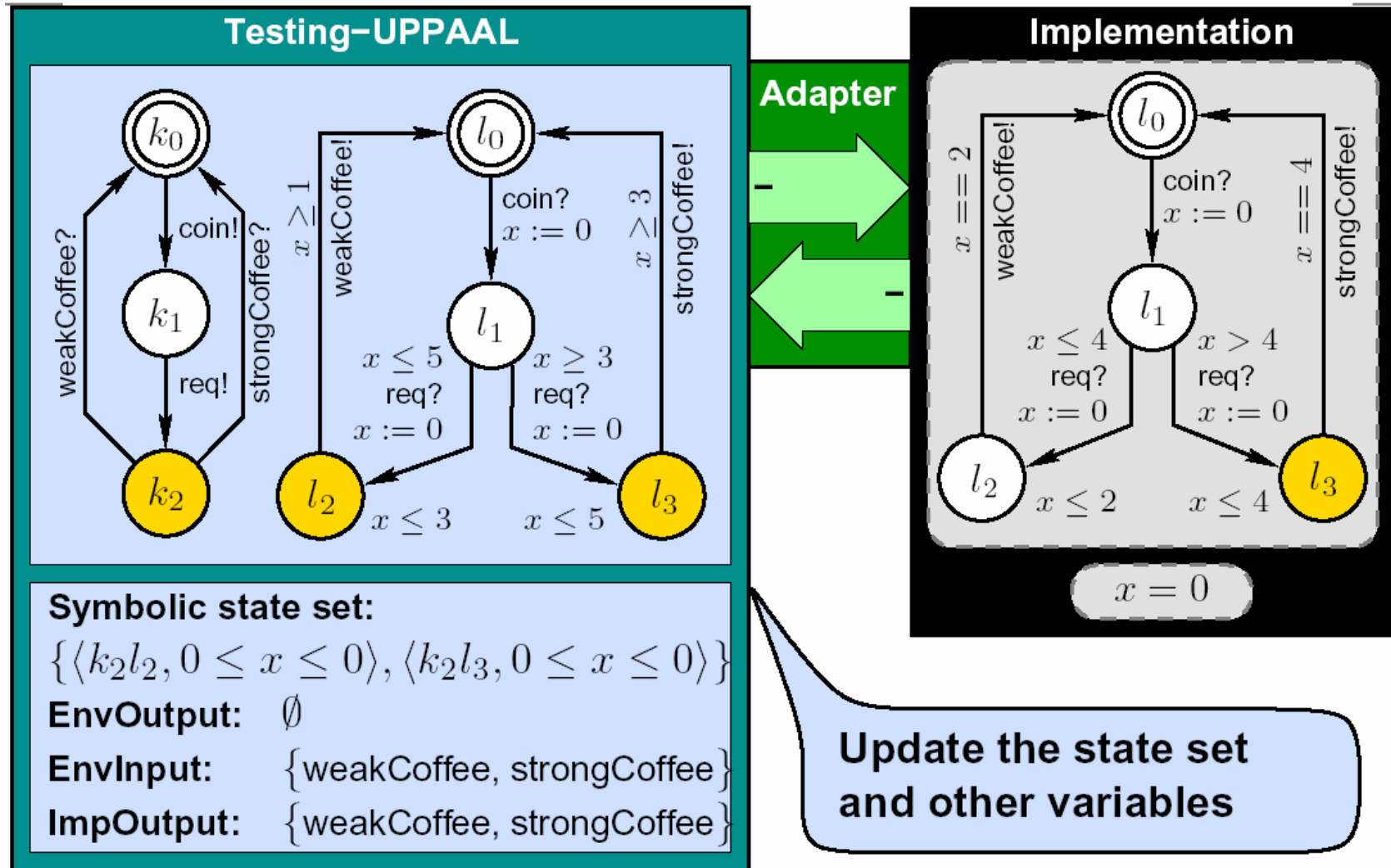
..no output so far:  
update the state set..

# Online Testing

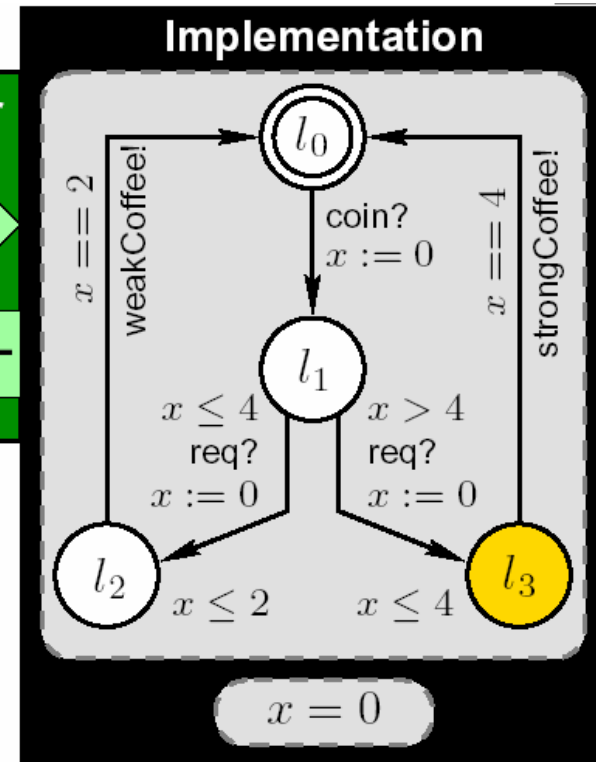
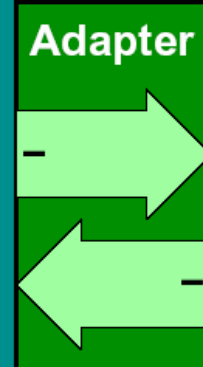
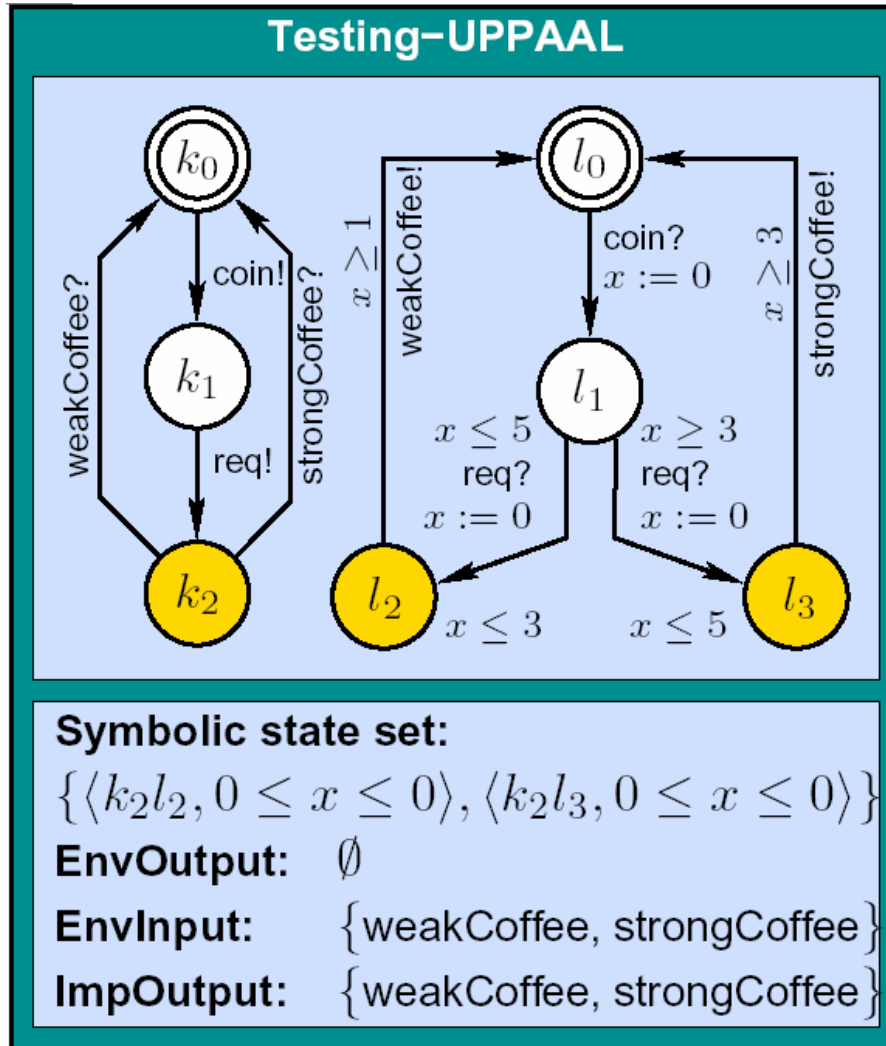


**Wait or offer input?  
let's offer "req"**

# Online Testing

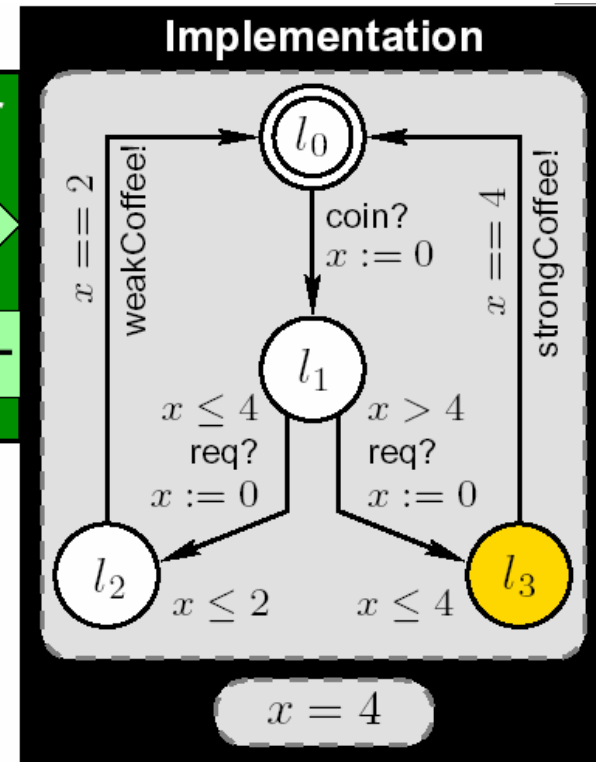
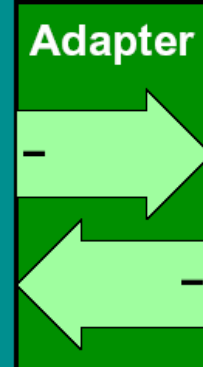
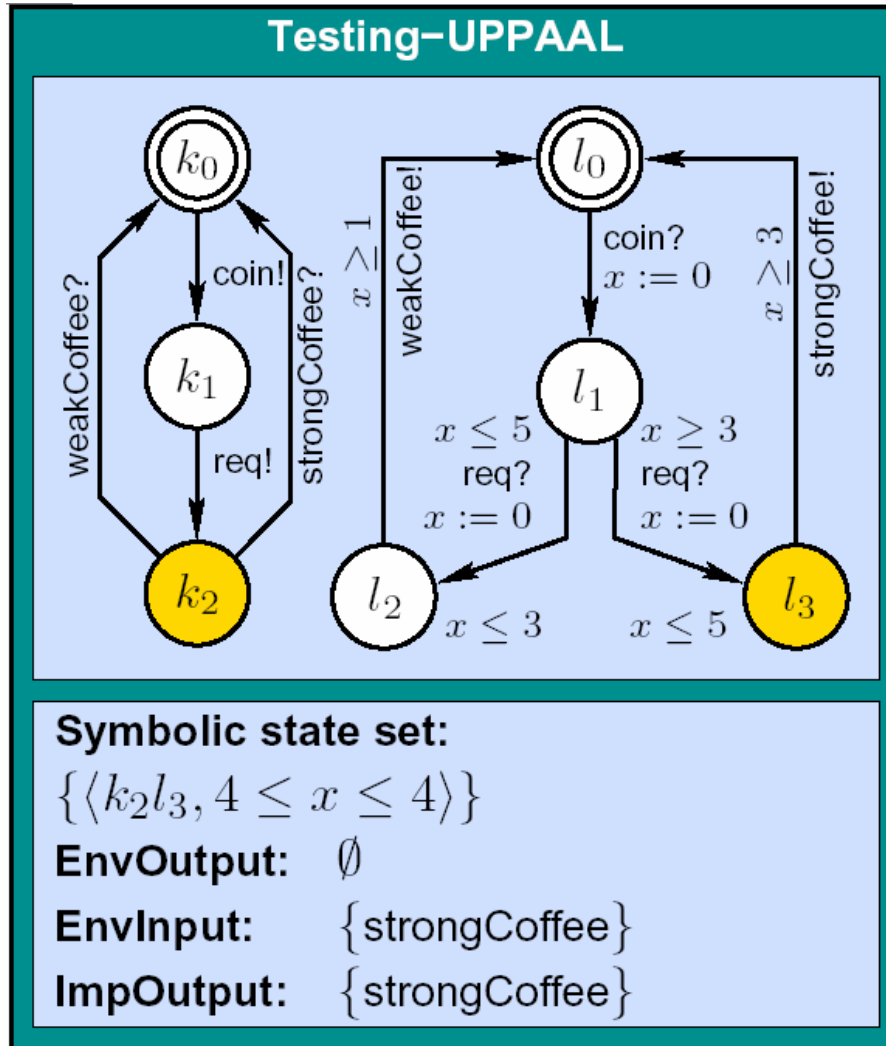


# Online Testing



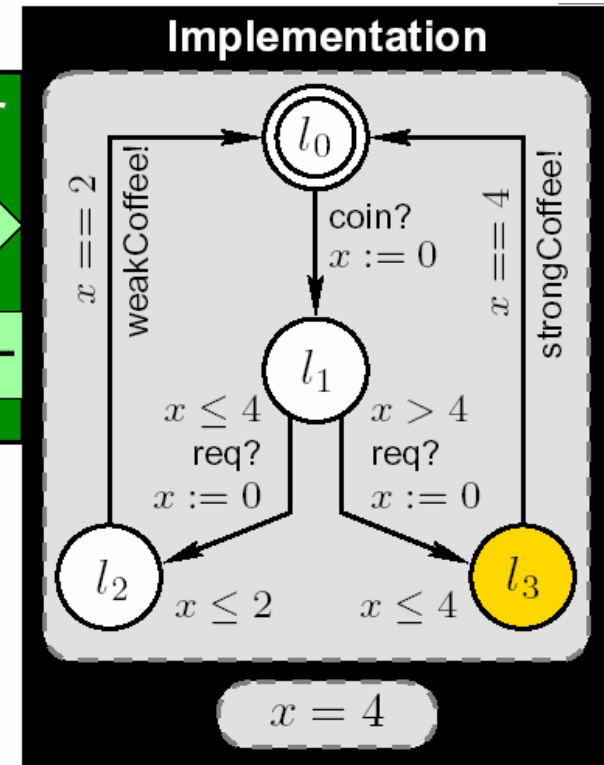
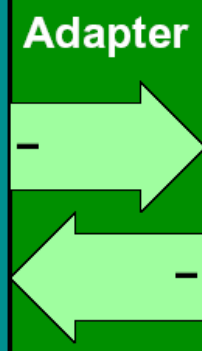
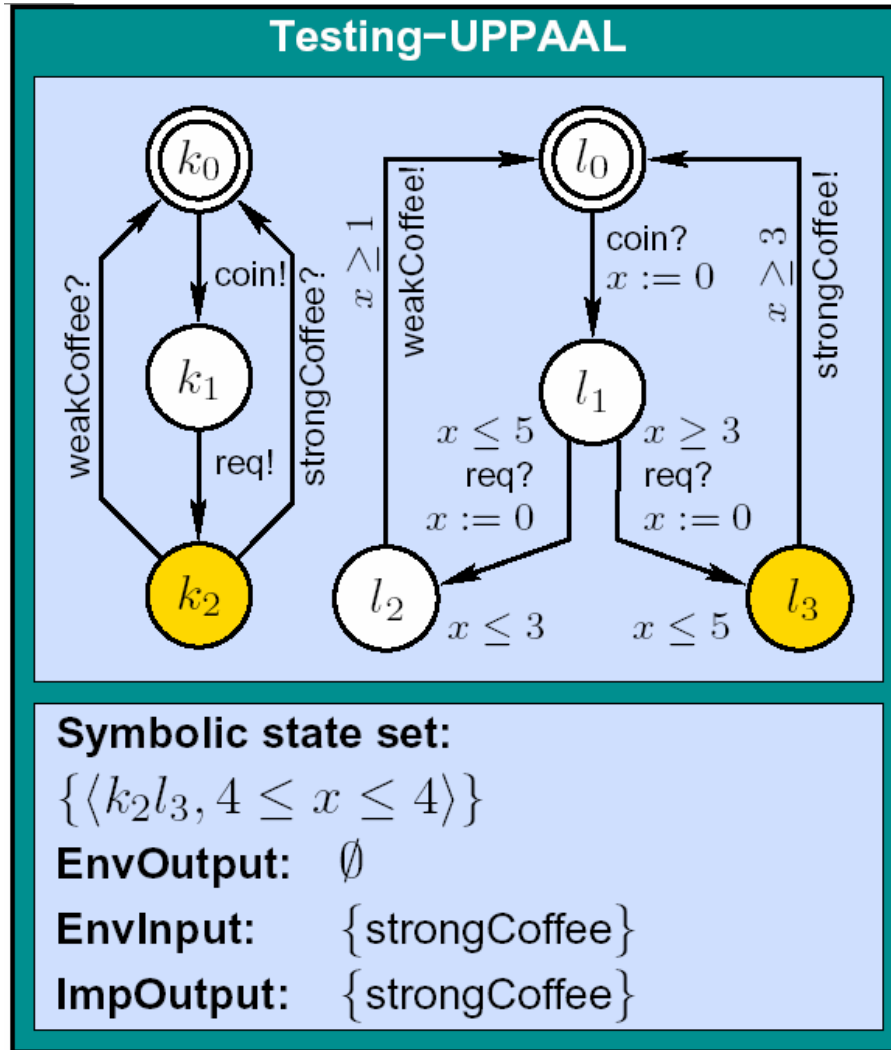
**Wait or offer input?  
Let's wait for 4 units**

# Online Testing



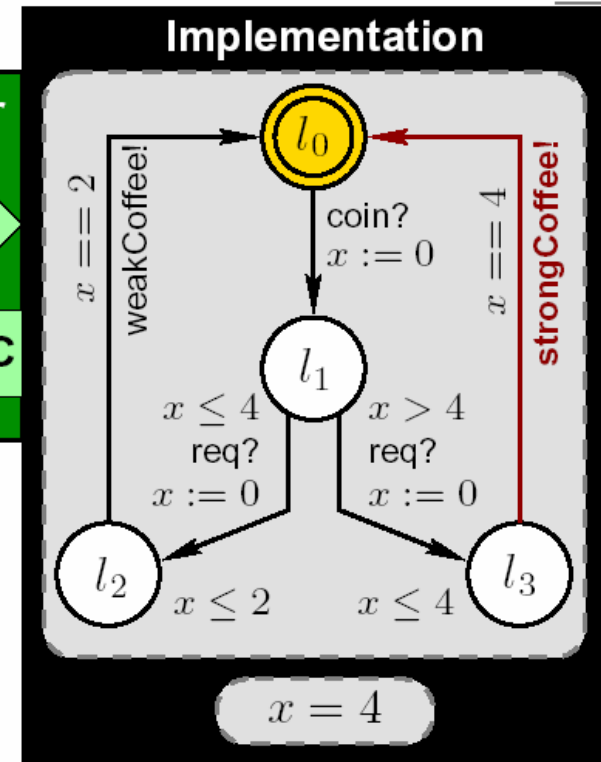
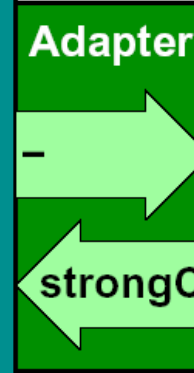
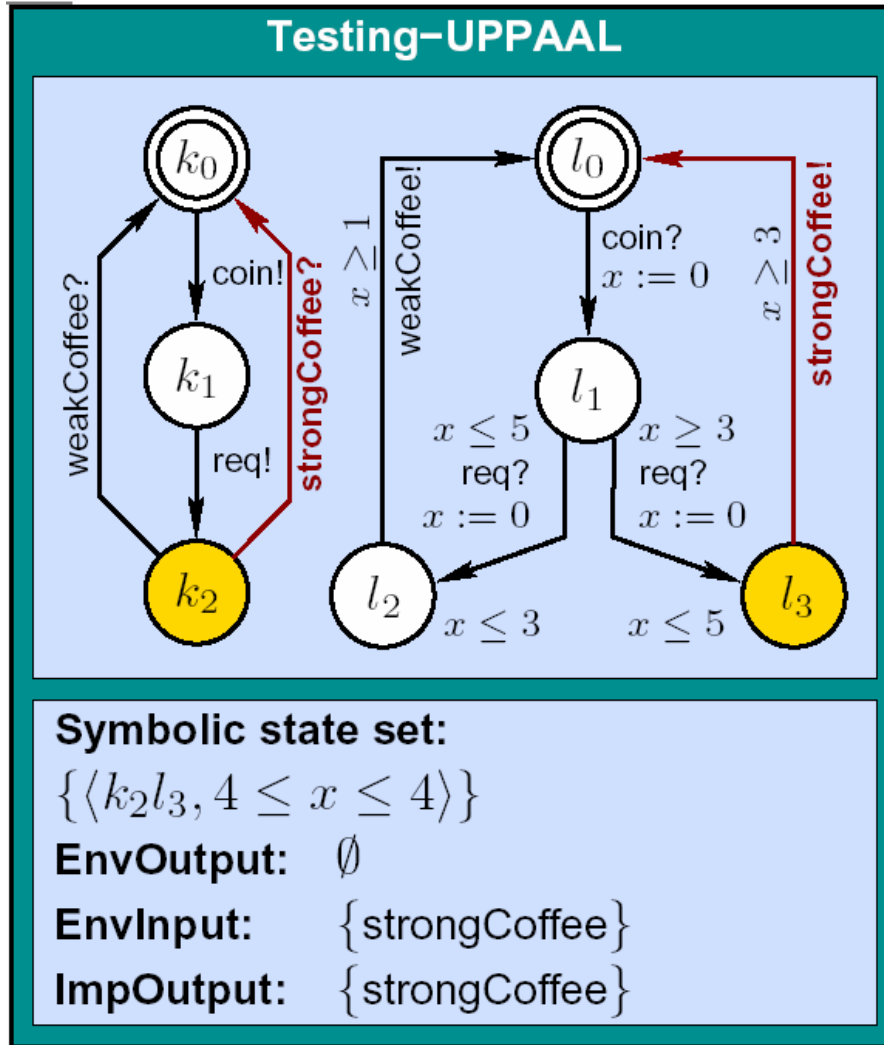
..no output so far:  
update the state set..

# Online Testing



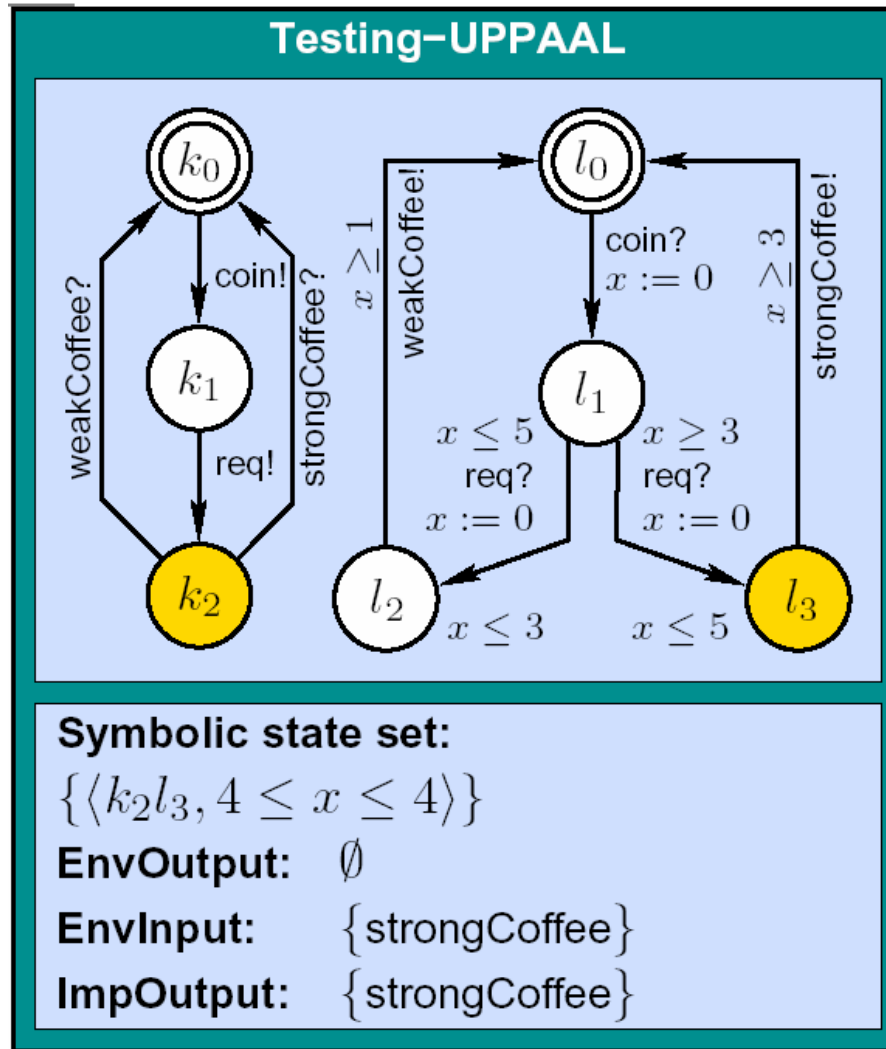
**Wait or offer input?  
Let's wait for 2 units**

# Online Testing

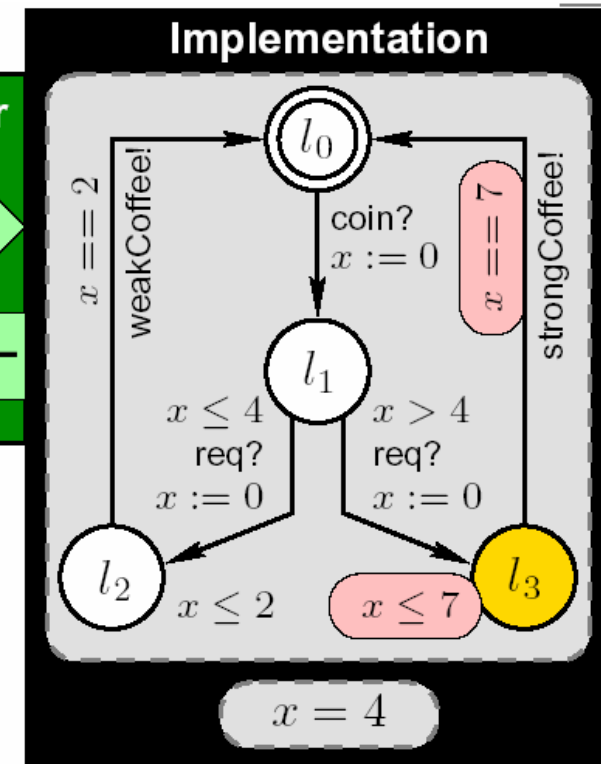
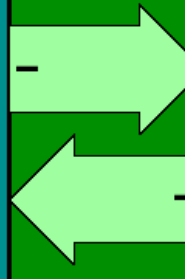


got output after 0 delay:  
update the state set

# Online Testing

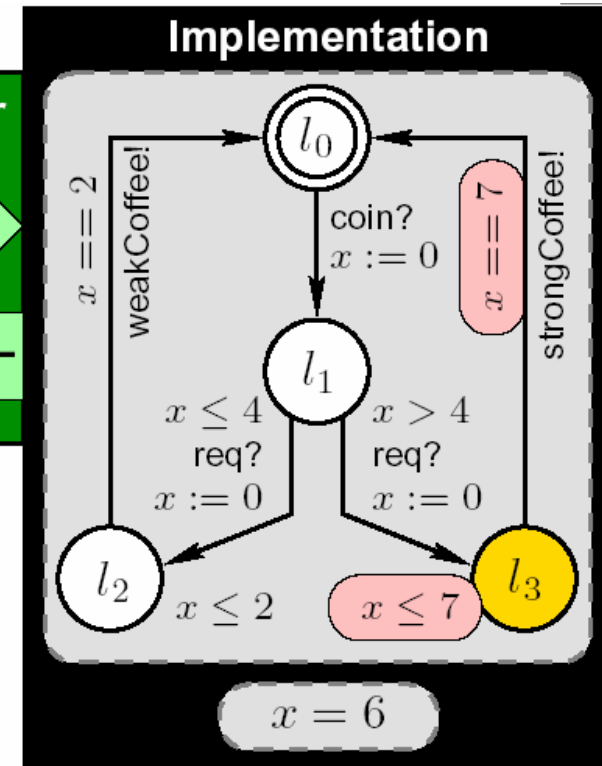
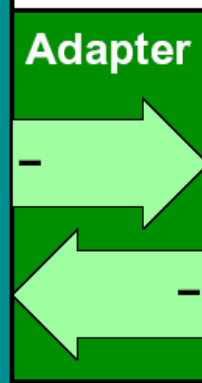
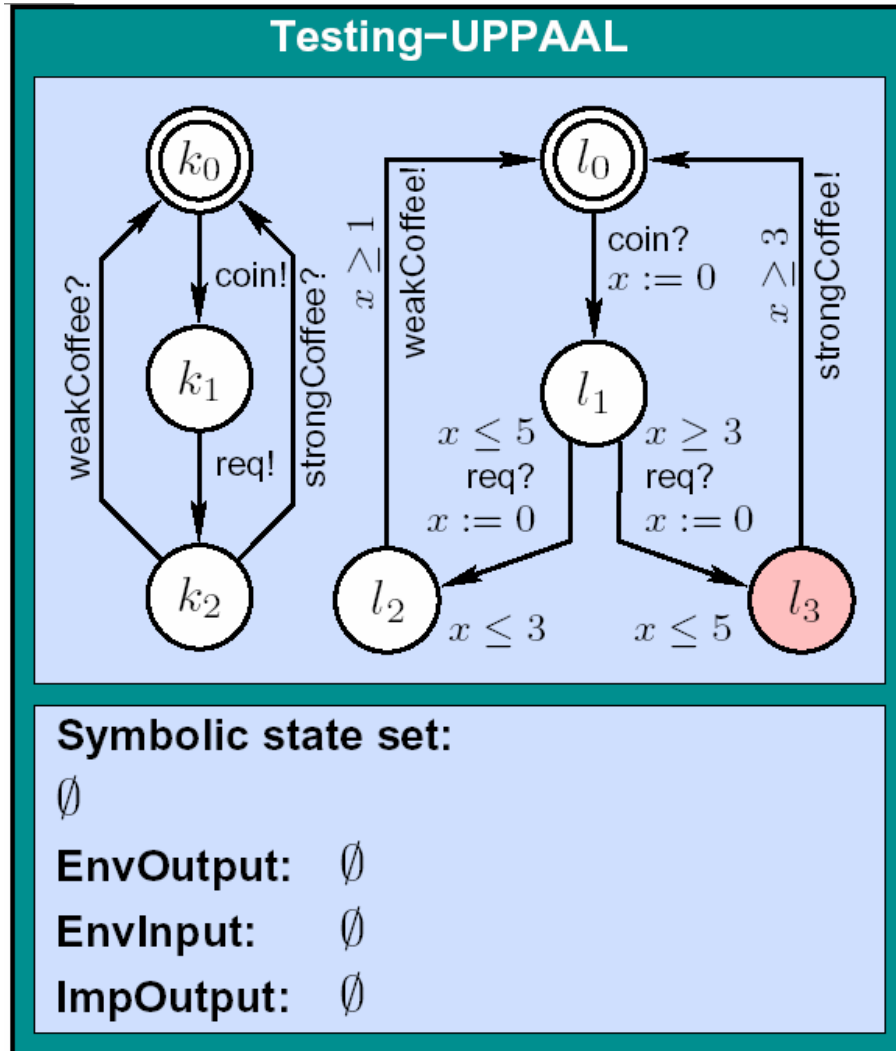


Adapter



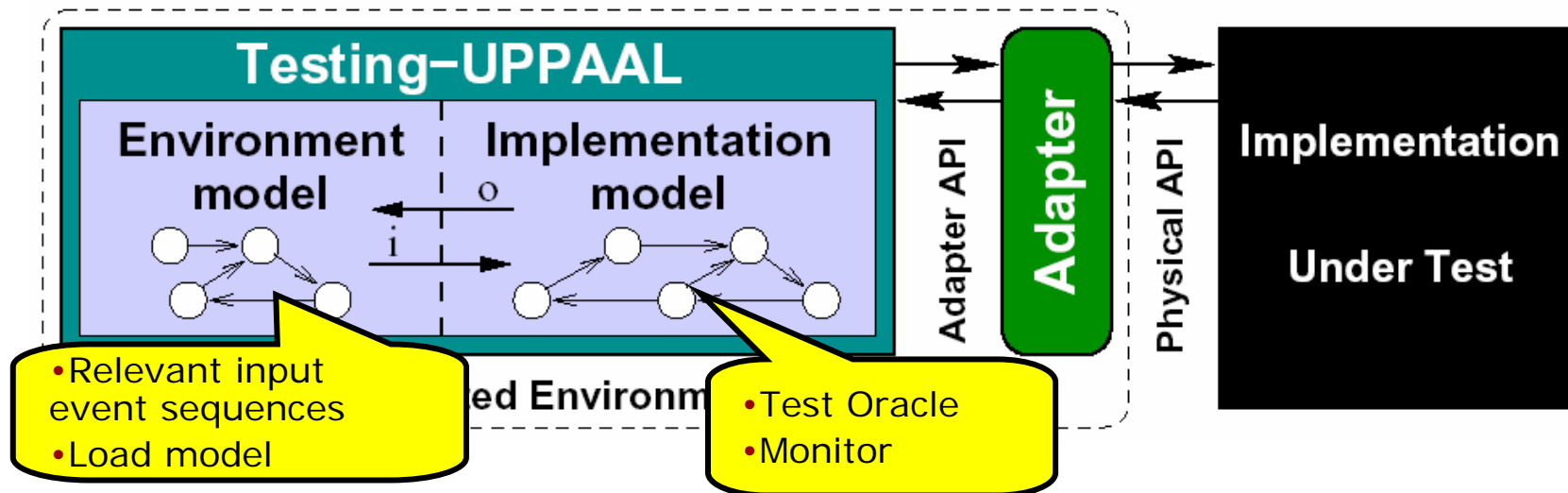
(what if there is a bug?)  
 Let's wait for 2 units

# Online Testing



..no output so far:  
update the state set.. (!)

# Test Specification



## *User Supplied Test Specification*

- *Closed TA Network* partitioned into *Env* and *IUT*.
  - IUT model weakly input input enabled
  - Model of *Environment*
- Designate *observable input* and *output* actions.
- Specify amount of real time per one time-unit in model.

# Online Algorithm

Algorithm *TestGenExec* (*TestSpec*) returns {**pass**, **fail**, **inconclusive**}

$S := \{\langle I_0, 0 \rangle\}$ , *continueTesting* := true

**While** *continueTesting* **do either**

1.  $i := \text{ChooseAction}(\text{EnvOutput}(S))$  // Offer an input

**send**  $i$  to SUT

$Z := \text{After}(S, i)$

2.  $\delta = \text{chooseDelay}(S)$  // Delay and wait for output

**Wait**( $\delta$ )

**if**  $o$  occurred after  $\delta' \leq \delta$  **then**

$S := \text{After}(S, \delta')$

1. **if**  $o \notin \text{ImpOutput}(S)$  **then return fail**

2. **if**  $o \notin \text{EnvInput}(S)$  **then return inconclusive**

$S := \text{After}(S, o)$

**else**

// no output within  $\delta$  time

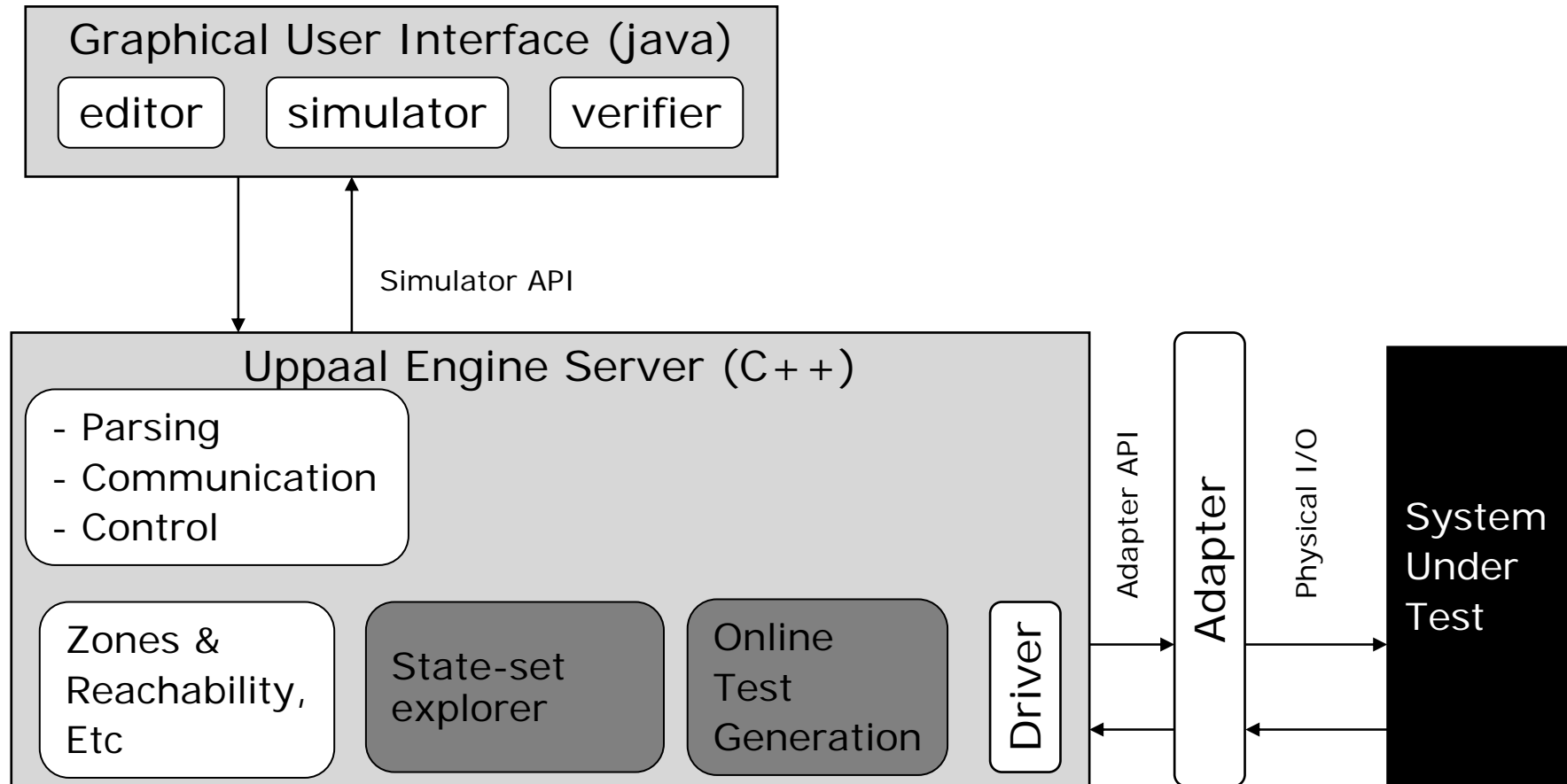
$S := \text{After}(S, \delta)$

**if**  $S = \emptyset$  **then return fail**

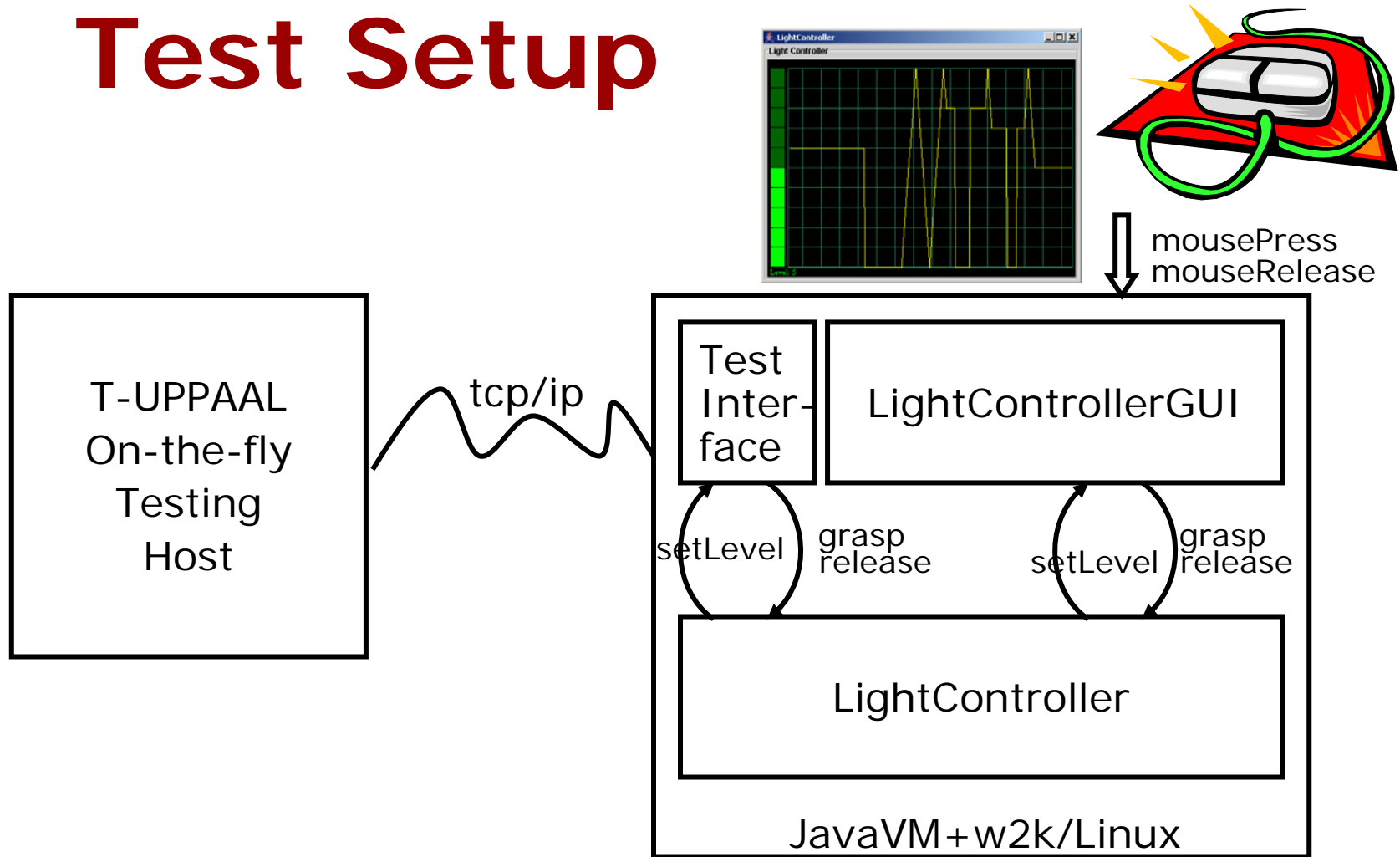
3. *continueTesting* := false // terminate

**return pass**

# T-UppAal: implementation



# Test Setup



# Danfoss EKC Case

## Electronic Cooling Controller



### Sensor Input

- air temperature sensor
- defrost temperature sensor
- (door open sensor)

### Keypad Input

- 2 buttons (~40 user settable parameters)

### Output Relays

- compressor relay
- defrost relay
- alarm relay
- (fan relay)

### Display Output

- alarm / error indication
- mode indication
- current calculated temperature

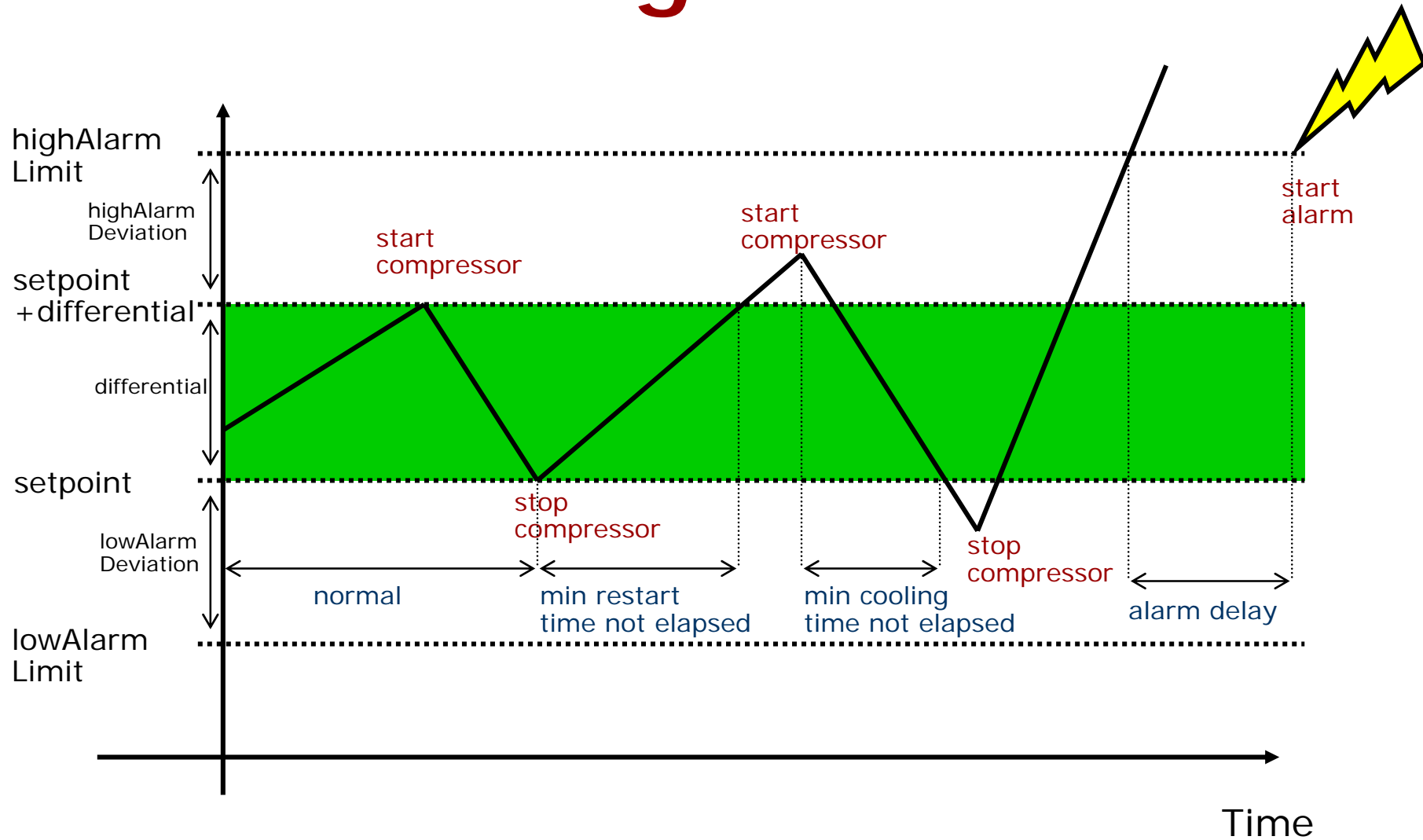


- Optional real-time clock or LON network module

# Industrial Cooling Plants

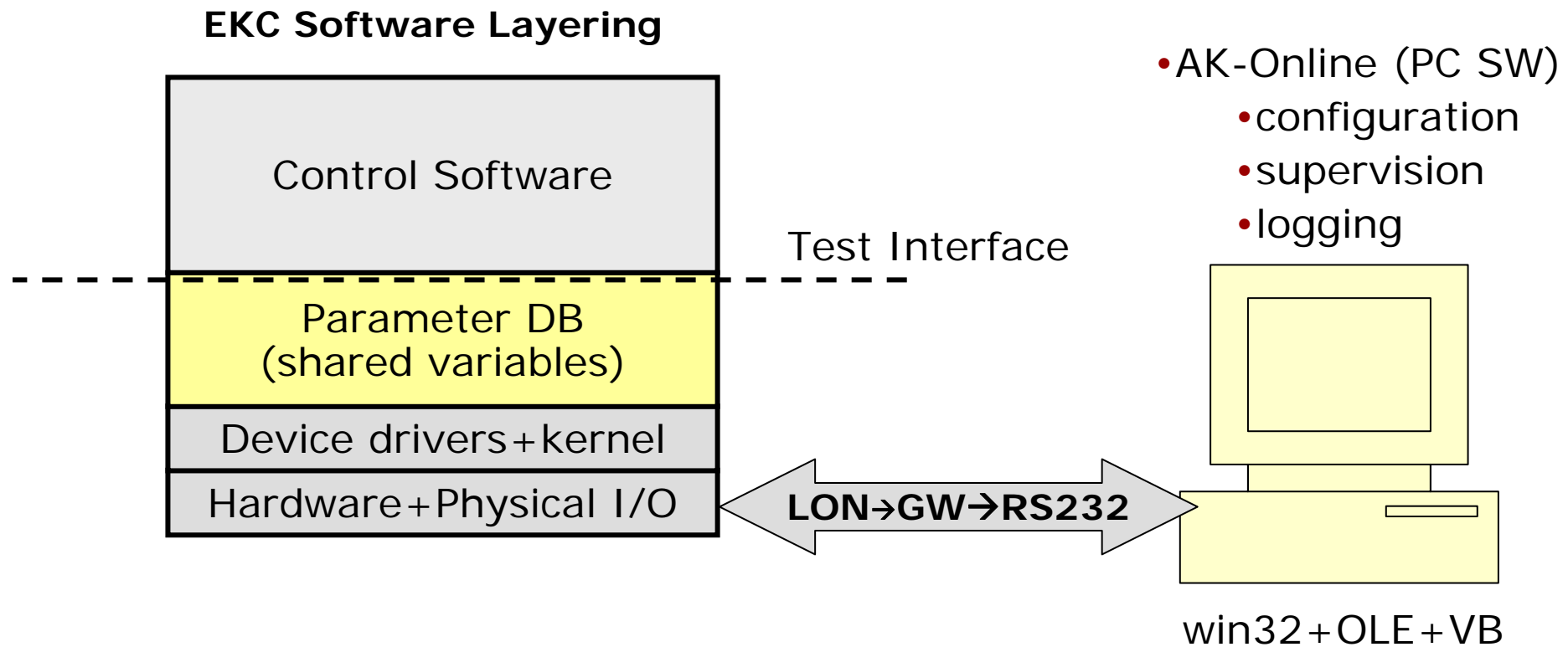


# Basic Refrigeration Control

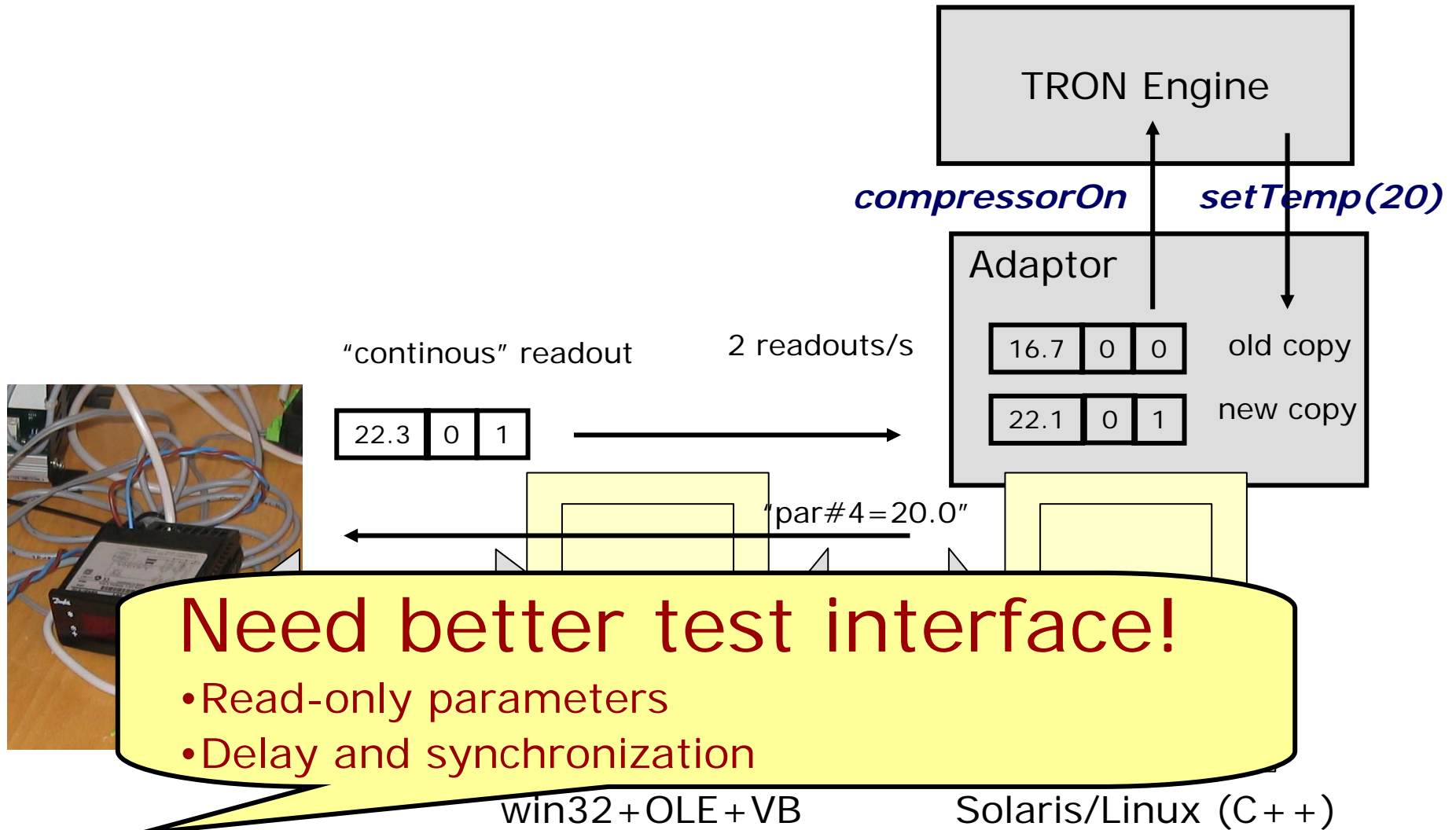


# EKC Adaptation 1

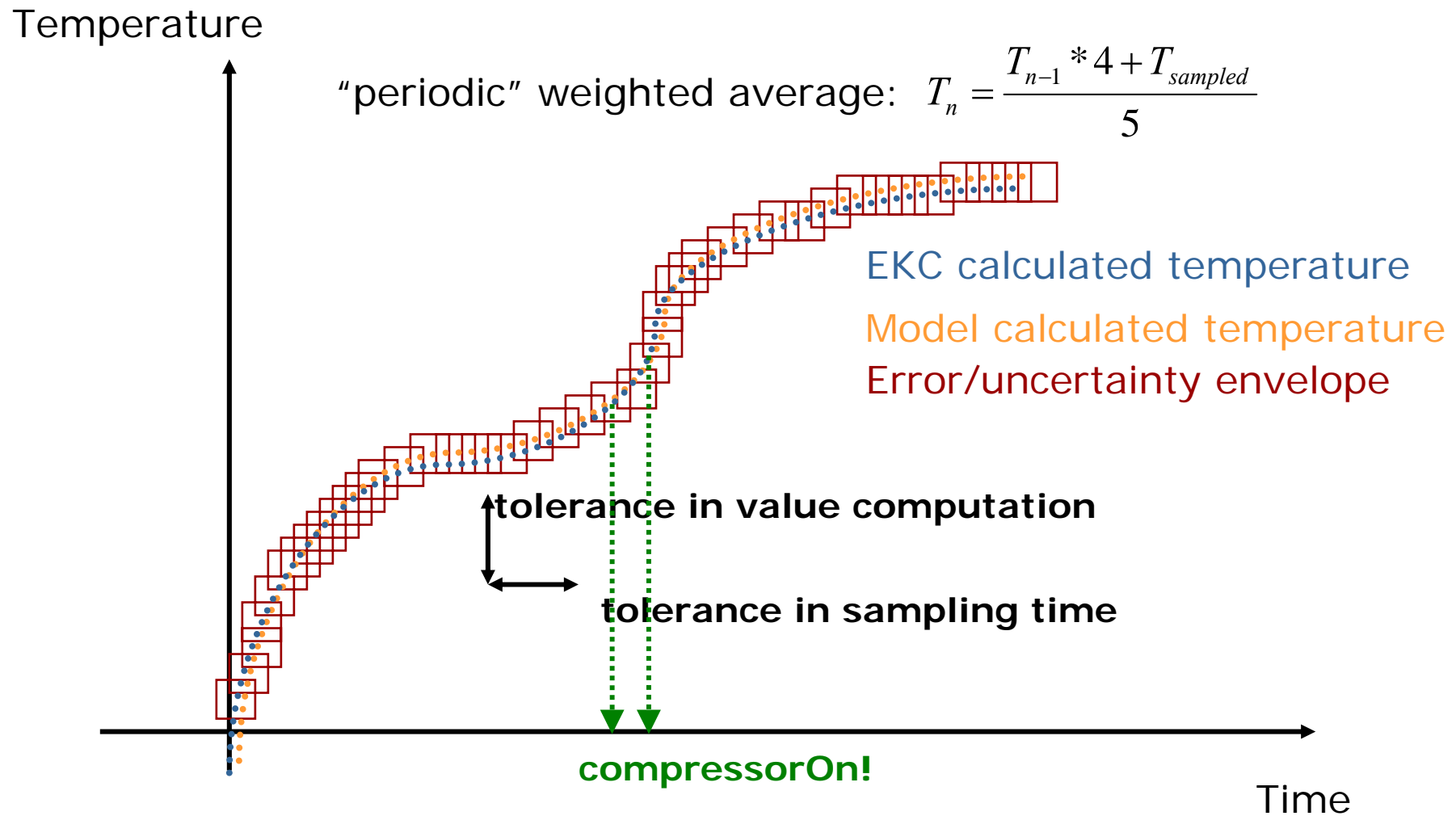
- Read and write parameter “database”
- 47 parameters



# EKC Adaptation 2

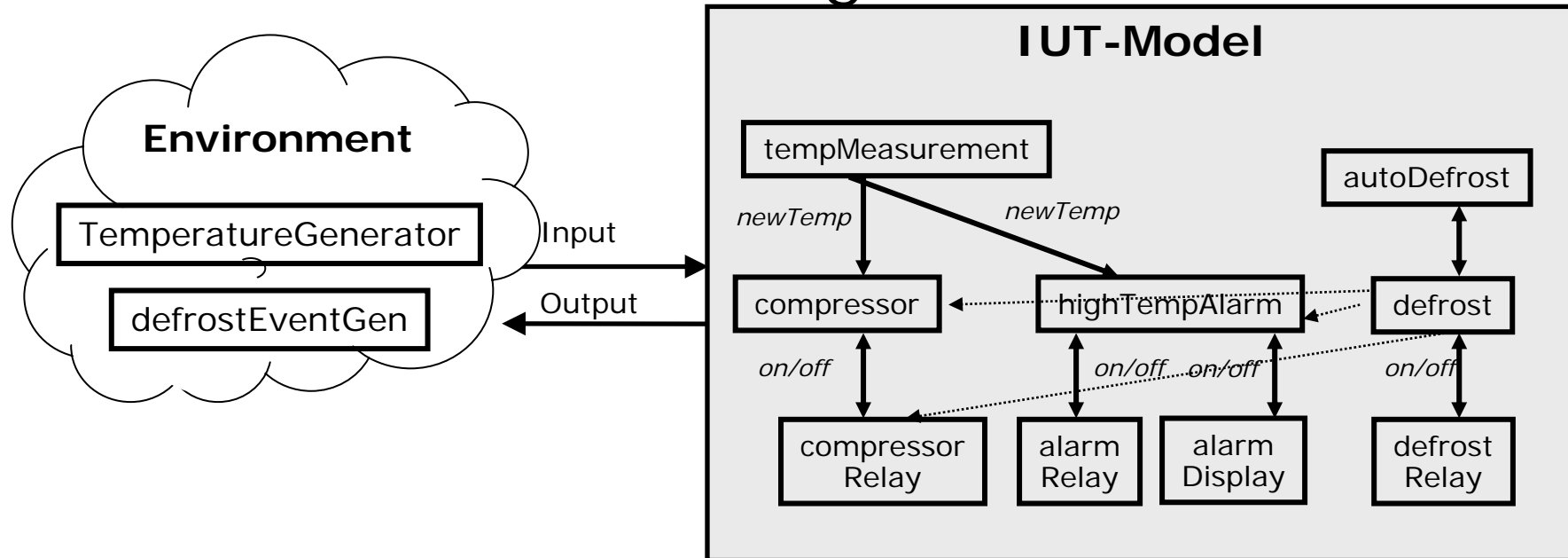


# Temperature Tracking



# Main Model Components

- 18 concurrent timed automata
- 14 clocks, 14 integers



# Reverse Engineering

- Unclear and incomplete specifications
- Method of Working
  1. Formulate hypothesis model
  2. Test
  3. **FAIL**-verdict  $\Rightarrow$  Refine model
  4. **(PASS)**  $\Rightarrow$  Confirm with Danfoss
- Detects differences between actual and modeled behavior
- *Indicates promising error-detection capability*
- 4 examples

# Ex1: Control Period

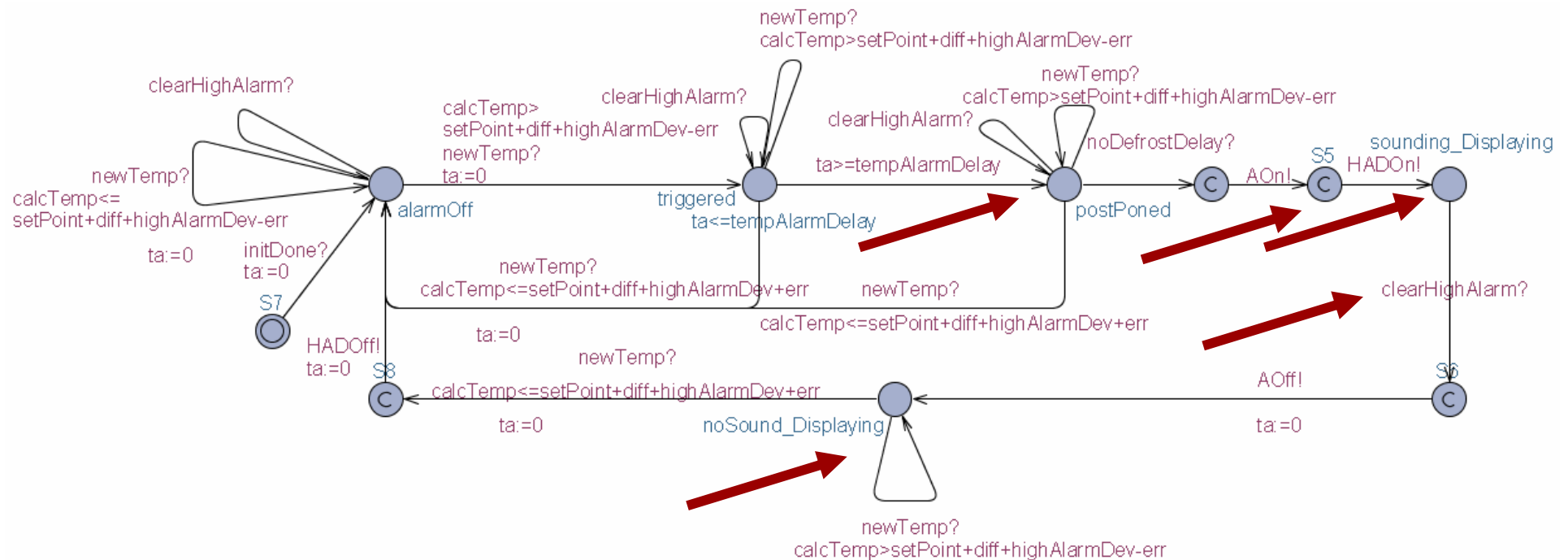
- Control actions issued when "calculatedTemp" crosses thresholds

"periodic" weighted average: 
$$T_n = \frac{T_{n-1} * 4 + T_{sampled}}{5}$$

- No requirements on period given
- Tested to be 1.2 seconds



# Ex2: High Alarm Monitor v2



- Add HighAlarmDisplay action
- Add location for “noSound, but alarmDisplaying”
- (Postpone alarms after defrosting)

# Ex3: Defrosting and Alarms

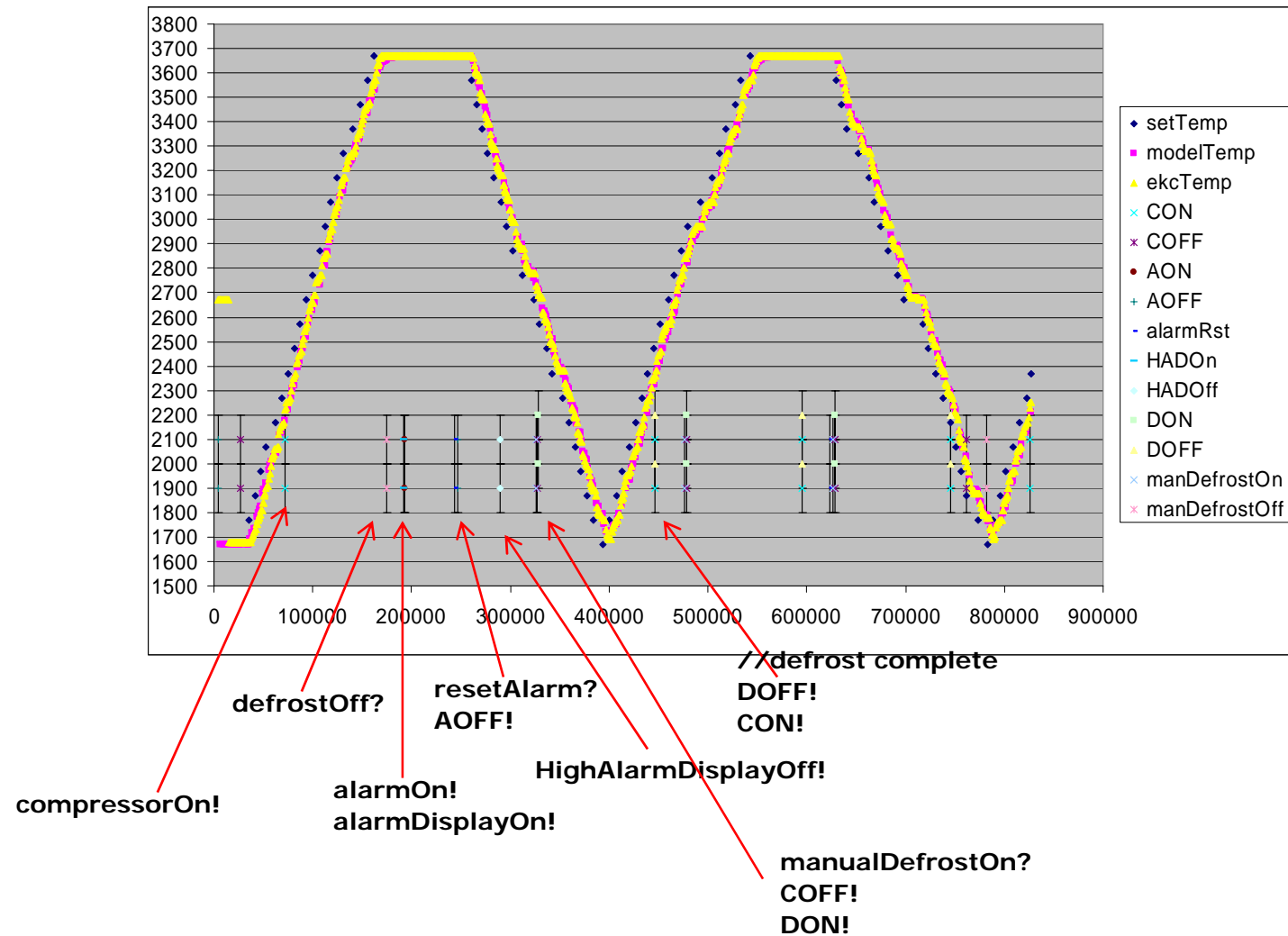
- When defrosting the temperature rises
- Postpone high temperature alarms during defrost
- System parameter `alarmDelayAfterDefrost`
- Several Interpretations
  1. Postpone `alarmDelayAfterDefrost + alarmDelay` after defrost?
  2. Postpone `alarmDelayAfterDefrost + alarmDelay` after `highTemp` detected?
  3. Postpone `alarmDelayAfterDefrost` until temperature becomes low; then use `alarmDelay`
- Option 3 applies!

# Ex4: Defrost TimeTolerance

- Defrost relays engaged earlier and disengaged later than expected
- Assumed 2 seconds tolerance
- Defrosting takes long time
- Implementation uses a low resolution timer (10 seconds)

# Example Test Run

(log visualization)



# END

---



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Basic Research  
in Computer Science



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