

UPPAAL

Verification Engine, Options & Patterns

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Outline

- UPPAAL
 - Modelling Language
 - Specification Language
- UPPAAL Verification Engine
 - Symbolic exploration algorithm
 - Zones & DBMs
- Verification Options
- Modelling Patterns

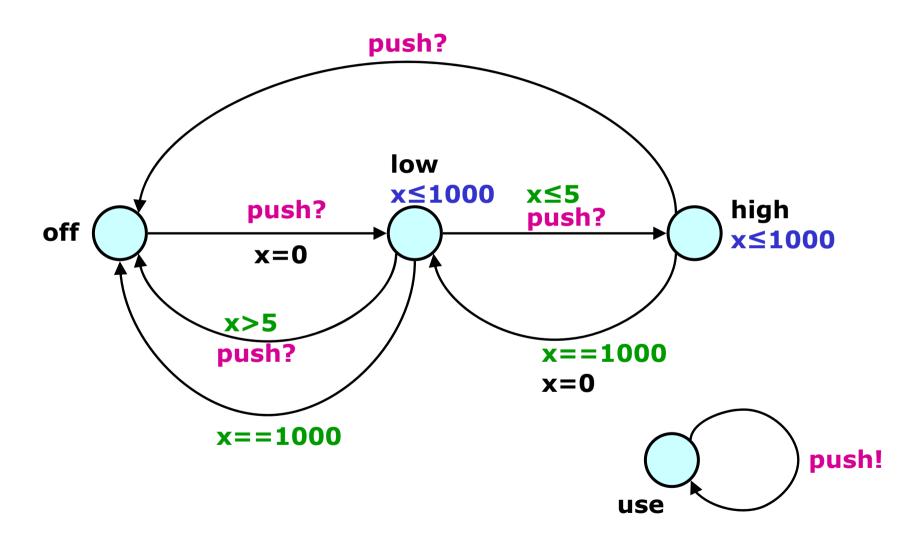
Goal: Be able to use the tool & understand what you are doing, not what the tool is doing.

Intuition only

Modelling Language

TA in a Nutshell





Modeling Language

- Network of TA = instances of templates
 - argument const type expression
 - argument type& name

Types

- built-in types: int, int[min,max], bool, arrays
- typedef struct { ... } name
- typedef built-in-type name +scalar sets

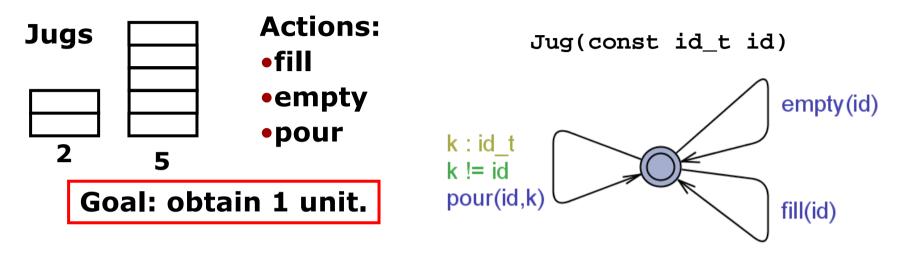
Functions

C-style syntax, no pointer but references OK.

Select

- name : type

Un-timed Example: Jugs



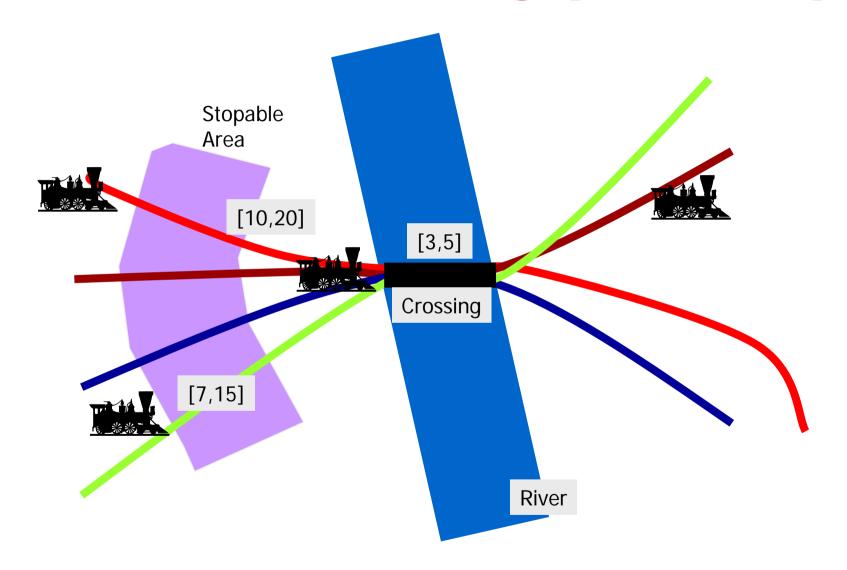
- Scalable, compact, & readable model.
 - const int N = 2; typedef int[0,N-1] id_t;
 - Jugs have their own id.
 - Actions = functions.
 - Pour: from id to another k different from id.

Jugs cont.

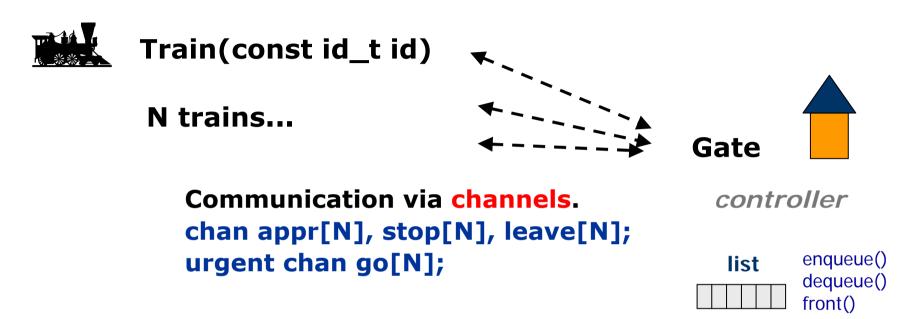
```
Jug levels & capacities:
   int level[N];
   const int capa[N] = \{2,5\};
void empty(id_t i) { level[i]=0; }
void fill(id_t i) { level[i] = capa[i]; }
  void pour(id_t i, id_t j)
        int max = capa[j] - level[j];
int poured = level[i] <? max;</pre>
        level[i] -= poured;
level[j] += poured;
```

Auto-instantiation: system Jug;

Train-Gate Crossing (Exercise)

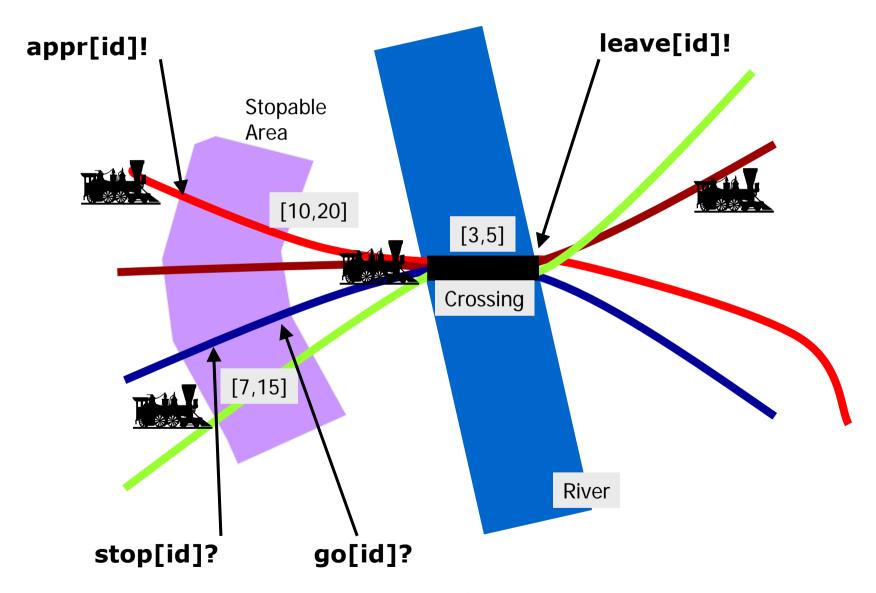


Train-Gate Modeling



- Scale the model:
 - const int N = 6; typedef int[0,N-1] id_t;
- Trains have their local clocks.
- The gate has its local list & functions.

Train-Gate Crossing



Scalar Sets

- Use: typedef scalar[N] setA;
 - defines a set of N scalars,
 - typedef scalar[N] setB;
 defines another set of N scalars,
 - it is very important to use the typedef.
 - chan a[setA]; is an array of channels ranging over a scalar set - similarly for other types.
 - limited operations to keep scalars symmetric.
- A way to specify symmetries in the model.
 - UPPAAL uses symmetry reduction automatically.
 - Reduction: Project the current state to a representative of its equivalence class (w.r.t. symmetry).

Specification Language

Validation Properties

- Possibly: E<> P

Safety Properties

- Invariant: A[] P

- Pos. Inv.: E[] *P*

Liveness Properties

Eventually: A<> P

- Leadsto: $P \rightarrow Q$

Bounded Liveness

- Leads to within: $P \rightarrow_{<t} Q$

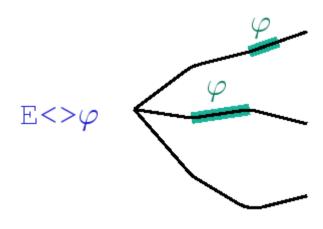
The expressions *P* and *Q* must be type safe, **side effect free**, and evaluate to a boolean.

Only references to integer variables, constants, clocks, and locations are allowed (and arrays of these).

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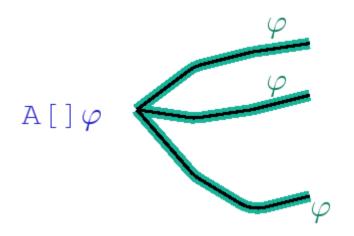


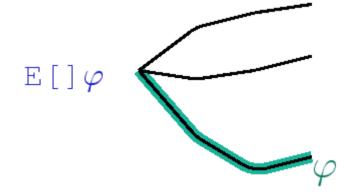
- Validation Properties
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- Validation Properties
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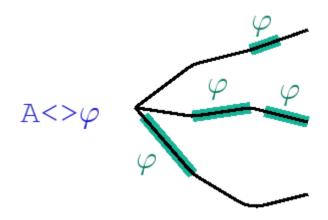


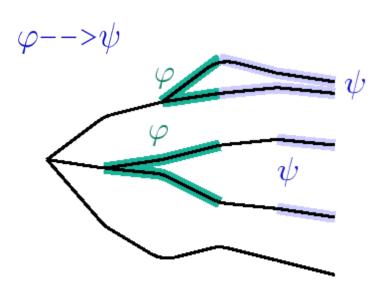


- Validation Properties
 - Possibly: E <> P
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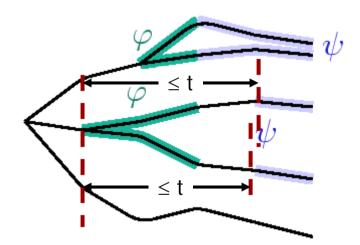


- Eventually: A<> P
- Leadsto: $P \rightarrow Q$
- Bounded Liveness
 - Leads to within: $P \rightarrow_{\leq t} Q$





- Validation Properties
 - Possibly: E <> P
- Safety Properties
 - Invariant: A[] P
 - Pos. Inv.: E[] *P*
- Liveness Properties
 - Eventually: A <> P
 - Leadsto: $P \rightarrow Q$
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 - Leads to within: $P \rightarrow_{<t} Q$



Jug Example

- Safety: Never overflow.
 - A[] forall(i:id_t) level[i] <= capa[i]</pre>
- Validation/Reachability: How to get 1 unit.
 - E<> exists(i:id_t) level[i] == 1

Train-Gate Crossing

- Safety: One train crossing.
 - A[] forall (i : id_t) forall (j : id_t)
 Train(i).Cross && Train(j).Cross imply i == j
- Liveness: Approaching trains eventually cross.
 - Train(0).Appr --> Train(0).Cross
 - Train(1).Appr --> Train(1).Cross
 - ...
- No deadlock.
 - A[] not deadlock

UPPAAL Verification Engine

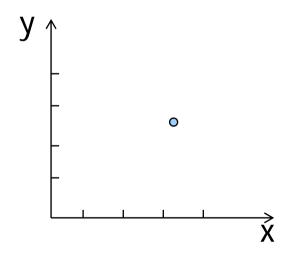
Overview – Intuition Only

- Zones and DBMs
- Reachability algorithm revisited
- Minimal Constraint Form

Zones

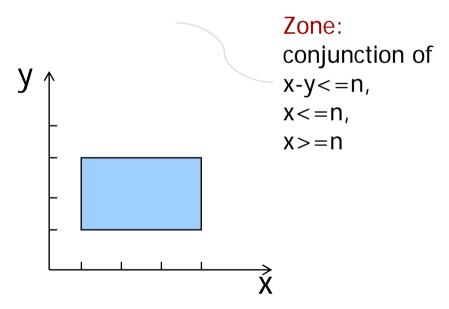
From infinite to finite



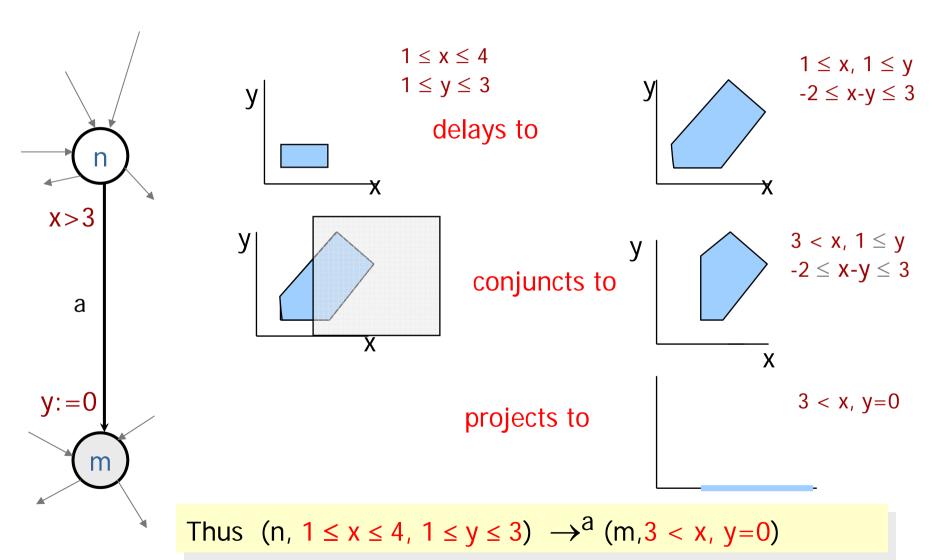


Symbolic state (set)

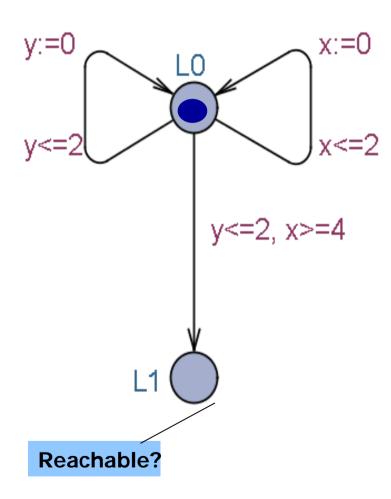
(n,
$$1 \le x \le 4$$
, $1 \le y \le 3$)

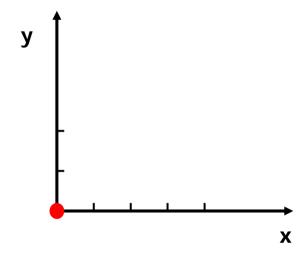


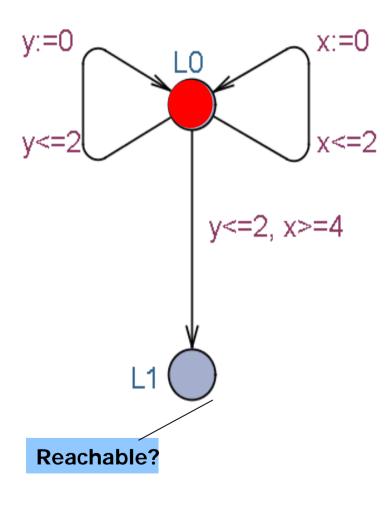
Symbolic Transitions

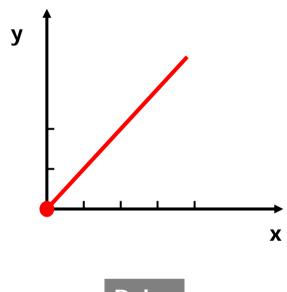


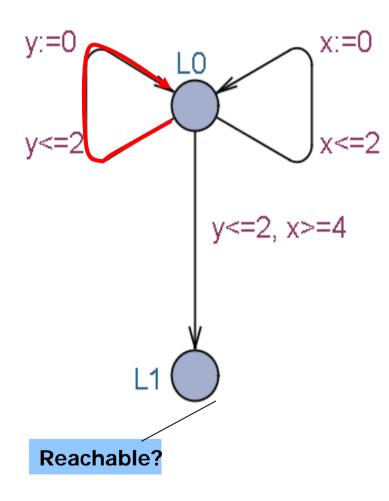
28-09-2010 TSW 23

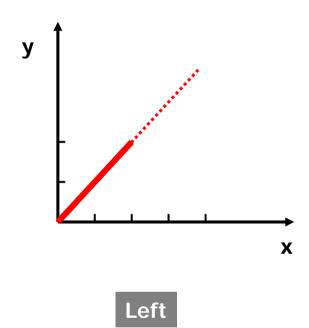


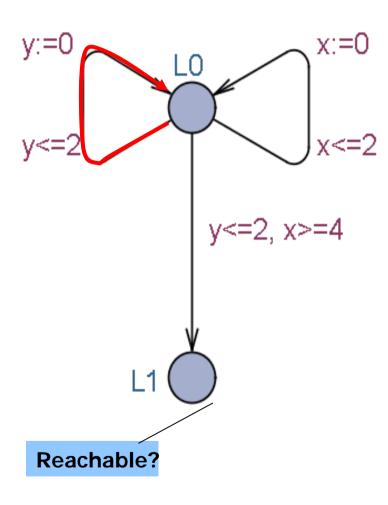


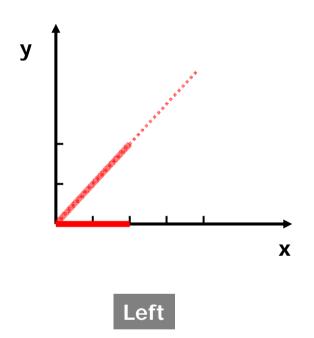


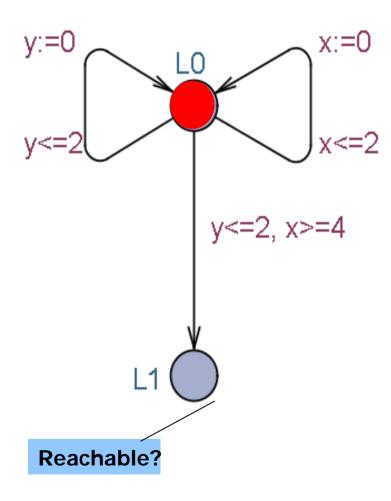


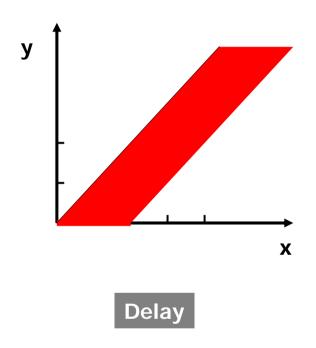


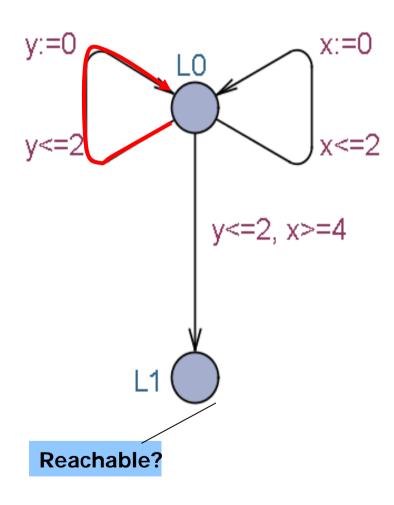


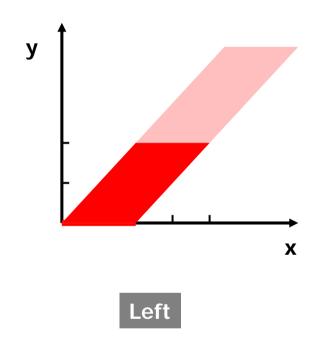


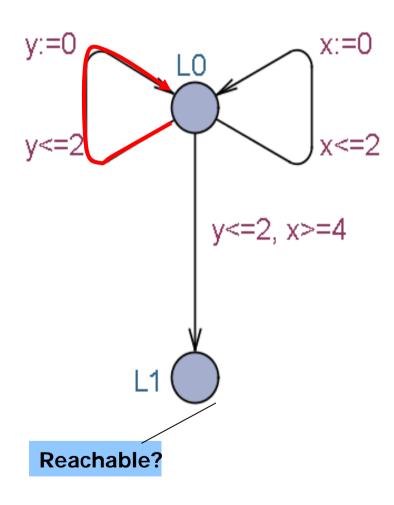


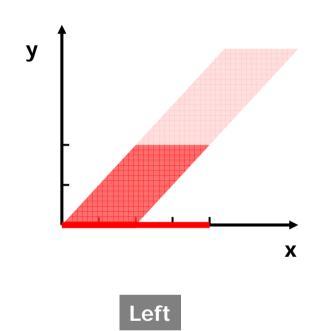


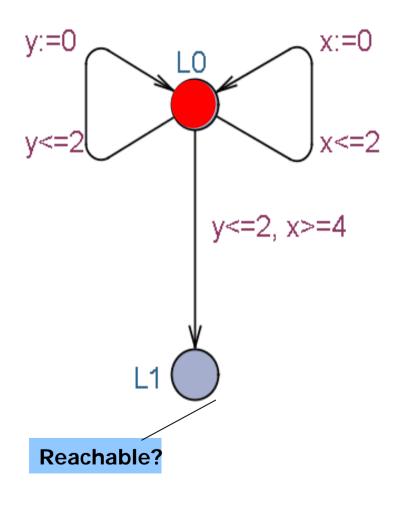


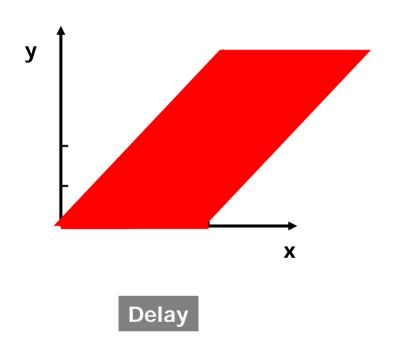


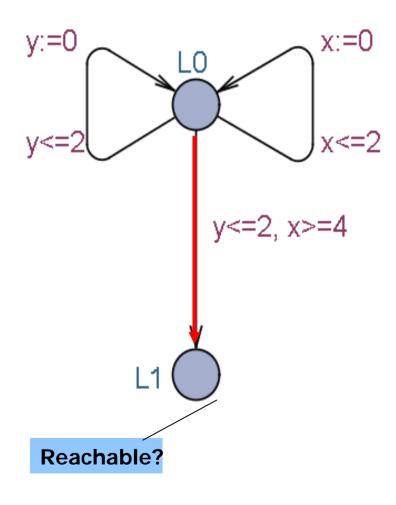


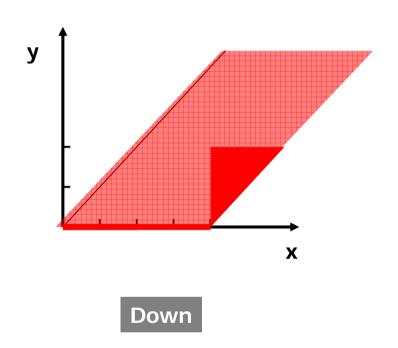






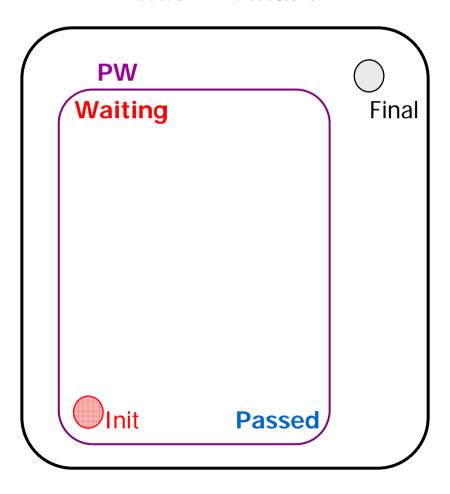






The simulator shows you symbolic states!

Init -> Final ?

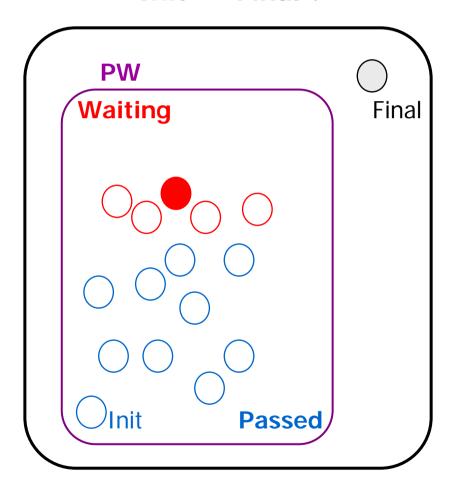


INITIAL Passed := \emptyset ; Waiting := $\{(n_0, Z_0)\}$

REPEAT

UNTIL Waiting = \emptyset return false

Init -> Final ?

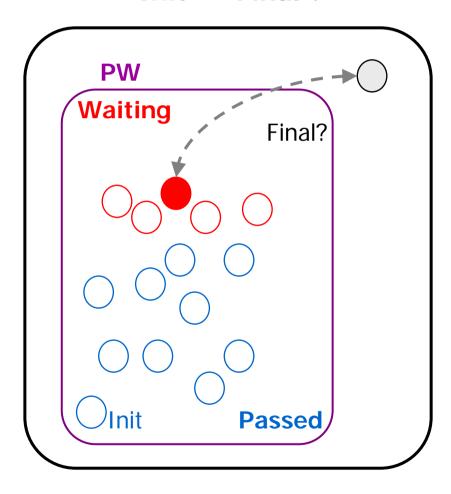


```
INITIAL Passed := \emptyset;
Waiting := \{(n_0, Z_0)\}
```

REPEAT
pick (n,Z) in Waiting

UNTIL Waiting = \emptyset return false

Init -> Final ?

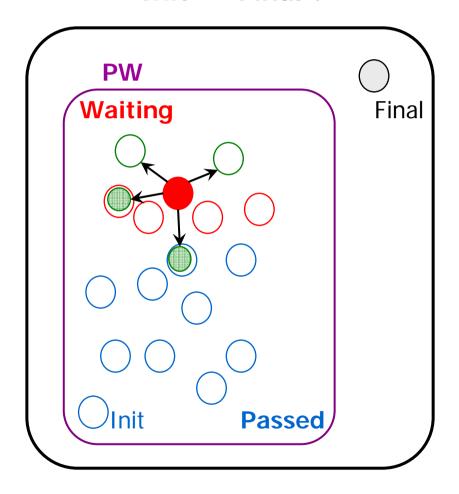


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```
REPEAT
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if (n,Z) = Final return true
```

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Init -> Final?



```
INITIAL Passed := \emptyset;
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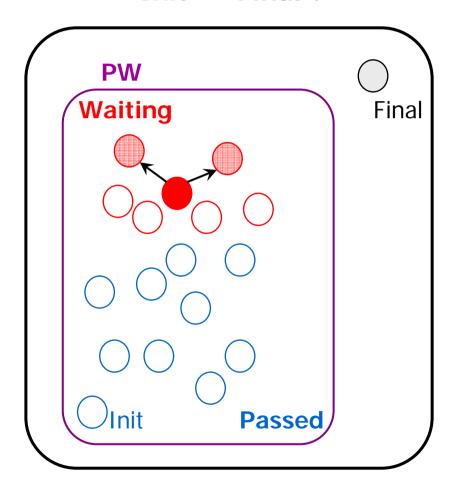
REPEAT

```
pick (n,Z) in Waiting
if (n,Z) = Final return true
for all (n,Z) \rightarrow (n',Z'):
if for some (n',Z'') Z' \subseteq Z'' continue
```

```
UNTIL Waiting = \emptyset return false
```

Forward Reachability Algorithm

Init -> Final ?



```
INITIAL Passed := \emptyset;
Waiting := \{(n_0, Z_0)\}
```

REPEAT

```
pick (n,Z) in Waiting

if (n,Z) = Final return true

for all (n,Z) \rightarrow (n',Z'):

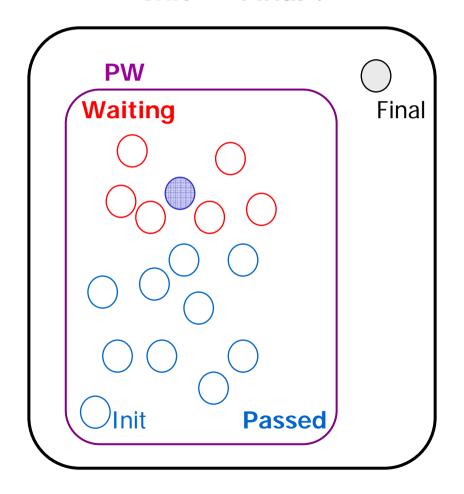
if for some (n',Z'') Z' \subseteq Z'' continue

else add (n',Z') to Waiting
```

```
UNTIL Waiting = \emptyset return false
```

Forward Reachability Algorithm

Init -> Final ?



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INITIAL Passed := \emptyset;

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REPEAT

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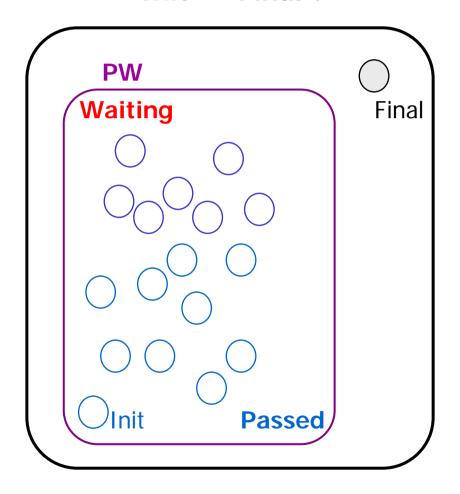
move (n,Z) to Passed

UNTIL Waiting = \emptyset

return false
```

Forward Reachability Algorithm

Init -> Final ?



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INITIAL Passed := \emptyset;

Waiting := \{(n_0, Z_0)\}

REPEAT

pick (n,Z) in Waiting

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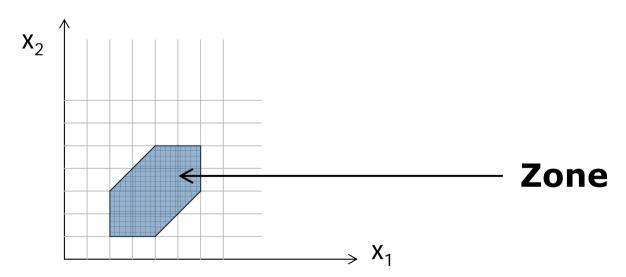
move (n,Z) to Passed

UNTIL Waiting = \emptyset
```

Difference Bound Matrices

$x_0 - x_0 < = 0$	$x_0 - x_1 < = -2$	$x_0 - x_2 < = -1$
$x_1 - x_0 < = 6$	$x_1 - x_1 < = 0$	$x_1 - x_2 < = 3$
$x_2 - x_0 < = 5$	$ x_2-x_1 < =1$	$x_2 - x_2 < = 0$

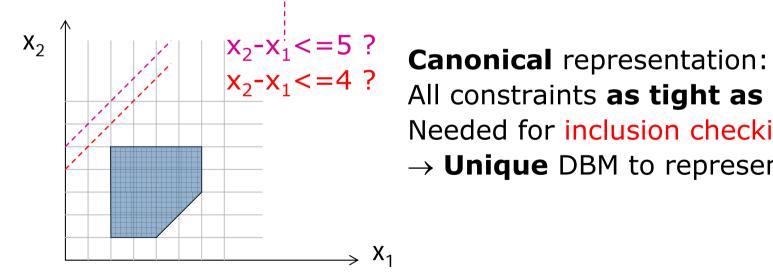
$$x_i - x_j < = c_{ij}$$



Difference Bound Matrices

$x_0 - x_0 < = 0$	$x_0 - x_1 < = -2$	$x_0 - x_2 < = -1$
$x_1 - x_0 < = 6$	$x_1 - x_1 < = 0$	$x_1 - x_2 < = 3$
$x_2 - x_0 < = 5$	x ₂ -x ₁ <= 3	$x_2 - x_2 < = 0$

$$x_i - x_j < = c_{ij}$$



All constraints as tight as possible.

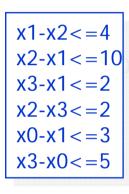
Needed for inclusion checking.

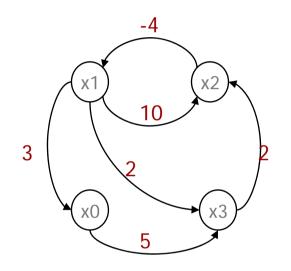
→ **Unique** DBM to represent a zone.

Canonical Datastructures for Zones

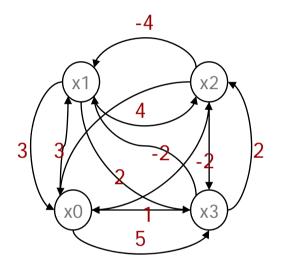
Minimal Constraint Form

RTSS 1997





Path Closure O(n^3)



Shortest Path Reduction O(n^3)



3 x2 x2 x3

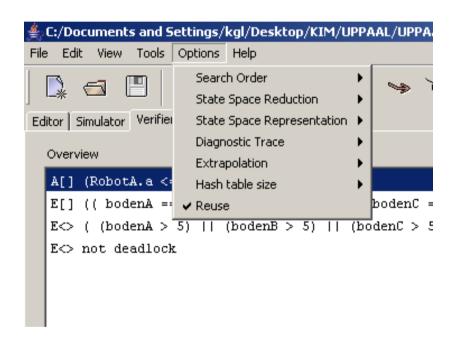
Space worst O(n^2) practice O(n)

Large gain in space. Small price in time.

Verification option "CDS".

Verification Options

Verification Options



Search Order

Depth First

Breadth First

State Space Reduction

None

Conservative

Aggressive

State Space Representation

DBM

Compact Form

Under Approximation
Over Approximation

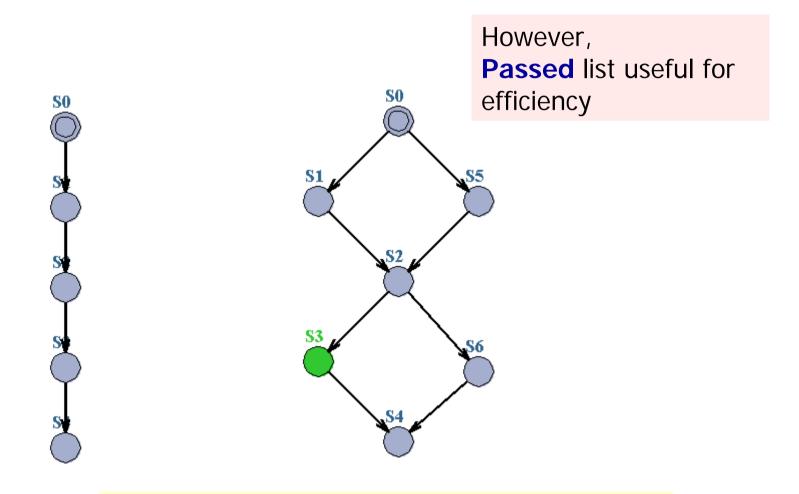
Diagnostic Trace

Some

Shortest

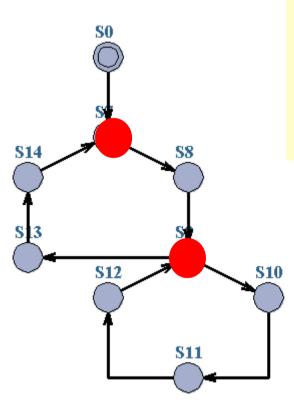
Fastest

State Space Reduction



No Cycles: Passed list not needed for termination

State Space Reduction

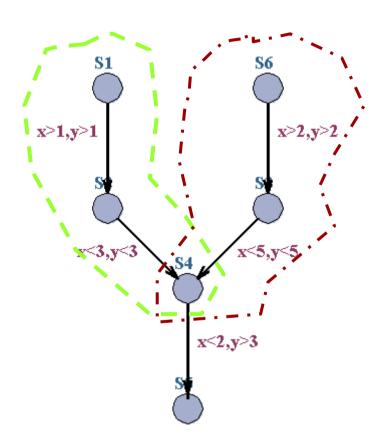


Cycles:

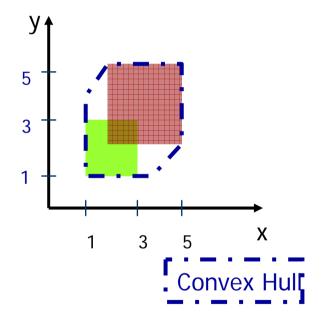
Only symbolic states involving loop-entry points need to be saved on **Passed** list

Over-approximation

Convex Hull

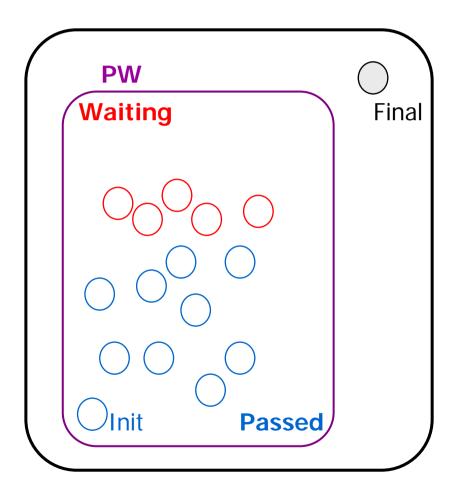


TACAS04: An **EXACT** method performing as well as Convex Hull has been developed based on abstractions taking max constants into account.



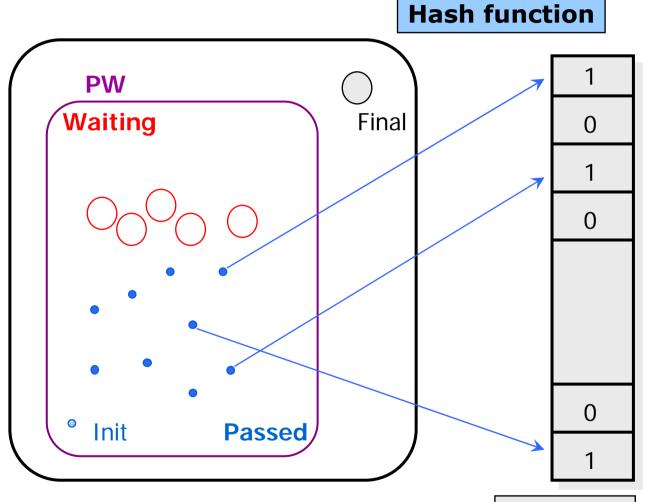
Under-approximation

Bitstate Hashing



Under-approximation

Bitstate Hashing



1 bit per passed state

Under-approx.
Several states
may collide on
the same bit.

Inclusion check only with waiting states. "Equality" with passed.

Bit Array

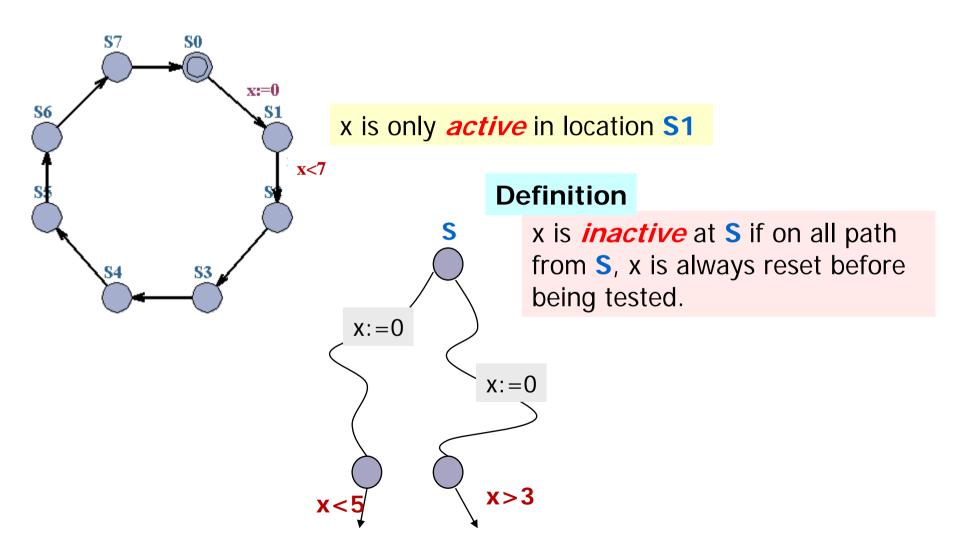
Modelling Patterns

Variable Reduction

- Reduce size of state space by explicitly resetting variables when they are not used!
- Automatically performed for clock variables (active clock reduction)

```
// Remove the front element of the queue
void dequeue()
{
    int i = 0;
    len -= 1;
    while (i < len)
    {
        list[i] = list[i + 1];
        i++;
        list[i] = 0;
}</pre>
```

Clock Reduction (Automatic)



Synchronous Value Passing

	Unconditional	Conditional	
One-way	c! c? in := var, var := 0	c! c? in := var, var := 0 cond(in)	
Asymmetric two-way	c! var := out c? in := var d? in := var, var := 0 d! var := out	c! var := out c? in := var, var := out d? in := var, var := 0 d! d!	

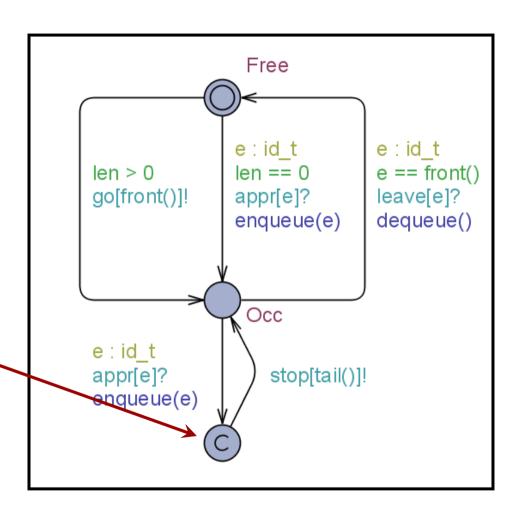
68

Atomicity

Loops & complex control structures: C-functions.

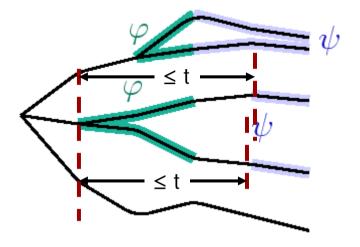
 To allow encoding of multicasting.

Committed locations.



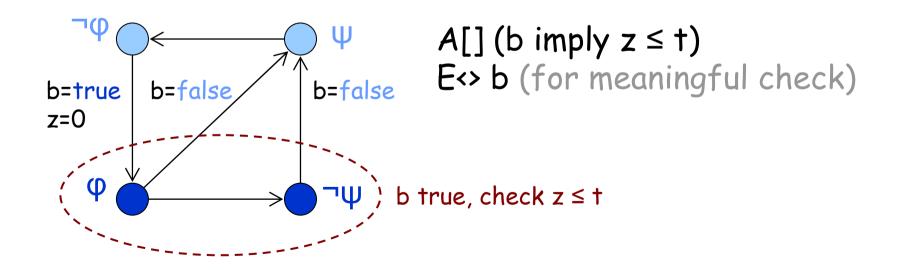
Bounded Liveness

- Leads to within: $\phi \rightarrow \langle t \psi$
 - More efficient than leadsto:
 φ leadsto_{≤t} ψ reduced to
 A□(b⇒z ≤ t) with
 - bool b set to true and clock
 z reset when φ holds.
 - When ψ holds set b to false.



Bounded Liveness

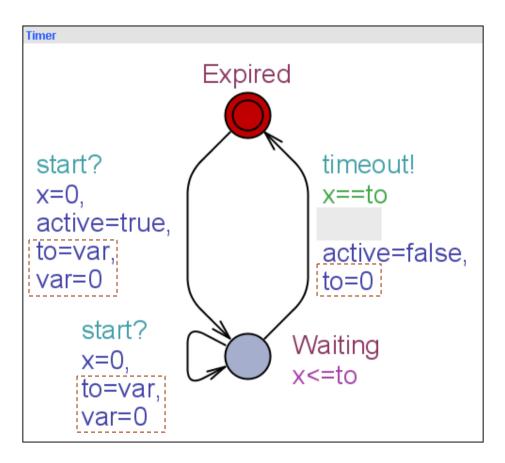
The truth value of b indicates whether or not ψ should hold in the future.



Timers

Parametric timer:

- (re-)start(value)
 start! var=value
- expired?
 active (bool)
 active go?
 (bool+urgent chan)
- time-out event timeout?

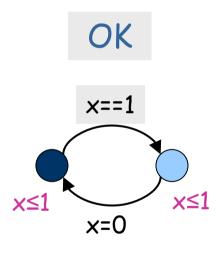


Declare 'to' with a tight range.

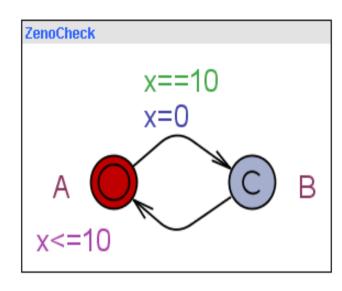
Zenoness

- Problem: UPPAAL does not check for zenoness directly.
 - A model has "zeno" behavior if it can take an infinite amount of actions in finite time.
 - That is usually not a desirable behavior in practice.
 - Zeno models may wrongly conclude that some properties hold though they logically should not.
 - Rarely taken into account.
- Solution: Add an observer automata and check for non-zenoness, i.e., that time will always pass.

Zenoness



Detect by •adding the observer:



Constant (10) can be anything (>0), but choose it well w.r.t. your model for efficiency. Clocks 'x' are local.

and check the property

ZenoCheck.A --> ZenoCheck.B